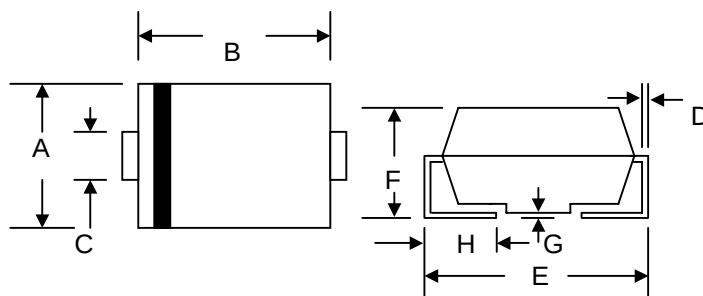


Data Sheet 2630, Rev.—

Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop, High Efficiency
- Surge Overload Rating to 50A Peak
- Low Power Loss
- Super-Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-O



Mechanical Data

- Case: Low Profile Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.093 grams (approx.)

SMB/DO-214AA				
Dim	Min	Max	Min	Max
A	3.30	3.94	0.130	0.155
B	4.06	4.70	0.160	0.185
C	1.91	2.11	0.075	0.083
D	0.152	0.305	0.006	0.012
E	5.08	5.59	0.2	0.220
F	2.13	2.44	0.084	0.096
G	0.051	0.203	0.002	0.008
H	0.76	1.27	0.029	0.05
in mm			in inch	

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Characteristic	Symbol	ES2A	ES2B	ES2C	ES2D	ES2E	ES2G	ES2J	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RRWM} V _R	50	100	150	200	300	400	600	V
RMS Reverse Voltage	V _{R(RMS)}	35	70	105	140	210	280	420	V
Average Rectified Output Current @T _L = 110°C	I _O	2.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50							A
Forward Voltage @I _F = 2.0A	V _{FM}	0.95				1.25	1.7		V
Peak Reverse Current @T _A = 25°C At Rated DC Blocking Voltage @T _A = 100°C	I _{RM}	5.0 500							μA
Reverse Recovery Time (Note 1)	t _{rr}	35							nS
Typical Junction Capacitance (Note 2)	C _j	25							pF
Typical Thermal Resistance (Note 3)	R _{θJL}	20							K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150							°C

Note: 1. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A,
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.
 3. Mounted on P.C. Board with 8.0mm² land area.

Data Sheet 2630,Rev.—

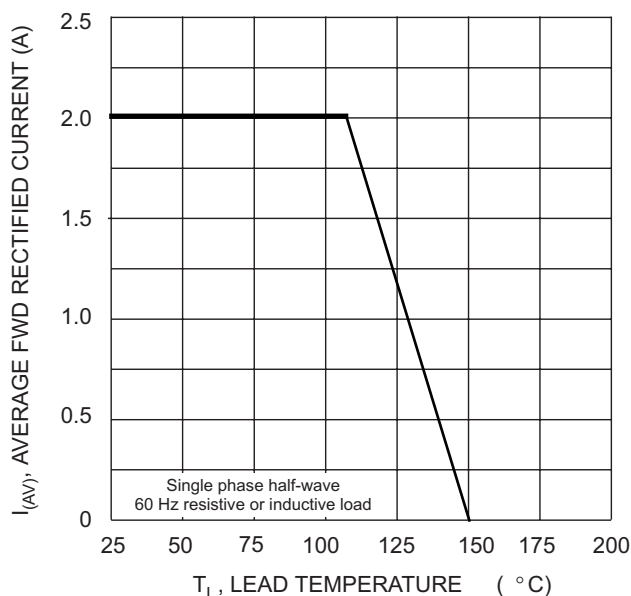


Fig. 1 Forward Current Derating Curve

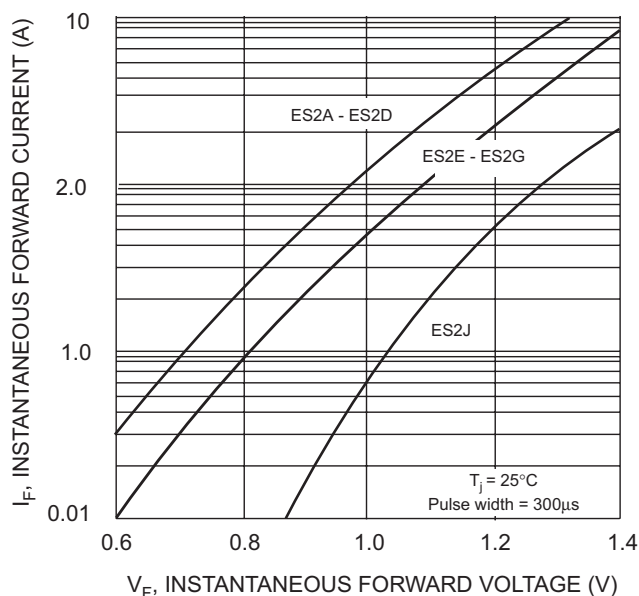


Fig. 2 Typical Forward Characteristics

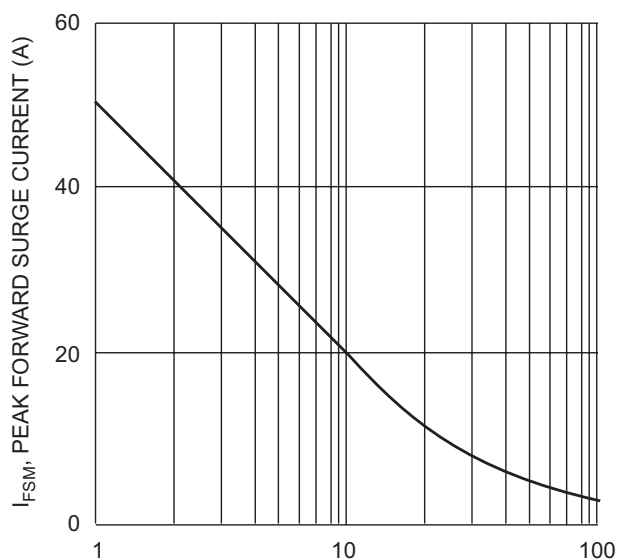


Fig. 3 Peak Forward Surge Current

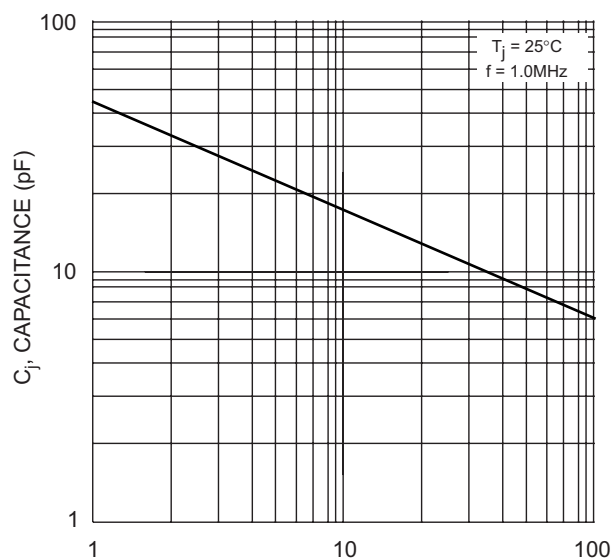
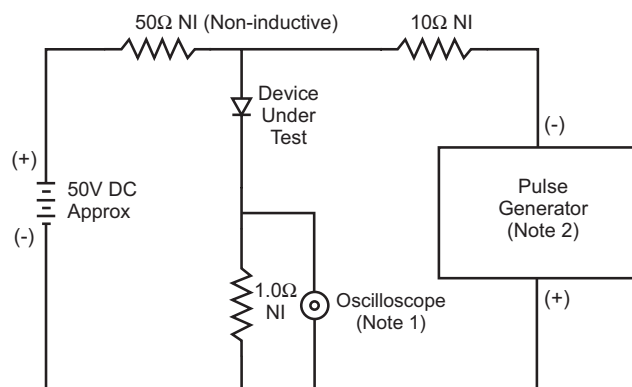


Fig. 4 Typical Junction Capacitance



- Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0M Ω , 22pF.
2. Rise Time = 10ns max. Input Impedance = 50 Ω .

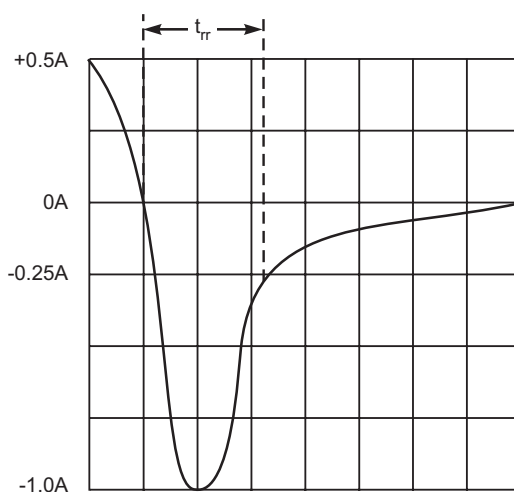


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

Data Sheet 2630,Rev.—

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