

# DC025 Triple Output-Series Power Modules: 18 Vdc to 36 Vdc Input; 25 W



The DC025 Triple Output-Series Power Modules use advanced, surface-mount technology and deliver high-quality, compact, dc-dc conversion at an economical price.

#### **Features**

- Small size: 71.1 mm x 61.0 mm x 12.7 mm (2.80 in. x 2.40 in. x 0.50 in.)
- Low output noise
- Industry-standard pinout
- Metal case with separate case ground pin
- 2:1 input voltage range
- Remote on/off (positive logic)
- UL\* Recognized, CSA<sup>†</sup> Certified, and VDE Licensed
- Within FCC and CISPR Class A Radiated Limits

# **Applications**

- Distributed power architectures
- Telecommunications

# **Options**

- Higher accuracy output voltage clamp set point
- Short pins: 2.79 mm ± 0.25 mm (0.110 in. ± 0.010 in.)
- Heat sink available for extended operation
- Negative logic remote on/off

## Description

The DC025 Triple Output-Series Power Modules are dc-dc converters that operate over an input voltage range of 18 Vdc to 36 Vdc and provide three outputs. These modules offer extremely low noise levels with industry-standard pinouts in a small footprint. Each highly reliable and efficient unit features remote on/off and current limit.

The maximum total output power of the DC025 Triple Output-Series Power Modules is limited to 25 W. The main output (Vo1) is designed to deliver the entire 25 W. The auxiliary outputs (Vo2 and Vo3) can provide a total of 22.5 W as long as the total output power does not exceed 25 W.

Efficiency greater than 80%, a wide operating temperature range, and a metal case are additional features of these modules.

<sup>\*</sup> UL is a registered trademark of Underwriters Laboratories, Inc.

<sup>†</sup> CSA is a registered trademark of Canadian Standards Association

## **Absolute Maximum Ratings**

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage Continuous	Vı	_	50	V
I/O Isolation Voltage:				
dc	_	_	500	V
Transient (1 minute)		_	850	V
Operating Case Temperature	Tc	-40	100	°C
Storage Temperature	Tstg	-55	125	°C

## **Electrical Specifications**

Unless otherwise indicated, specifications apply to all modules over all operating input voltage, resistive load, and temperature conditions.

**Table 1. Input Specifications** 

Parameter	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	Vı	18	28	36	Vdc
Maximum Input Current (VI = 0 V to 36 V; Io = Io, max; see Figure 1.)	II, max	_	_	3.0	А
Inrush Transient	i <sup>2</sup> t	_	_	0.2	A <sup>2</sup> s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 µH source impedance; Tc = 25 °C; see Figure 18 and Design Considerations section.)	_	_	30	_	mAp-p
Input Ripple Rejection (120 Hz)	_	_	60	_	dB

#### **Fusing Considerations**

#### CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow, dc fuse with a maximum rating of 5 A in series with the ungrounded input lead. Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

# **Electrical Specifications** (continued)

**Table 2. Output Specifications** 

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Voltage (Over all operating input voltage, resistive	DC025ABK-M	V01 V02	4.80 10.80	_ _	5.20 13.70	Vdc Vdc
load, and temperature conditions until end		Vo <sub>3</sub>	-10.80	_	-13.70	Vdc
of life. See Figure 20.)	DC025ACL-M	V01	4.80	_	5.20	Vdc
		Vo <sub>2</sub>	13.77	_	17.20	Vdc
		Voз	-13.77	_	-17.20	Vdc
Output Voltage Set Point	DC025ABK-M	VO1, set	4.90	5.00	5.10	Vdc
$(V_1 = 28 \text{ V}; \text{Tc} = 25 \text{ °C}; \text{Io1} = 2.0 \text{ A},$		VO2, set	11.83	12.20	12.57	Vdc
Io2 = Io3 = 0.5 A)		Vo3, set	-11.83	-12.20	-12.57	Vdc
	DC025ACL-M	VO1, set	4.90	5.00	5.10	Vdc
		VO2, set	14.84	15.30	15.76	Vdc
		VO3, set	-14.84	-15.30	-15.76	Vdc
Output Regulation:						
Line (V <sub>I</sub> = 18 V to 36 V)	All			0.1	0.2	%
Load (See Figures 5—8.)	All	Vo <sub>1</sub>	_	0.1	0.2	%
(IO1 = IO, min to IO, max, IO2 = IO3 = IO, min)		.,		0.5	4 -	0.4
Temperature (See Figures 2—4.)	All	Vo <sub>1</sub>	_	0.5	1.5	%
$(Tc = -40  ^{\circ}C \text{ to } +100  ^{\circ}C)$						
Output Ripple and Noise						
(See Figure 19.):						
RMS	All	Vo1	_		25	mVrms
Deal (const (511 (constal))	A 11	Vo2, Vo3	_	_	30	mVrms
Peak-to-peak (5 Hz to 20 MHz)	All	V01	_	_	100	mVp-p
		V02, V03	_	_	150	mVp-p
Output Current	DC025ABK-M	lo <sub>1</sub>	0.5	_	5.0	A
(At lo < lo, min, the modules may exceed	D0005401 14	lo2, lo3	0.1		1.0	A
output ripple specifications.)	DC025ACL-M	lo <sub>1</sub>	0.5	_	5.0	A
		lo2, lo3	0.1	_	0.83	Α
Output Current-limit Inception	DC025ABK-M	lo <sub>1</sub>	_	6	7.5	Α
(Vo = 90% of Vo, nom and minimum load on		lo2, lo3	_	2	3.0	A
other outputs. See Figures 9—12.)	DC025ACL-M	lo <sub>1</sub>	_	6	7.5	A
		lo2, lo3	_	2	3.0	Α
Output Short-circuit Current	DC025ABK-M	<b>l</b> 01	_	8	10.5	Α
(Vo = 1 V and minimum load on other		lo2, lo3	_	3	4.5	Α
outputs.)	DC025ACL-M	lo <sub>1</sub>	-	8	10.5	A
		lo2, lo3	_	3	4.5	Α
Efficiency						
$(V_1 = 28 \text{ V}; T_C = 25 \text{ °C}; \text{ see Figures 13, 14,}$						
and 20.):						
lo1 = 2.5 A, lo2 = lo3 = 0.5 A	DC025ABK-M	η	79	82	-	%
Io1 = 2.0 A, Io2 = Io3 = 0.5 A	DC025ACL-M	η	79	82	_	%

# **Electrical Specifications** (continued)

Table 2. Output Specifications (continued)

Parameter	Device	Symbol	Min	Тур	Max	Unit
Dynamic Response						
(ýlo/ýt = 1 A/10 μs, V <sub>I</sub> = 28 V, Tc = 25 °C):						
Load Change from Io = 50% to 75% of						
IO, max:						
Peak Deviation	All	Vo <sub>1</sub>	_	80	_	mV
Settling Time (Vo < 10% peak	All	_	_	1	_	ms
deviation)						
Load Change from Io = 50% to 25% of						
IO, max:						
Peak Deviation	All	Vo <sub>1</sub>	_	80	_	mV
Settling Time (Vo < 10% peak	All	_	_	0.5	_	ms
deviation)						

### **Table 3. Isolation Specifications**

Parameter	Min	Тур	Max	Unit
Isolation Capacitance	_	0.02	_	μF
Isolation Resistance	10	_	_	MΩ

# **General Specifications**

Parameter	Min	Тур	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C)		2,906,000		hours
Weight	_	_	113 (4.0)	g (oz.)

# **Feature Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions and Design Considerations for further information.

Parameter	Device	Symbol	Min	Тур	Max	Unit
Remote On/Off		-				
(V <sub>I</sub> = 0 V to 36 V; open collector or equivalent						
compatible; signal referenced to V <sub>I</sub> (–) terminal.						
See Figures 17 and 21 and Feature						
Descriptions.):						
DC025XXX-M (positive logic):						
Logic Low—Module Off						
Logic High—Module On						
DC025XXX1-M (negative logic):						
Logic Low—Module On						
Logic High—Module Off						
Module Specifications:		_				_
On/Off Current—Logic Low	All	lon/off	_	_	1.0	mA
On/Off Voltage:						.,
Logic Low	All	Von/off	0	_	1.2	V
Logic High (lon/off = 0)	All	Von/off	_	_	10	V
Open Collector Switch Specifications:	A II	1				
Leakage Current During Logic High	All	lon/off	_	_	50	μA
(Von/off = 10 V) Output Low Voltage During Logic Low	All	Von/off			1.2	V
(lon/off = 1 mA)	All	Von/off		_	1.2	V
Turn-on Time	All			5		ms
(Io = 80% of Io, max; Vo within ±1% of steady	All	_		3	_	1115
state)						
Output Voltage Overshoot (See Figure 17.)	All			0	5	%
Output Overvoltage Clamp	DC025ABK-M	Vo <sub>1</sub>	_	6	6.8	V
Carpar o rorromago o arrip		Vo <sub>2</sub>	_	15	17	v
		Voз	_	-15	-17	V
	DC025ACL-M	Vo <sub>1</sub>	_	6	6.8	V
		Vo <sub>2</sub>	_	19	21	V
		Voз	_	-19	-21	V
Output Voltage Set-point Adjustment Range	All	_	90	_	110	% Vo, nom

#### **Characteristic Curves**

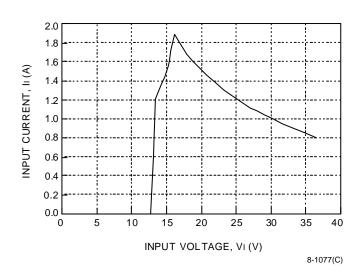


Figure 1. DC025 Triple Output-Series Typical Input Characteristics

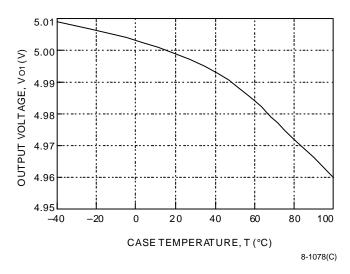


Figure 2. DC025 Triple Output-Series Typical
Output Voltage Variation of 5 V Output
Over Ambient Temperature Range

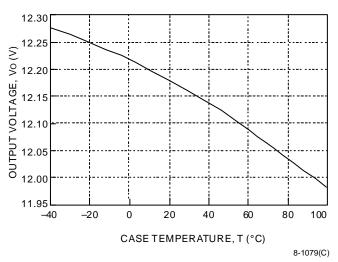


Figure 3. DC025 Triple Output-Series Typical
Output Voltage Variation of 12 V Output
Over Ambient Temperature Range

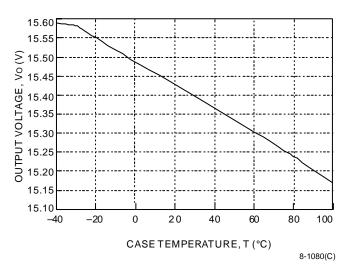
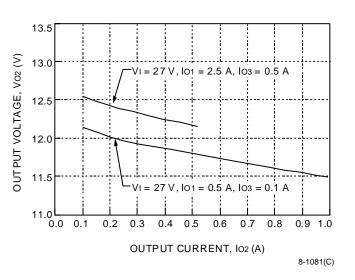


Figure 4. DC025 Triple Output-Series Typical
Output Voltage Variation of 15 V Output
Over Ambient Temperature Range

# Characteristic Curves (continued)



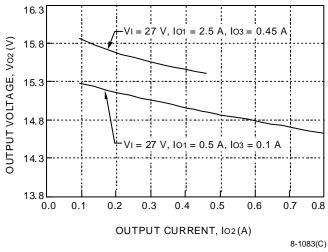
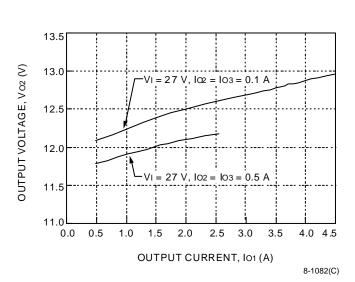


Figure 7. DC025ACL-M Typical Load Regulation

Figure 5. DC025ABK-M Typical Load Regulation



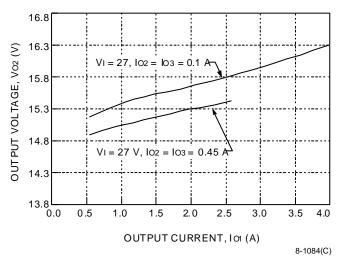


Figure 8. DC025ACL-M Typical Cross Regulation with Respect to Io1

Figure 6. DC025ABK-M Typical Cross Regulation with Respect to Io1

**Note:** Given the same load conditions, Output 3 has regulation characteristics similar to Output 2, except the polarity is negative.

8-1085(C)

8-1086(C)

# Characteristic Curves (continued)

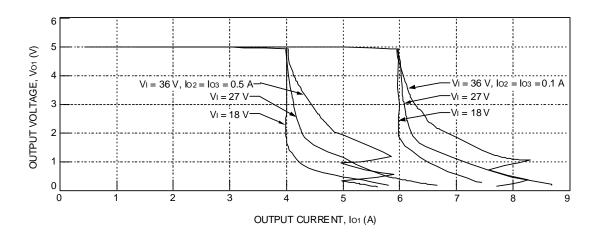


Figure 9. DC025ABK-M Typical 5 V Output Characteristics

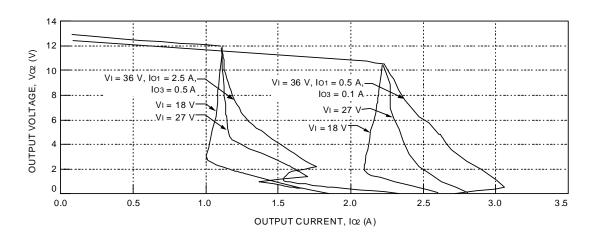


Figure 10. DC025ABK-M Typical 12 V Output Characteristics

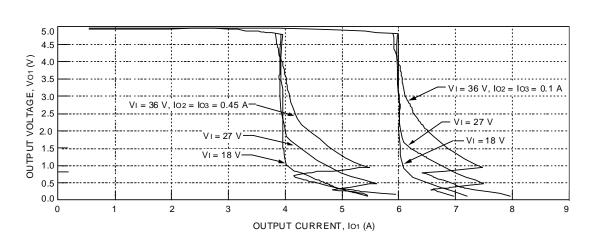


Figure 11. DC025ACL-M Typical 5 V Output Characteristics

8-1087(C)

# Characteristic Curves (continued)

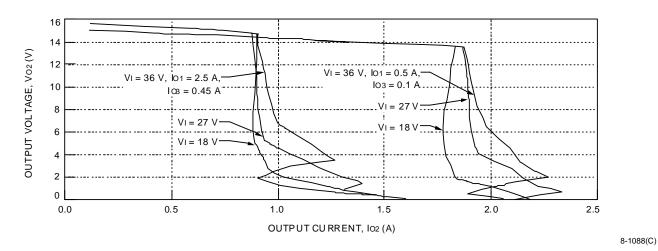
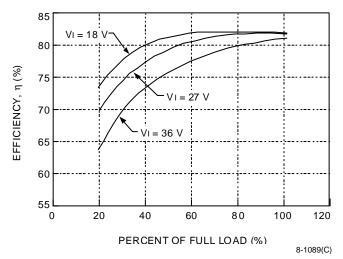
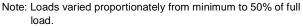
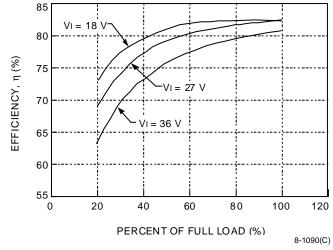


Figure 12. DC025ACL-M Typical 15 V Output Characteristics







Note: Loads varied proportionately from minimum to 50% of full load.

Figure 13. DC025ABK-M Typical Converter Efficiency

Figure 14. DC025ACL-M Typical Converter Efficiency

### Characteristic Curves (continued)

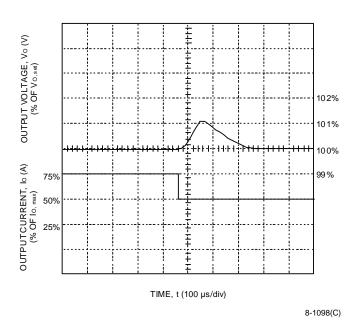


Figure 15. DC025 Triple Output-Series Typical
Output Voltage for a Step Load Change
from 75% to 50% of Full Load on
Output 1

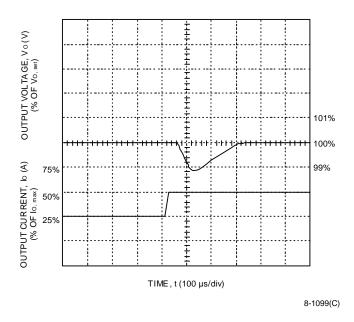


Figure 16. DC025 Triple Output-Series Typical
Output Voltage for a Step Load Change
from 25% to 50% of Full Load on
Output 1

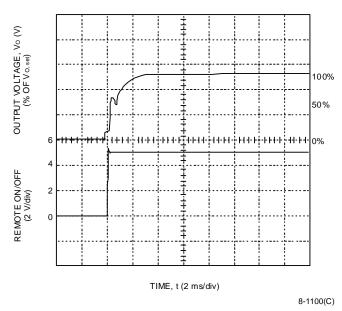
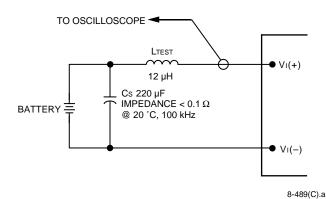


Figure 17. DC025 Triple Output-Series Typical
Output Voltage Start-Up when Signal
Applied to Remote On/Off

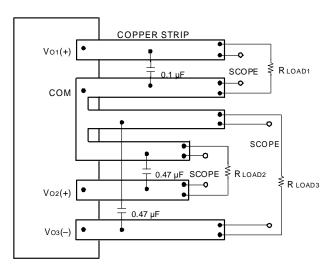
# **Test Configurations**



Note: Input reflected-ripple current is measured with a simulated source impedance (LTEST) of 12 µH. Capacitor Cs offsets possible battery impedance. Current is measured at the input of the module.

Figure 18. Input Reflected-Ripple Test Setup

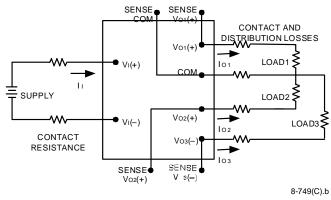
## **Test Configurations** (continued)



8-811(C)

Note: Use the specified ceramic capacitor. Scope measurement should be made by using a BNC socket. Position the load between 50 mm (2 in.) and 75 mm (3 in.) from the module.

Figure 19. Output Noise Measurement Test Setup



Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \frac{\sum_{j=1}^{3} |[V_{0j}(+) - V_{COM}]I_{0j}|}{[V_{I}(+) + (-V_{I}(-))]I_{I}} \times 100$$

Figure 20. Triple Output Voltage and Efficiency Measurement Test Setup

## **Design Considerations**

### Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. A 33  $\mu\text{F}$  electrolytic capacitor (ESR < 0.7 % at 100 kHz) mounted close to the power module helps to ensure the stability of the unit.

### **Safety Considerations**

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL*-1950, *CSA* 22.2-950, and EN60950.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements.

If the input meets extra-low voltage (ELV) requirements, then the converter's output is considered ELV.

The input to these units is to be provided with a maximum 5 A normal-blow fuse in the ungrounded lead.

## **Feature Descriptions**

### **Output Overvoltage Clamp**

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage control that reduces the risk of output overvoltage.

#### **Current Limit**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

## **Output Voltage Set-Point Adjustment**

The output voltage adjustment feature provides the capability of increasing or decreasing the output voltage set point of a module. This can be accomplished by using an external resistor connected between the TRIM pin and either the Vo1(+) or common pins. With an external resistor between the TRIM and common pins (Radj-up), the output voltage set point (Vo, adj) increases.

$$R_{\text{adj-up}} = \left(\frac{42.35}{\text{Vo. adj} - \text{Vo. nom}}\right) \text{k}\Omega$$

**Note:** The output voltage adjustment range must not exceed 110% of the nominal output voltage between the Vo<sub>1</sub>(+) and common terminals.

With an external resistor connected between the TRIM and Vo1(+) pins (Radj-down), the output voltage set point (Vo, adj) decreases.

$$R_{\text{adj-down}} \ = \left(\frac{\left(V_{\text{O, adj}} - 2.5\right) \times 16.94}{V_{\text{O, nom}} - V_{\text{O, adj}}}\right) \, k\Omega$$

Note: The output voltage adjustment must be 90% or more of the nominal output voltage between the Vo<sub>1</sub>(+) and common terminals.

#### Remote On/Off

Two remote on/off options are available. Positive logic remote on/off turns the module on during a logic high voltage on the REMOTE ON/OFF pin, and off during a logic low. Negative logic remote on/off, suffix code "1," turns the module off during a logic high and on during a logic low.

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the  $V_1(-)$  terminal ( $V_{on/off}$ ). The switch can be an open collector or equivalent (see Figure 21). A logic low is  $V_{on/off} = 0$  V to 1.2 V. The maximum  $I_{on/off}$  during a logic low is 1 mA. The switch should maintain a logic low voltage while sinking 1 mA.

During a logic high, the maximum  $V_{on/off}$  generated by the power module is 10 V. The maximum allowable leakage current of the switch at  $V_{on/off}$  = 10 V is 50  $\mu$ A.

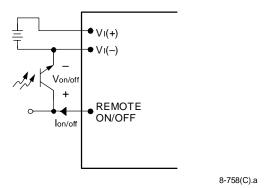
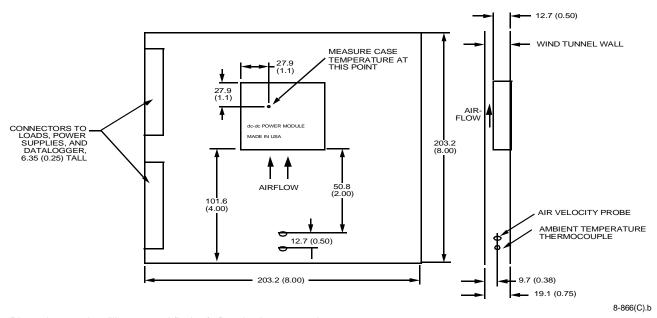


Figure 21. Remote On/Off Implementation

#### **Thermal Considerations**



Note: Dimensions are in millimeters and (inches). Drawing is not to scale.

Figure 22. Thermal Test Setup

The 25 W triple output power modules are designed to operate in a variety of thermal environments. As with any electronic component, sufficient cooling must be provided to ensure reliable operation. Heat dissipating components inside the module are thermally coupled to the case to enable heat removal by conduction, convection, and radiation to the surrounding environment.

The thermal data presented is based on measurements taken in a wind tunnel. The test setup shown in Figure 22 was used to collect data. Actual performance can vary depending on the particular application environment.

## Thermal Considerations (continued)

#### **Basic Thermal Performance**

The maximum operating temperature of the DC025 Triple Output-Series Power Modules at a given operating condition can be predicted by combining the power dissipation curves (Figures 23 through 27), the power derating curve (Figure 28), and the thermal resistance curve (Figure 28).

Use Figures 23 through 28 and the steps below to predict the safe operating region for many different operating and environmental conditions.

1. Calculate the total output power.

Pototal = 
$$(Io_1 \times Vo_1) + (Io_2 \times Vo_2) + (Io_3 \times Vo_3)$$

- Use Pototal with the appropriate figure (Figure 23 or 25) to determine the fixed losses (PP) associated with operating at Pototal. These losses are independent of which output the load is being drawn from.
- Use the desired output current (Io1) with Figure 25 to determine Ps1, which is the additional power being dissipated due to loading of the main output.
- Repeat Step 3 for outputs 2 and 3 using the appropriate figure (Figure 23 or 27) to determine Ps2 and Ps3, which is the power dissipated due to loading of the auxiliary outputs.
- 5. Find the total power dissipated (Pototal) by adding the four power dissipations obtained in Steps 2 through 4.

$$Pototal = PP + Ps1 + Ps2 + Ps3$$

 Use the estimated total power dissipated (Pototal) along with Figure 28 to determine the maximum ambient temperature allowable for a given air velocity.

For example, consider the DC025ABK power module operating with 27 V input and output currents  $I_{01} = 2.5 \text{ A}$ ,  $I_{02} = 0.5 \text{ A}$ ,  $I_{03} = 0.5 \text{ A}$ .

The total output power (Pototal) is 24.5 W. The total power dissipation is Pototal = 4.86 W, which is obtained by adding:

PP = 4.5 W (from Figure 23)

Ps1 = 0.22 W (from Figure 25)

Ps2 = 0.07 W (from Figure 23)

Ps3 = 0.07 W (from Figure 23)

Figure 28 shows that in natural convection the maximum operating ambient temperature for this module is approximately 66 °C.

Keep in mind that the procedure above provides approximations of the temperature and air velocities required to keep the case temperature below its maximum rating. The maximum case temperature, as monitored at the point shown in Figure 22, should be maintained at 100 °C or less under all conditions.

### **Air Velocity**

The air velocity required to maintain a desired maximum case temperature for a given power dissipation and ambient temperature can be calculated using Figure 28 and the following equation:

$$\theta \text{CA} = \frac{T\text{Cmax} - T\text{A}}{P\text{Dtotal}}$$

where:

- θcA is the thermal resistance from case-to-ambient air (°C/W)
- Tcmax is the desired maximum case temperature (°C)
- TA is the ambient inlet temperature (°C)
- Pototal is the total power dissipated by the module
   (W) at the desired operating condition

For example, to maintain a maximum case temperature of 85 °C with an ambient inlet temperature of 65 °C and a power dissipation of 4.86 W, the thermal resistance is:

$$\theta$$
CA  $\tilde{\sigma} \frac{85 \, ^{\circ}C - 65 \, ^{\circ}C}{4.86 \, W} = 4.1 \, ^{\circ}C/W$ 

This corresponds to an airflow greater than 0.38 ms<sup>-1</sup> (75 fpm) in Figure 28.

# **Thermal Considerations** (continued)

### Air Velocity (continued)

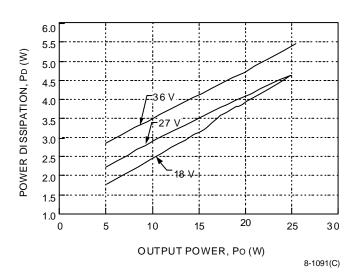


Figure 23. DC025ABK-M Fixed Losses, Pp

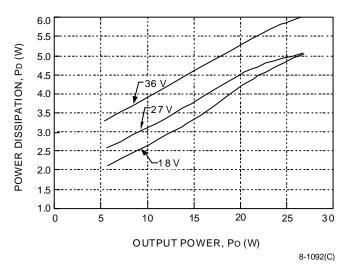


Figure 24. DC025ACL-M Fixed Losses, Pp

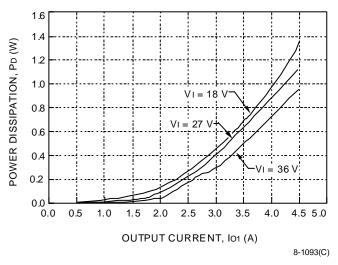


Figure 25. DC025ABK-M, DC025ACL-M Losses, Associated with 5 V Output, Ps1

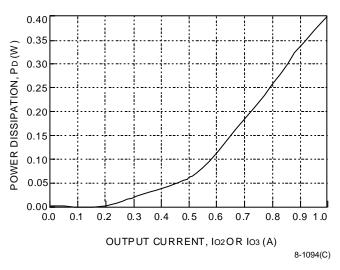


Figure 26. DC025ABK-M, Losses Associated with ±12 V Output, Ps2/Ps3

### Thermal Considerations (continued)

#### Air Velocity (continued)

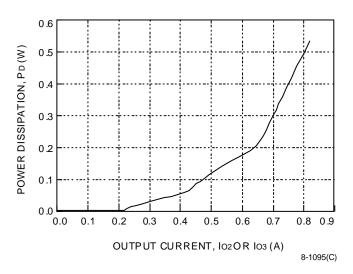


Figure 27. DC025ACL-M Losses Associated with ±15 V Output, Ps<sub>2</sub>/Ps<sub>3</sub>

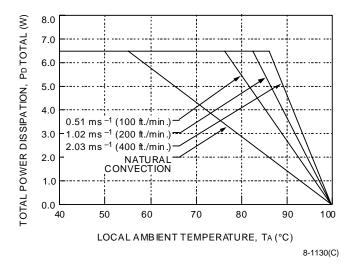


Figure 28. Total Power Dissipation vs. Local
Ambient Temperature and Air Velocity

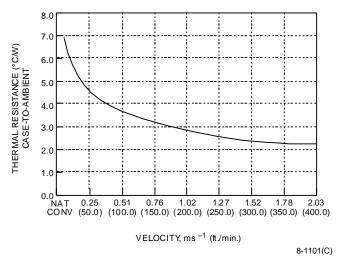


Figure 29. Case-to-Ambient Thermal Resistance vs.
Air Velocity

#### **Use of Heat Sinks and Cold Plates**

The DC025 Triple Output-Series case includes through-threaded M3 x 0.5 mounting holes allowing attachment of heat sinks or cold plates from either side of the module. The mounting torque must not exceed 0.56 N/m (5 in.-lb).

The following thermal model can be used to determine the required thermal resistance of the sink to provide the necessary cooling:

where PD is the power dissipated by the module,  $\theta$ cs represents the interfacial contact resistance between the module and the sink, and  $\theta$ sA is the sink-to-ambient thermal impedance (°C/W). For thermal greases or foils, a value of  $\theta$ cs = 0.1 °C/W to 0.3 °C/W is typical.

The required  $\theta_{SA}$  is calculated from the following equation:

$$\theta$$
sa =  $\frac{Tc - Ta}{Pptotal} - \theta cs$ 

Note that this equation assumes that all dissipated power must be shed by the sink. Depending on the user-defined application environment, a more accurate model including heat transfer from the sides and rear of the module can be used. This equation provides a conservative estimate in such instances.

For further thermal information on these modules, refer to the *Thermal Management for CC-, CW, DC, DW-Series 25 W to 30 W Board-Mounted Power Modules* Technical Note.

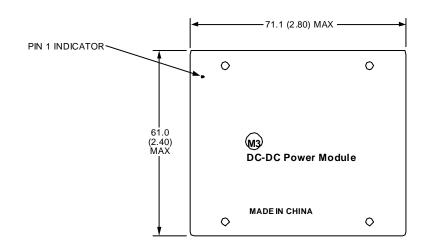
# **Outline Diagram**

Dimensions are in millimeters and (inches).

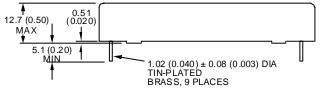
Copper paths must not be routed beneath the power module standoffs.

Tolerances:  $x.x \pm 0.5 \text{ mm} (0.02 \text{ in.}), x.xx \pm 0.25 \text{ mm} (0.010 \text{ in.}).$ 

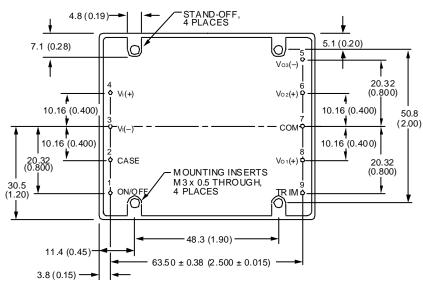
#### **Top View**



**Side View** 



# **Bottom View**



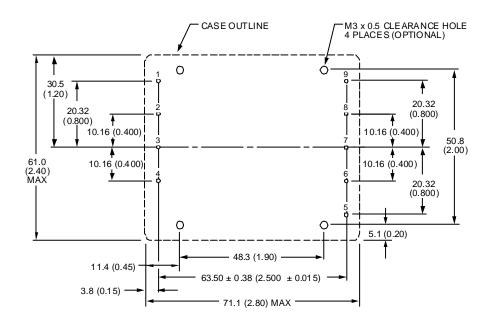
8-846(C)

# **Recommended Hole Pattern**

Component-side footprint.

Dimensions are in millimeters and (inches).

Recommended hole size for pin: 1.27 mm (0.050 in.)



8-846(C)

# Ordering Information

Table 4. Ordering Information Table

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Comcode
18 V—36 V	+5 V, ±12 V	25 W	positive	DC025ABK-M	107587313
18 V—36 V	+5 V, ±15 V	25 W	positive	DC025ACL-M	107587339

Optional features may be ordered using the device code suffixes shown below. To order more than one option, list suffixes in numerically descending order followed by the -M suffix, indicating metric (M3 x 0.5 heat sink hardware). The heat sinks designed for this package have an M prefix, i.e., MHSTxxx45 and MHSLxxx45 (see *Thermal Energy Management CC-, CW-, DC-, and DW-Series 25 W to 30 W Board-Mounted Power Modules* Technical Note).

Table 5. Options Table

Option	Device Code Suffix
Short pins: 2.79 mm ± 0.25 mm (0.110 in. ± 0.010 in.)	8
Negative on/off logic	1

Please contact your Lineage Power Account Manager or Field Application Engineer for pricing and availability of options.



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DS96-128EPS (Replaces DS94-054EPS)