

GENERAL DESCRIPTION

The ADIS16203/PCB is a simple evaluation board that provides convenient access to the ADIS16203 using a standard 2mm, 2x6, connector interface. These connectors can be accessed using a variety of cable options, including standard 1mm ribbon cables. The ADIS16203/PCB is designed to be evaluated in an existing digital platform (MCU, DSP, FPGA, PLD, etc) or as part of the ADISEVAL system. Four mounting holes (sized for 2-56 or 2mm screws) have been provided to secure the board during evaluation.

CIRCUIT DESCRIPTION

The schematic, layout and parts list for the ADIS16203/PCB can be found in Figure 1, Figure 2, and Table 1.

The ADIS16203 has dedicated data/control registers which are used to control all input/output activity. These registers can be accessed using the 4-wire serial port interface (SPI) signals on J1: SCLK, CS, DOUT and DIN. For specific information on using the ADIS16203's SPI interface, refer to the ADIS16203 datasheet. Auxiliary functions, such as the 12-bit ADC input, 12-bit DAC output and digital I/O functions, can be accessed using J2. C1 is not installed but the pads are offered for additional filtering of power supply inputs.

Table 1 – ADIS16203/PCB Parts List

Reference Designator	Part Description
U1	ADIS16203CCCZ
J1,J2	Connector, 12-pin, dual row, 2mm
C1	Not installed

NOTE: The ADIS16203CCCZ is designed to provide incline angle measurements along the pitch and roll axes. It will provide erroneous measurements if it is not oriented "normal" to the earth's surface.

SPECIAL NOTES ON HANDLING

Note that the ADIS16203/PCB is not reverse polarity protected. Reversing the power supply or applying inappropriate voltages to any pin (outside the Absolute Maximum Ratings in the ADIS16203 data sheet) may damage the ADIS16203/PCB.

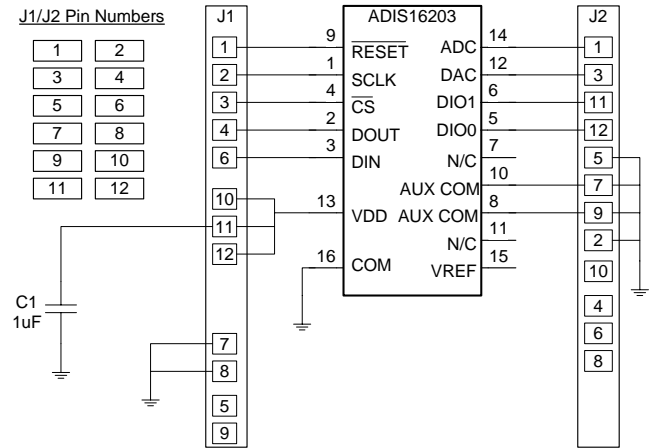


Figure 1 - ADIS16203/PCB Schematic

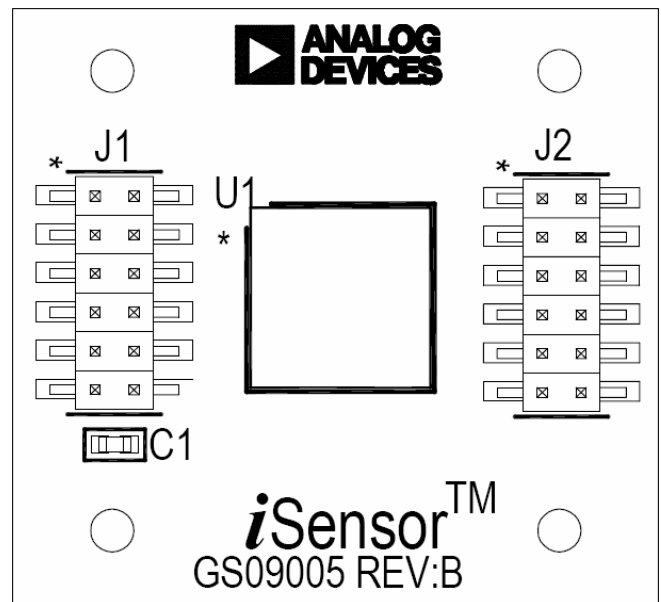


Figure 2 - ADIS16203/PCB Layout (Top View)

ORDERING GUIDE

Model	Package Description
ADIS16203/PCBZ	Evaluation Board, RoHS Compliant
ADISEVAL	iSensor™ PC Evaluation System