

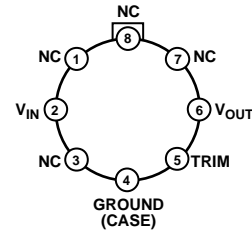
### FEATURES

- 10 V output,  $\pm 0.3\%$  maximum
- Adjustment range,  $\pm 3\%$  minimum
- Excellent temperature stability, 8.5 ppm/ $^{\circ}\text{C}$  maximum
- Low noise, 30  $\mu\text{V}$  p-p maximum
- Low supply current, 1.4 mA maximum
- Wide input voltage range, 12 V to 40 V
- High load driving capability, 10 mA
- No external components
- Short-circuit proof

### GENERAL DESCRIPTION

The REF01 precision voltage reference provides a stable 10 V output that can be adjusted over a 3% range with minimal effect on temperature stability. Single-supply operation over an input voltage range of 12 V to 40 V, a low current drain of 1 mA, and excellent temperature stability are achieved with an improved band gap design. Low cost, low noise, and low power make the REF01 an excellent choice whenever a stable voltage reference is required. Applications include DACs and ADCs, portable instrumentation, and digital voltmeters. Full military temperature range devices with screening to MIL-STD-883 are available. For new designs, refer to ADR01.

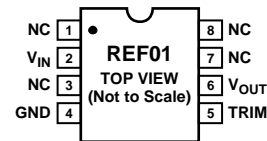
### PIN CONFIGURATIONS



NC = NO CONNECT. DO NOT CONNECT ANYTHING ON THESE PINS. SOME OF THEM ARE RESERVED FOR FACTORY TESTING PURPOSES.

00373-F-001

Figure 1. TO-99 (J Suffix)



NC = NO CONNECT. DO NOT CONNECT ANYTHING ON THESE PINS. SOME OF THEM ARE RESERVED FOR FACTORY TESTING PURPOSES.

00373-F-002

Figure 2. 8-Lead PDIP (P-Suffix)  
8-Lead CERDIP (Z-Suffix)  
8-Lead SOIC (S-Suffix)

OUTPUT RESISTORS			
REF01 OPTION	R9	R11	R12
P AND S PACKAGES	18k $\Omega$	4.5k $\Omega$	33.3k $\Omega$
J AND Z PACKAGES, AND 883C PRODUCT	50k $\Omega$	2k $\Omega$	16.7k $\Omega$

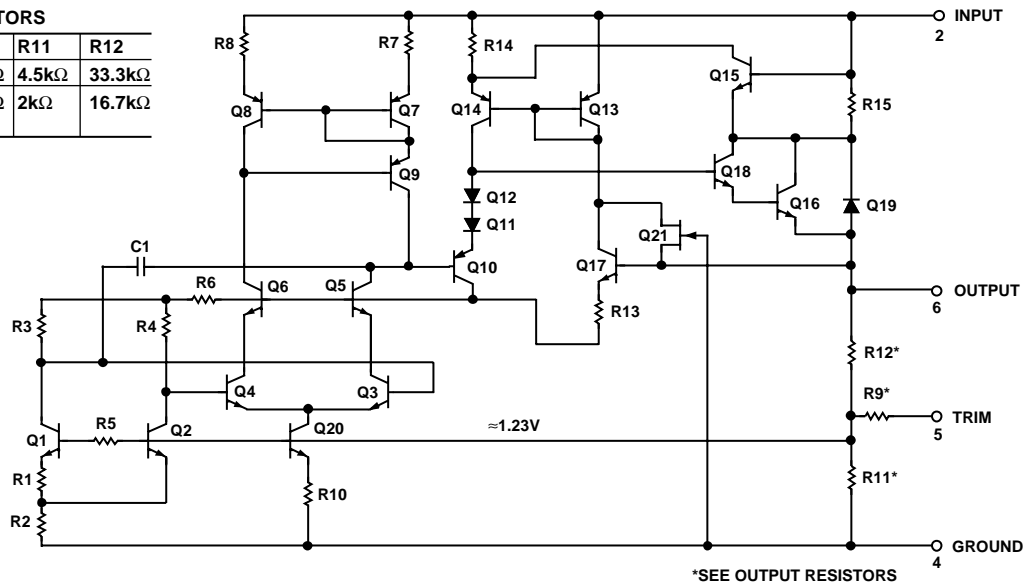


Figure 3. Simplified Schematic

00373-F-003

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

# SPECIFICATIONS

## ELECTRICAL SPECIFICATIONS

@  $V_{IN} = 15\text{ V}$ ,  $T_A = 25^\circ\text{C}$ , unless otherwise noted.

Table 1.

Parameter	Symbol	Conditions	REF01A/REF01E			REF01H			Unit
			Min	Typ	Max	Min	Typ	Max	
Output Voltage	$V_O$	$I_L = 0\text{ mA}$	9.97	10.00	10.03	9.95	10.00	10.05	V
Output Adjustment Range	$\Delta V_{TRIM}$	$R_P = 10\text{ k}\Omega$	$\pm 3.0$	$\pm 3.3$		$\pm 3.0$	$\pm 3.3$		%
Output Voltage Noise <sup>1</sup>	$e_{n\text{ p-p}}$	0.1 Hz to 10 Hz		30			30		$\mu\text{V p-p}$
S, Z, P Packages	$e_{n\text{ p-p}}$	0.1 Hz to 10 Hz		35			35		$\mu\text{V p-p}$
J, 883 Parts									
Line Regulation <sup>2</sup>		$V_{IN} = 13\text{ V to }33\text{ V}$		0.006	0.010		0.006	0.010	%/V
Load Regulation <sup>2</sup>		$I_L = 0\text{ mA to }10\text{ mA}$		0.005	0.008		0.006	0.010	%/mA
Turn-On Settling Time <sup>3</sup>	$t_{ON}$	To $\pm 0.1\%$ of final value		5			5		$\mu\text{s}$
Quiescent Supply Current	$I_{SY}$	No load		1.0	1.4		1.0	1.4	mA
Load Current	$I_L$		10			10			mA
Sink Current <sup>4</sup>	$I_S$		-0.3	-0.5		-0.3	-0.5		mA
Short-Circuit Current	$I_{SC}$	$V_O = 0$		30			30		mA

<sup>1</sup> Sample tested.

<sup>2</sup> Line and load regulation specifications include the effect of self-heating.

<sup>3</sup> Guaranteed by design, not production tested.

<sup>4</sup> During sink current test, the device meets the output voltage specified.

@  $V_{IN} = 15\text{ V}$ ,  $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$  for REF01A/REF01E, and  $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$  for REF01H, and  $I_L = 0\text{ mA}$ , unless otherwise noted.

Table 2.

Parameter	Symbol	Conditions	REF01A/REF01E			REF01H			Unit
			Min	Typ	Max	Min	Typ	Max	
Output Voltage Change	$\Delta V_{OT}$	$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$		0.02	0.06		0.07	0.17	%
with Temperature <sup>1, 2</sup>		$-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$		0.06	0.15		0.18	0.45	%
Output Voltage	$TCV_O$			3.0	8.5		10.0	25.0	ppm/°C
Temperature Coefficient <sup>3</sup>									
Change in $V_O$ Temperature Coefficient		$R_P = 10\text{ k}\Omega$		0.7			0.7		ppm/%
with Output Adjustment									
Line Regulation		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$		0.007	0.012		0.007	0.012	%/V
( $V_{IN} = 13\text{ V to }33\text{ V}$ ) <sup>4</sup>		$-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$		0.009	0.015		0.009	0.015	%/V
Load Regulation		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$		0.006	0.010		0.007	0.012	%/mA
( $I_L = 0\text{ mA to }8\text{ mA}$ ) <sup>4</sup>		$-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$		0.007	0.012		0.009	0.015	%/mA

<sup>1</sup>  $\Delta V_{OT}$  is defined as the absolute difference between the maximum output voltage and the minimum output voltage over the specified temperature range expressed as a percentage of 10 V:

$$\Delta V_{OT} = \left| \frac{V_{MAX} - V_{MIN}}{10\text{ V}} \right| \times 100$$

<sup>2</sup>  $\Delta V_{OT}$  specification applies trimmed to 10000 V or untrimmed.

<sup>3</sup>  $TCV_O$  is defined as  $\Delta V_{OT}$  divided by the temperature range; therefore,

$$TCV_O(0^\circ\text{C to }70^\circ\text{C}) = \frac{\Delta V_{OT}(0^\circ\text{C to }70^\circ\text{C})}{70^\circ\text{C}} \quad \text{and} \quad TCV_O(-55^\circ\text{C to }125^\circ\text{C}) = \frac{\Delta V_{OT}(-55^\circ\text{C to }125^\circ\text{C})}{180^\circ\text{C}}$$

<sup>4</sup> Line and load regulation specifications include the effect of self-heating.

# REF01

@  $V_{IN} = 15\text{ V}$ ,  $T_A = 25^\circ\text{C}$ , unless otherwise noted.

**Table 3.**

Parameter	Symbol	Conditions	REF01C			Unit
			Min	Typ	Max	
Output Voltage	$V_O$	$I_L = 0\text{ mA}$	9.90	10.00	10.10	V
Output Adjustment Range	$\Delta V_{TRIM}$	$R_P = 10\text{ k}\Omega$	$\pm 2.7$	$\pm 3.3$		%
Output Voltage Noise <sup>1</sup>						
S, Z, P Packages	$e_{n\text{-p-p}}$	0.1 Hz to 10 Hz		30		$\mu\text{V p-p}$
J, 883 Parts	$e_{n\text{-p-p}}$	0.1 Hz to 10 Hz		35		$\mu\text{V p-p}$
Line Regulation <sup>2</sup>		$V_{IN} = 13\text{ V to }33\text{ V}$		0.009	0.015	%/V
Load Regulation <sup>2</sup>		$I_L = 0\text{ mA to }8\text{ mA}$		0.006	0.015	%/mA
Turn-On Settling Time <sup>3</sup>	$t_{ON}$	To $\pm 0.1\%$ of final value		5		$\mu\text{s}$
Quiescent Supply Current	$I_{SY}$	No load		1.0	1.6	mA
Load Current	$I_L$		8			mA
Sink Current <sup>4</sup>	$I_S$		-0.3	-0.5		mA
Short-Circuit Current	$I_{SC}$	$V_O = 0$		30		mA

<sup>1</sup> Sample tested.

<sup>2</sup> Line and load regulation specifications include the effect of self-heating.

<sup>3</sup> Guaranteed by design, not production tested.

<sup>4</sup> During sink current test, the device meets the output voltage specified.

@  $V_{IN} = 15\text{ V}$ ,  $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$  for REF01CJ, REF01CZ, and  $-40^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$  for REF01CP and REF01CS, unless otherwise noted.

**Table 4.**

Parameter	Symbol	Conditions	REF01C			Unit
			Min	Typ	Max	
Output Voltage Change with Temperature <sup>1,2</sup>	$\Delta V_{OT}$			0.14	0.45	%
Output Voltage Temperature Coefficient <sup>3</sup>	$TCV_O$			20	65	ppm/ $^\circ\text{C}$
Change in $V_O$ Temperature Coefficient with Output Adjustment		$R_P = 10\text{ k}\Omega$		0.7		ppm/ $^\circ\text{C}$
Line Regulation <sup>4</sup>		$V_{IN} = 13\text{ V to }30\text{ V}$		0.011	0.018	%/V
Load Regulation <sup>4</sup>		$I_L = 0\text{ to }5\text{ mA}$		0.008	0.018	%/mA

<sup>1</sup>  $\Delta V_{OT}$  is defined as the absolute difference between the maximum output voltage and the minimum output voltage over the specified temperature range expressed as a percentage of 10 V:

$$\Delta V_{OT} = \left| \frac{V_{MAX} - V_{MIN}}{10\text{ V}} \right| \times 100$$

<sup>2</sup>  $\Delta V_{OT}$  specification applies trimmed to +10,000 V or untrimmed.

<sup>3</sup>  $TCV_O$  is defined as  $\Delta V_{OT}$  divided by the temperature range; therefore,

$$TCV_O(0^\circ\text{C to }70^\circ\text{C}) = \frac{\Delta V_{OT}(0^\circ\text{C to }70^\circ\text{C})}{70^\circ\text{C}} \quad \text{and} \quad TCV_O(-55^\circ\text{C to }125^\circ\text{C}) = \frac{\Delta V_{OT}(-55^\circ\text{C to }125^\circ\text{C})}{180^\circ\text{C}}$$

<sup>4</sup> Line and load regulation specifications include the effect of self-heating.

## ABSOLUTE MAXIMUM RATINGS

Table 5.

Parameter	Rating <sup>1</sup>
Input Voltage	40 V
Output Short-Circuit Duration (to Ground or $V_{IN}$ )	Indefinite
Storage Temperature Range J, S, and Z Packages	-65°C to +150°C
P Package	-65°C to +125°C
Operating Temperature Range	
REF01A	-55°C to +125°C
REF01CJ	0°C to 70°C
REF01CP, REF01CS, REF01E, REF01H	-40°C to +85°C
Junction Temperature ( $T_J$ )	-65°C to +150°C
Lead Temperature (Soldering @ 60 sec)	300°C

<sup>1</sup> Absolute maximum ratings apply to both DICE and packaged parts, unless otherwise noted.

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ESD CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



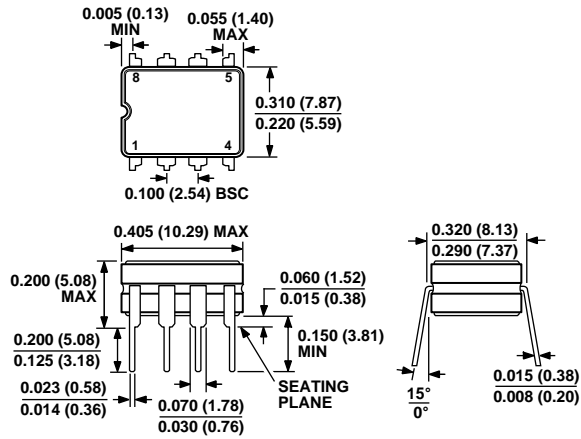
## THERMAL RESISTANCE

Table 6.

Package Type	$\theta_{JA}$ <sup>1</sup>	$\theta_{JC}$	Unit
TO-99 (J)	170	24	°C/W
8-Lead CERDIP (Z)	162	26	°C/W
8-Lead PDIP (P)	110	50	°C/W
8-Pin SOIC (S)	160	44	°C/W

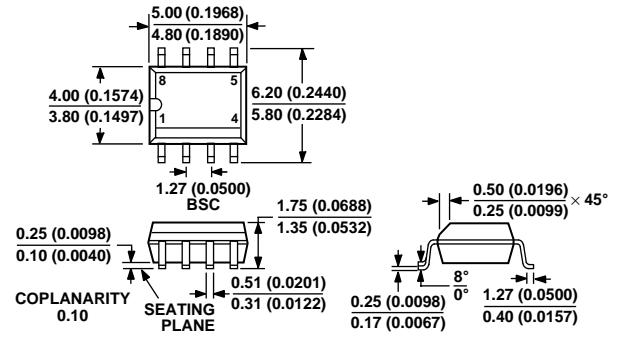
<sup>1</sup>  $\theta_{JA}$  is specified for worst-case mounting conditions; that is,  $\theta_{JA}$  is specified for device in socket for TO, CERDIP, and PDIP packages.  $\theta_{JA}$  is specified for device soldered to printed circuit board for SOIC package.

# OUTLINE DIMENSIONS



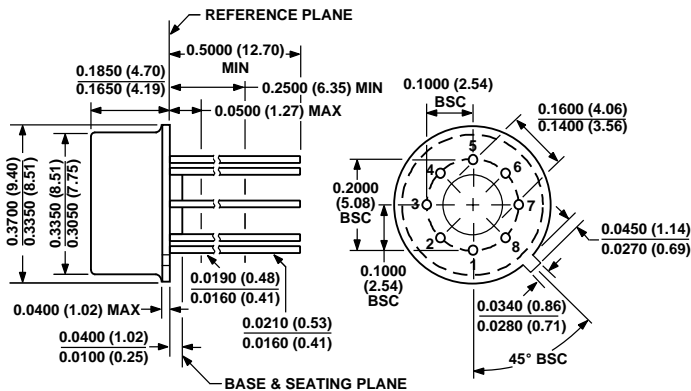
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

Figure 22. 8-Lead Ceramic Dual In-Line Package [CERDIP] (Q-8) Z-Suffix  
Dimensions shown in inches and (millimeters)



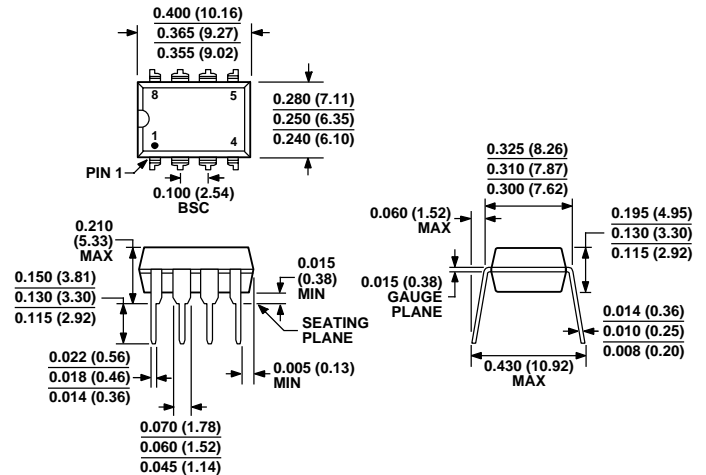
COMPLIANT TO JEDEC STANDARDS MS-012-AA  
CONTROLLING DIMENSIONS ARE IN MILLIMETERS; INCH DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF MILLIMETER EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

Figure 24. 8-Lead Standard Small Outline Package [SOIC] Narrow Body (R-8) S-Suffix  
Dimensions shown in millimeters and (inches)



COMPLIANT TO JEDEC STANDARDS MO-002-AK  
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

Figure 23. 8-Lead Metal Header [TO-99] (H-08) J-Suffix  
Dimensions shown in inches and (millimeters)



COMPLIANT TO JEDEC STANDARDS MS-001-BA  
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN. CORNER LEADS MAY BE CONFIGURED AS WHOLE OR HALF LEADS.

Figure 25. 8-Lead Plastic Dual In-Line Package [PDIP] Narrow Body (N-8) P-Suffix  
Dimensions shown in inches and (millimeters)

# REF01

## ORDERING GUIDE

Model	$T_A = 25^\circ\text{C}$		Package Description <sup>1</sup>	Package Option
	$\Delta V_{OS}$ Max (mV)	Temperature Range ( $^\circ\text{C}$ )		
REF01AJ/883C	$\pm 30$	-55 to +125	8-Lead TO-99	J-Suffix (H-08)
REF01EJ	$\pm 30$	-40 to +85	8-Lead TO-99	J-Suffix (H-08)
REF01CJ	$\pm 100$	0 to 70	8-Lead TO-99	J-Suffix (H-08)
REF01EZ	$\pm 30$	-40 to +85	8-Lead CERDIP	Z-Suffix (Q-8)
REF01HZ	$\pm 50$	-40 to +85	8-Lead CERDIP	Z-Suffix (Q-8)
REF01AZ/883C	$\pm 30$	-55 to +125	8-Lead CERDIP	Z-Suffix (Q-8)
REF01CP	$\pm 100$	-40 to +85	8-Lead PDIP	P-Suffix (N-8)
REF01CPZ <sup>2</sup>	$\pm 100$	-40 to +85	8-Lead PDIP	P-Suffix (N-8)
REF01HPZ <sup>2</sup>	$\pm 50$	-40 to +85	8-Lead PDIP	P-Suffix (N-8)
REF01HP	$\pm 50$	-40 to +85	8-Lead PDIP	P-Suffix (N-8)
REF01HS <sup>3</sup>	$\pm 50$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01HS-REEL <sup>3</sup>	$\pm 50$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01HSZ <sup>2,3</sup>	$\pm 50$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01HSZ-REEL <sup>2,3</sup>	$\pm 50$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CS <sup>3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CS-REEL <sup>3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CS-REEL7 <sup>3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CSZ-REEL <sup>2,3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CSZ-REEL7 <sup>2,3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)
REF01CSZ <sup>2,3</sup>	$\pm 100$	-40 to +85	8-Lead SOIC	S-Suffix (R-8)

<sup>1</sup> Burn-in is available on commercial and industrial temperature range parts in CERDIP, PDIP, and TO-99 packages.

<sup>2</sup> Z = Pb-free part.

<sup>3</sup> For availability and burn-in information on SOIC packages, contact your local Sales office.

