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The **LTC®1642** data sheet has been modified as shown below in **Bold**. For complete specifications, typical performance characteristics and applications information, please see the **LTC1642** data sheet.

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DC ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_{CC} = 5\text{V}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{CC}	Operating Voltage Range		●	2.97		16.5	V
V_{FB}	FB Pin Voltage Threshold (FB Falling)		●	1.208	1.220	1.232	V
ΔV_{FB}	FB Pin Threshold Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$	●		5	15	mV
V_{OV}	OV Pin Voltage Threshold (OV Rising)		●	1.208	1.220	1.232	V
ΔV_{OV}	OV Pin Threshold Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$	●		5	15	mV
V_{RST}	RST TMR Pin Voltage Threshold (RST TMR Rising)		●	1.200	1.220	1.250	V
ΔV_{RST}	RST TMR Pin Threshold Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$	●		5	15	mV
V_{BRK}	BRK TMR Pin Voltage Threshold (BRK TMR Rising)		●	1.200	1.220	1.250	V
ΔV_{BRK}	BRK TMR Pin Threshold Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$	●		5	15	mV
ΔV_{CR}	CRWBR Pin Threshold Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$	●		4	15	mV
ΔV_{GATE}	External N-Channel Gate Drive	$V_{GATE} - V_{CC}, V_{CC} = 3\text{V}$	●	4.5	5.9	8.0	V
		$V_{GATE} - V_{CC}, V_{CC} = 5\text{V}$	●	10	11.5	14	V
		$V_{GATE} - V_{CC}, V_{CC} = 15\text{V}$	●	4.5	8.5	18	V
V_{ONLO}	ON Pin Threshold (High to Low)		●	1.20	1.22	1.26	V
V_{REF}	Reference Output Voltage	No Load	●	1.208	1.220	1.232	V
ΔV_{LNR}	Reference Line Regulation	$2.97\text{V} \leq V_{CC} \leq 16.5\text{V}$, No Load	●		5	15	mV

For further information regarding this specification notice contact:

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