

Features

- RoHS compliant*
- Multiple SCSI termination resistors
- Stable thin-film-on-silicon technology
- Ultra-miniature packages to JEDEC standards



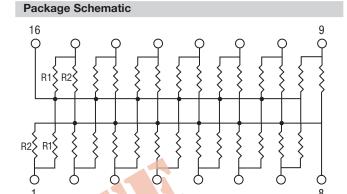
Applications

- Single-ended SCSI termination
- SCSI host adaptor cards
- All SCSI bus devices

Thin Film on Silicon 2QSP / 2NBS-XX3 Dual Terminator

General Information

Dual Terminator networks are used to correctly terminate single-ended SCSI bus lines. The standard R1, R2 combination ensures that the high frequency component of a signal transition will see an impedance at the termination equal to the characteristic impedance of the line. Fabricated with Tantalum Nitride on Silicon, these resistors feature excellent stability, TCR and tracking performance. The JEDEC standard miniature packages offer the most space-efficient Dual Terminator resistor array available.



Electrical & Environmental Characteristics

Electrical Characteristics	Symbol	Minimum	Nominal	Maximum	Unit
Resistance Range	R	220		750	Ω
Tolerance: Absolute		±1 %		±5 %	Ω
Ratio		±0.5 %		±2 %	Ω
TCR: Absolute Tracking			100 25		ppm/°C ppm/°C
Operating Voltage				50	V
Environmental Characteristics ESD		2 K			V
Operating Temperature	TJ	-55		+125	°C
Storage Temperature	T _{stg}	-65		+150	°C
Power Rating per Resistor @ 70 °C				0.1	Watt
Power Rating per Package @ 70 °C: QSOP: 16 Pin 20, 24 Pin 28 Pin				0.75 1.00 1.12	Watt Watt Watt
NBSOIC: 8 Pin 14, 16 Pin				0.60 1.00	Watt Watt

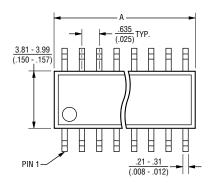
^{*}RoHS Directive 2002/95/EC Jan 27, 2003 including Annex

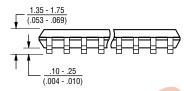
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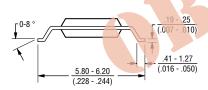
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Mechanical Characteristics

QSOP Package Dimensions





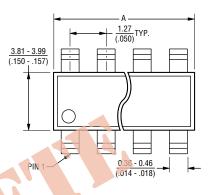


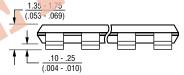
Model	Α			
2QSP16	4.80 - 4.98 (.189196)			
2QSP20	8.56 - 8.74 (.337344)			
2QSP24	8.56 - 8.74 (.337344)			
2QSP28	9.80 - 9.98 (.386393)			

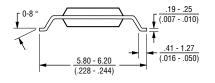
Governing dimensions are in mm. Dimensions in parentheses are in inches and are approximate.

JEDEC Reference Number MO-137.

Narrow-Body SOIC Package Dimensions







Model	Α			
2NBS08	4.80 - 4.98 (.189196)			
2NBS14	8.56 - 8.74 (.337344)			
2NBS16	9.80 - 9.98 (.386393)			

Governing dimensions are in mm. Dimensions in parentheses are in inches and are approximate.

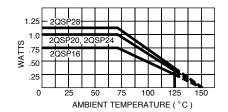
JEDEC Reference Number MS-012.

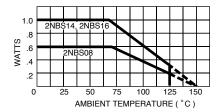
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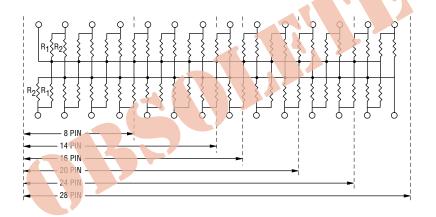
QSOP Package Power Temperature Derating Curve

Narrow-Body SOIC Package Power Temperature Derating Curve



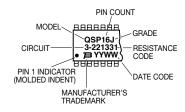


Schematic



Typical Part Marking

Represents total content. Layout may vary.



Standard Resistance Values

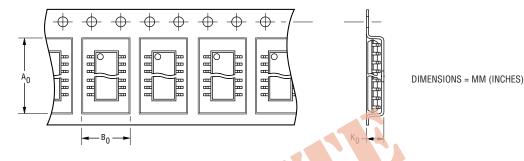
Resistor Designator	Resistance (ohms)	Resistance Code		
R1	220	221		
R2	330	331		
R1	330	331		
R2	680	681		
R1	750	751		
R2	750	751		

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Dispensing

For large quantities, the product will be dispensed in Tape and Reel (see diagram below).



Package	Α ₀	B ₀	K ₀	Width	Pitch	No. of Pieces	
						per 13 reel	per tube
QSOP							
16 Pin	6.4 (0.252)	5.2 (0.205)	2.1 (0.083)	12 (0.472)	8 (0.315)	3,500	98
20, 24 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	56
28 Pin	6.5 (0.256)	10.3 (0.406)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	49
NBSOIC							
8 Pin	6.4 (0.252)	9.0 (0.354)	2.1 (0.083)	12 (0.472)	8 (0.315)	3,500	98
14 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	56
16 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	49

Product Class Thin-Film-on-Silicon Standard Package Style QSP = QSOP NBS = Narrow-Body SOIC Pin Count QSP = 16, 20, 24, 28 NBS = 8, 14, 16 Dispensing R = Reel T = Tube Standard Grade Tolerance J = ±5 % G = ±2 % F = ±1 % Circuit 3 = Dual Terminator Resistance Value Code 1st three digits specify R1 resistance code. Last three digits specify R2 resistance code. Terminations • LF = 100 % Sn (RoHS Compliant)



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