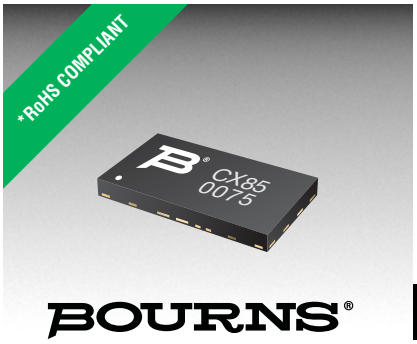


BOURNS®

Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High speed performance
- Small SMT package
- RoHS compliant*
- Agency recognition:

Applications

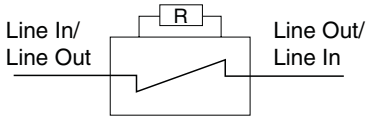
- Voice / VDSL cards
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

TBU-CX Series - TBU® High Speed Protectors

General Information

The TBU-CX Series of Bourns® TBU® products are low capacitance dual bidirectional high speed protection components, constructed using MOSFET semiconductor technology, and designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.

External Resistor



The TBU® high speed protector placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events. The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.

Agency Approval

Description	
UL	File Number: E315805

Absolute Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Value	Unit
V_{imp}	Peak impulse voltage withstand with duration less than 10 ms	TBU-CX025-VTC-WH TBU-CX040-VTC-WH TBU-CX050-VTC-WH TBU-CX065-VTC-WH TBU-CX085-VTC-WH	250 400 500 650 850	V
V_{rms}	Continuous A.C. RMS voltage	TBU-CX025-VTC-WH TBU-CX040-VTC-WH TBU-CX050-VTC-WH TBU-CX065-VTC-WH TBU-CX085-VTC-WH	100 200 250 300 425	V
T_{op}	Operating temperature range		-40 to +125	°C
T_{stg}	Storage temperature range		-65 to +150	°C
T_{jmax}	Maximum Junction Temperature		+125	°C
ESD	HBM ESD Protection per IEC 61000-4-2		±2	kV

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

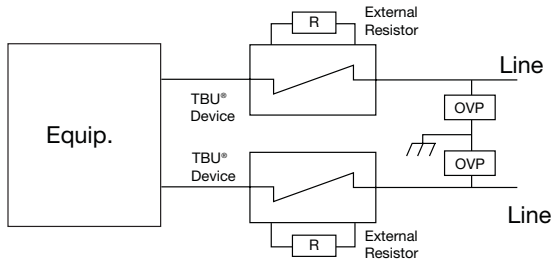
Symbol	Parameter		Part Number	Min.	Typ.	Max.	Unit
$I_{trigger}$	Current required for the device to go from operating state to protected state ($R_{external}$ = 0 ohm)		TBU-CXxxx-VTC-WH	500	750	1000	mA
R_{device}	Series resistance of the TBU device ($R_{external}$ = 0 ohm)	V_{imp} = 250 V $I_{trigger}$ (min.) = 500 mA V_{imp} = 400 V $I_{trigger}$ (min.) = 500 mA V_{imp} = 500 V $I_{trigger}$ (min.) = 500 mA V_{imp} = 650 V $I_{trigger}$ (min.) = 500 mA V_{imp} = 850 V $I_{trigger}$ (min.) = 500 mA	TBU-CX025-VTC-WH TBU-CX040-VTC-WH TBU-CX050-VTC-WH TBU-CX065-VTC-WH TBU-CX085-VTC-WH		2.6 3.6 5.0 7.0 10.7	3.0 4.2 5.7 8.0 13.0	Ω
t_{block}	Time for the device to go from normal operating state to protected state					1	µs
I_Q	Current through the triggered TBU® device with 50 Vdc circuit voltage			0.25	0.50	1.00	mA
V_{reset}	Voltage below which the triggered TBU® device will transition to normal operating state			12	16	20	V
$R_{th(j-l)}$	Junction to package pads - FR4 using recommended pad layout				98		°C/W
$R_{th(j-l)}$	Junction to package pads - FR4 using heat sink on board (6 cm^2) (1 in^2)				40		°C/W

*RoHS Directive 2002/95/EC Jan 27, 2003 including Annex.
Specifications are subject to change without notice.
Customers should verify actual device performance in their specific applications.

Reference Application

The TBU® devices are general use protectors used in a wide variety of applications. The maximum voltage rating of the TBU device should never be exceeded. Where necessary, an OVP should be employed to limit the maximum voltage. A cost-effective protection solution combines Bourns® TBU® protection devices with a pair of Bourns® MOVs. For bandwidth sensitive applications, a Bourns® GDT may be substituted for the MOV. See "Trigger Current vs External Resistor Value" graph for selecting the optimum trigger current value using a 0 ohm – 50 ohm resistor value.

Note: Line Resistance =
TBU® Device Resistance + $R_{external}$ Resistance



Basic TBU Operation

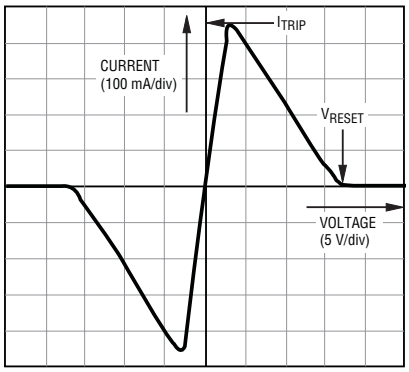
The TBU® device, constructed using MOSFET semiconductor technology, placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events. The TBU® device operates in approximately 1 µs - once line current exceeds the TBU® device's trigger current Itrigger. When operated, the TBU® device restricts line current to less than 1 mA typically. When operated, the TBU® device will block all voltages including the surge up to rated limits.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V_{reset} level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below V_{reset} (such as unpowered signal lines).

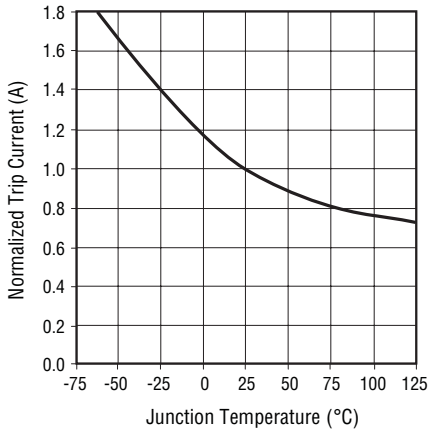
If the line has a normal DC bias above V_{reset}, the voltage across the TBU® device may not fall below V_{reset} after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, with software monitoring as one method used to accomplish this. Bourns application engineers can provide further assistance.

Performance Graphs

Typical V-I Characteristics (TBU-CX050-VTC-WH with Rext = 1 Ω)

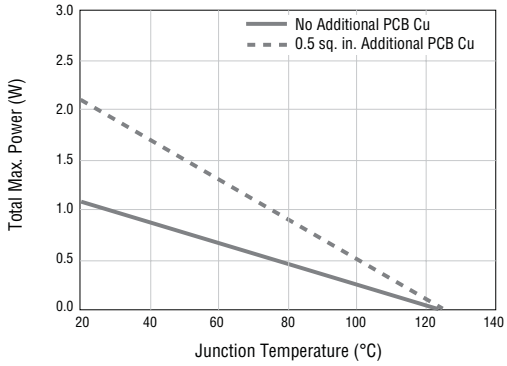


Typical Trigger Current vs. Temperature

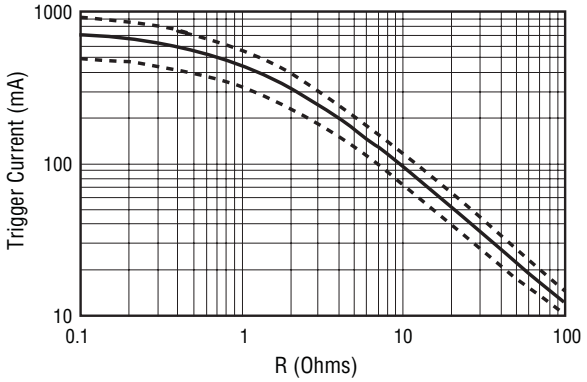


Performance Graphs (Continued)

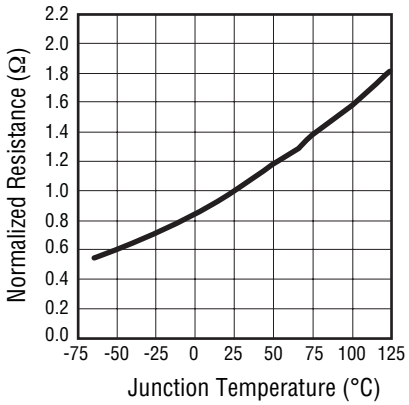
Power Derating Curve



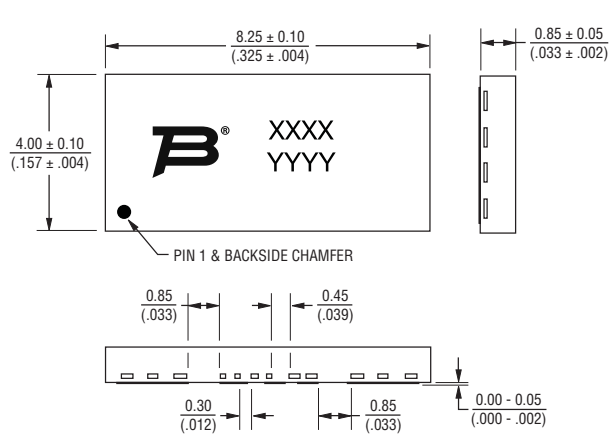
Trigger Current vs. External Resistor Value

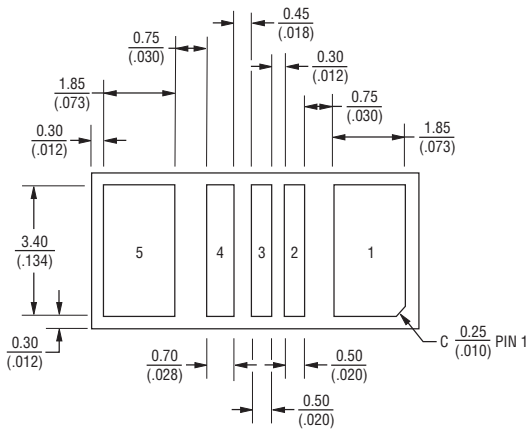


Typical Resistance vs. Temperature



Product Dimensions



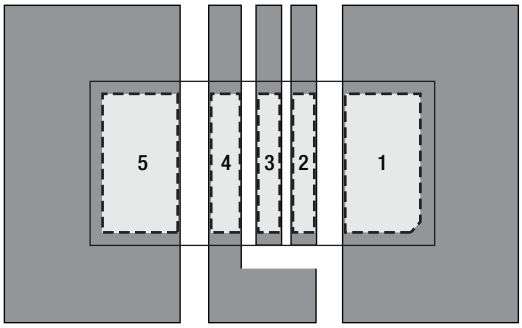


Pad Designation

Pad #	Pin Out
1	Line In/Out
2	External R Pad
3	External R Pad
4	NU
5	Line Out/In

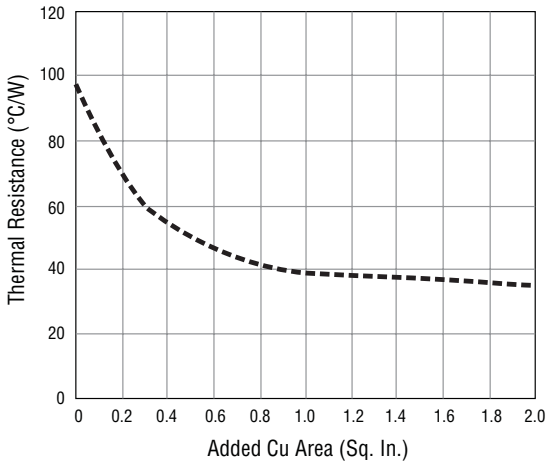
Recommended Pad Layout

TBU® protectors have matte-tin termination finish. The suggested layout should use Non-Solder Mask Define (NSMD). The recommended stencil thickness is 0.10-0.12 mm (.004-.005 in.) with a stencil opening size 0.025 mm (.0010 in.) less than the device pad size. As when heat sinking any power device, it is recommended that wherever possible, extra PCB copper area is allowed. For minimum parasitic capacitance, do not allow any signal, ground or power signals beneath any of the pads of the device.



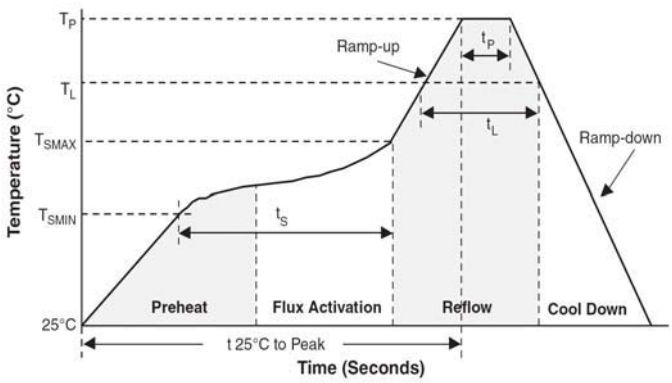
Dark grey areas show added PCB copper area for better thermal resistance.

Thermal Resistance vs Additional PCB Cu Area



Reflow Profile

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/sec. max.
Preheat - Temperature Min. (Tsmin) - Temperature Max. (Tsmax) - Time (tsmin to tsmax)	 150 °C 200 °C 60-180 sec.
Time maintained above: - Temperature (TL) - Time (tL)	 217 °C 60-150 sec.
Peak/Classification Temperature (Tp)	260 °C
Time within 5 °C of Actual Peak Temp. (tp)	20-40 sec.
Ramp-Down Rate	6 °C/sec. max.
Time 25 °C to Peak Temperature	8 min. max.



TBU-CX Series - TBU® High Speed Protectors

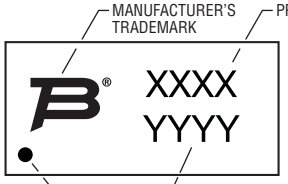
BOURNS®

How to Order

TBU - CX 085 - VTC - WH

- TBU® Product
- Series
  - CX = Bi-Series
- Impulse Voltage Rating
  - 025 = 250 V
  - 040 = 400 V
  - 050 = 500 V
  - 065 = 650 V
  - 085 = 850 V
- Trigger Current
  - VTC = Variable Trigger Current
- Hold to Trip Ratio Suffix
  - W = Hold to Trip Ratio
- Package Suffix
  - H = DFN Package

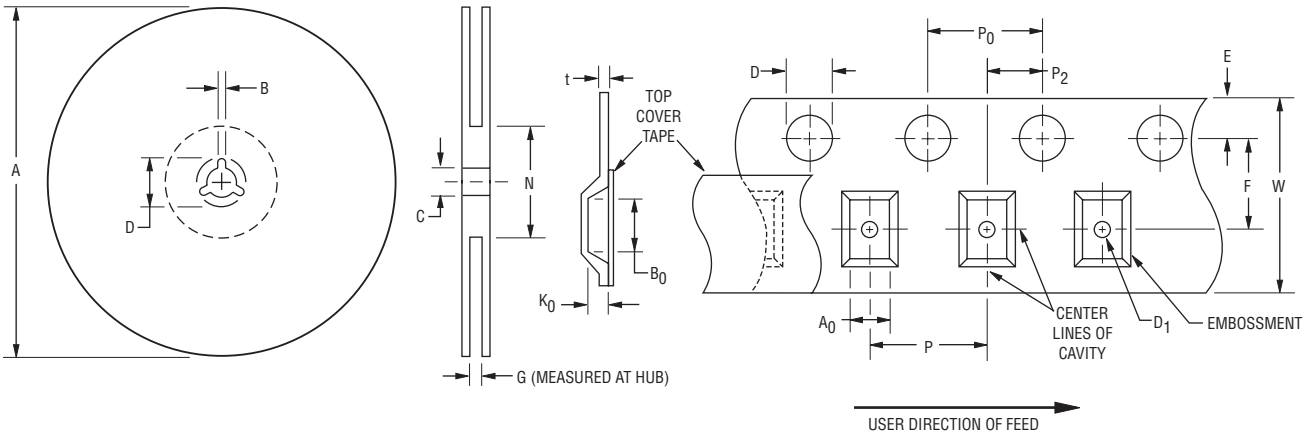
Typical Part Marking



- PRODUCT CODE
  - 1ST & 2ND DIGITS INDICATE PRODUCT FAMILY: CX = TBU-CX SERIES
  - 3RD & 4TH DIGITS INDICATE IMPULSE VOLTAGE:
    25 = 250 V 50 = 500 V 85 = 850 V
    40 = 400 V 65 = 650 V
- MANUFACTURING DATE CODE
  - 1ST DIGIT INDICATES THE YEAR'S 6-MONTH PERIOD.
  - 2ND DIGIT INDICATES THE WEEK NUMBER IN THE 6-MONTH PERIOD.
  - 3RD & 4TH DIGITS INDICATE SPECIFIC LOT FOR THE WEEK.

6-MONTH PERIOD CODES:
A = JAN-JUN 2009 C = JAN-JUN 2010 E = JAN-JUN 2011
B = JUL-DEC 2009 D = JUL-DEC 2010 F = JUL-DEC 2011

Packaging Specifications



QUANTITY: 3000 PIECES PER REEL

A		B		C		D		G	N
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
326 (12.835)	330 (13.002)	1.5 (.059)	2.5 (.098)	12.8 (.504)	13.5 (.531)	20.2 (.795)	-	16.5 (.650)	102 (4.016)

A_0		B_0		D		D_1		E		F	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
4.3 (.169)	4.5 (.177)	8.45 (.333)	8.65 (.341)	1.5 (.059)	1.6 (.063)	1.5 (.059)	-	1.65 (.065)	1.85 (.073)	7.4 (.291)	7.6 (.299)
K_0		P		P_0		P_2		t		W	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
1.0 (.039)	1.2 (.047)	7.9 (.311)	8.1 (.319)	3.9 (.159)	4.1 (.161)	1.9 (.075)	2.1 (.083)	0.25 (.010)	0.35 (.014)	15.7 (.618)	16.3 (.642)

DIMENSIONS: MM (INCHES)

REV. 12/10