



BSS84W

P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
-50V	$10\Omega @ V_{GS} = -5V$	-130mA

Description

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

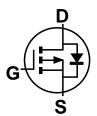
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
 Lead Free Plating (Matte Tin Finish Annealed over Alloy 42
 Leadframe). (3)
- Weight: 0.006 grams (Approximate)

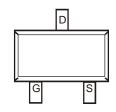




Top View



Equivalent Circuit



Top View

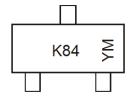
Ordering Information (Notes 4 & 5)

Part Number	Compliance	Case	Packaging
BSS84W-7-F	Standard	SOT323	3000 / Tape & Reel
BSS84WQ-7-F	Automotive	SOT323	3000 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K84 = Product Type Marking Code YM or \overline{Y} M = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Shanghai A/T Site

Date Code Key

Year	201	2	~		2018	20	19	2020		2021	2	2022
Code	Z		~		F	(3	Н				J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-50	V
Drain-Gate Voltage (Note 6)		V_{DGR}	-50	V
Gate-Source Voltage	Continuous	V _{GSS}	±20	V
Drain Current (Note 6)	Continuous	I _D	-130	mA
Pulsed Drain Current (Note 6)		I _{DM}	-1	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	200	mW
Thermal Resistance, Junction to Ambient	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV _{DSS}	-50	-75		V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current	I _{DSS}			-1 -2 -100	μΑ μΑ nA	V _{DS} = -50V, V _{GS} = 0V, T _J = +25°C V _{DS} = -50V, V _{GS} = 0V, T _J = +125°C V _{DS} = -25V, V _{GS} = 0V, T _J = +25°C		
Gate-Body Leakage	I _{GSS}			±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}$, $I_D = -1mA$		
Static Drain-Source On-Resistance	R _{DS(ON)}		6	10	Ω	$V_{GS} = -5V, I_D = -0.1A$		
Forward Transconductance	g FS	0.05	_	_	S	V _{DS} = -25V, I _D = -0.1A		
DYNAMIC CHARACTERISTICS								
Input Capacitance	Ciss			45	pF			
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = -25V, V_{GS} = 0V, f = 1.0MHz$		
Reverse Transfer Capacitance	Crss	_	_	12	pF			
SWITCHING CHARACTERISTICS								
Turn-On Delay Time	t _{D(ON)}		10	_	ns	$V_{DD} = -30V$, $I_D = -0.27A$,		
Turn-Off Delay Time	t _{D(OFF)}	_	18	_	ns	$R_{GEN} = 50\Omega$, $V_{GS} = -10V$		

6. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Incorporated's suggested pad layout document, which can be found on our website at http://www.diodes.com/package-outlines.html.

7. Short duration pulse test used to minimize self-heating effect.



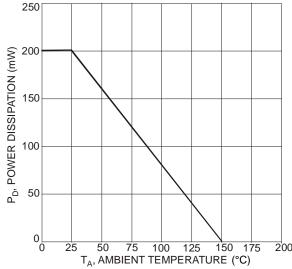
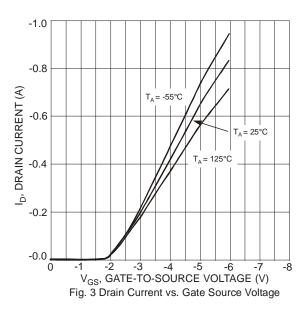


Fig. 1 Max Power Dissipation vs. Ambient Temperature



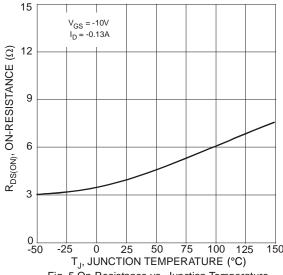


Fig. 5 On-Resistance vs. Junction Temperature

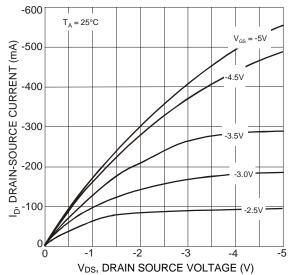
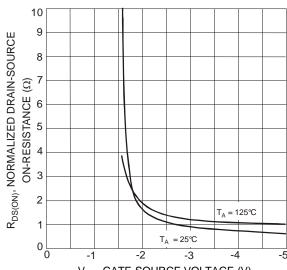


Fig. 2 Drain Source Current vs. Drain Source Voltage



V_{GS}, GATE-SOURCE VOLTAGE (V) Fig. 4 On-Resistance vs. Gate-Source Voltage

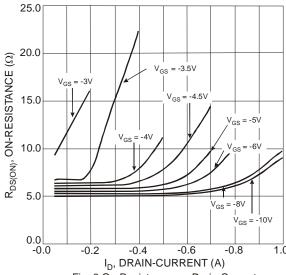


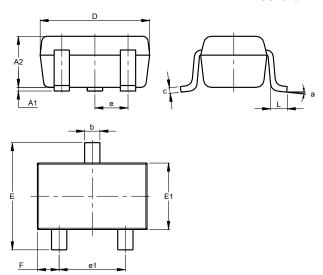
Fig. 6 On-Resistance vs. Drain-Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

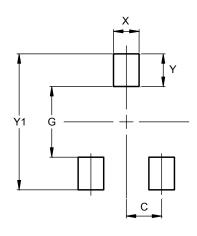


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	0.650 BSC					
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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