

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
-100V	4.2Ω @ V <sub>GS</sub> = -10V	-0.27A
	5.0Ω @ V <sub>GS</sub> = -4.0V	-0.24A

## Description

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

## Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package  
ESD Protected up to 2KV (HBM)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

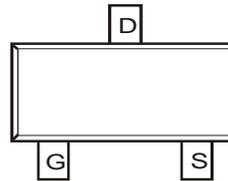
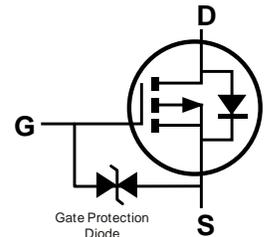
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe.  
Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)



SOT23



Top View


 Top View  
Pin Configuration


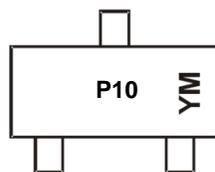
Equivalent Circuit

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP10H4D2S-7	SOT23	3,000/Tape & Reel
DMP10H4D2S-13	SOT23	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



P10 = Product Type Marking Code  
 YM = Date Code Marking  
 Y or Y̅ = Year (ex: E = 2017)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021	2022
Code	C	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-100	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C	I <sub>D</sub>	-0.27	A
		T <sub>A</sub> = +70°C		-0.21	
Pulsed Drain Current (10µs Pulse, Duty Cycle ≤1%)			I <sub>DM</sub>	-1.0	A
Maximum Body Diode Continuous Current (Note 6)			I <sub>S</sub>	-0.42	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation	(Note 5)		P <sub>D</sub>	0.38	W
	(Note 6)			0.44	
Thermal Resistance, Junction to Ambient	Steady State	(Note 5)	R <sub>θJA</sub>	333	°C/W
Thermal Resistance, Junction to Ambient		(Note 6)	R <sub>θJA</sub>	282	
Thermal Resistance, Junction to Case		(Note 6)	R <sub>θJC</sub>	115	
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-100	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = -100V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1.0	-2.3	-3.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	2.8	4.2	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.5A
		—	3.2	5.0		V <sub>GS</sub> = -4.0V, I <sub>D</sub> = -0.1A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.82	-1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -0.2A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	87	—	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	5.6	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	2.9	—		
Gate Resistance	R <sub>G</sub>	—	15.3	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Q <sub>g</sub>	—	1.8	—	nC	V <sub>DS</sub> = -80V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -0.5A
Gate-Source Charge	Q <sub>gs</sub>	—	0.3	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	0.5	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	3.3	—	ns	V <sub>DS</sub> = -50V, I <sub>D</sub> = -0.5A, V <sub>GS</sub> = -10V, R <sub>G</sub> = 10Ω
Turn-On Rise Time	t <sub>R</sub>	—	2.6	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	8.4	—		
Turn-Off Fall Time	t <sub>F</sub>	—	4.9	—		
Reverse Recovery Time	t <sub>RR</sub>	—	17.8	—	ns	V <sub>R</sub> = -100V, I <sub>F</sub> = -1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	24.8	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

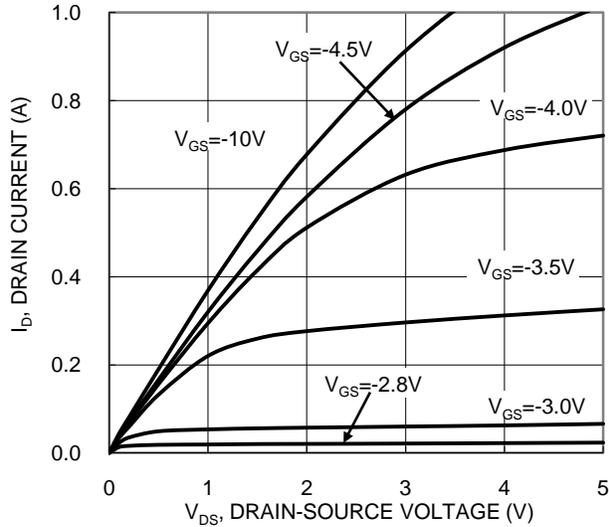


Figure 1. Typical Output Characteristic

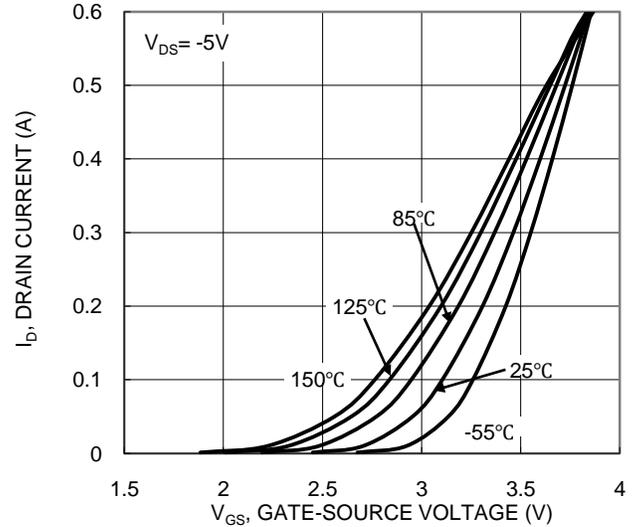


Figure 2. Typical Transfer Characteristic

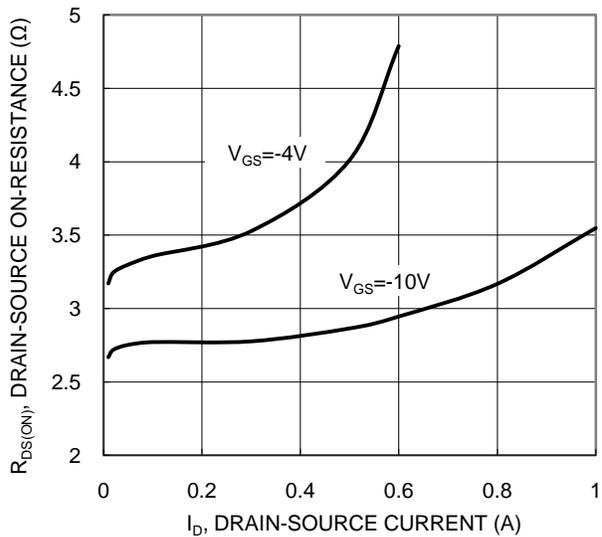


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

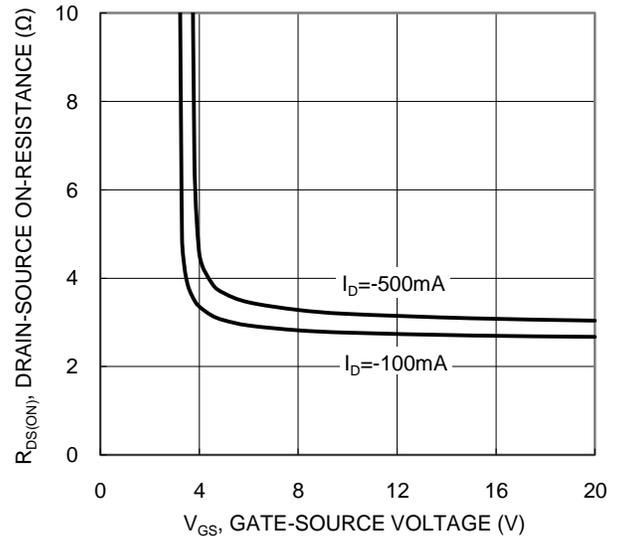


Figure 4. Typical Transfer Characteristic

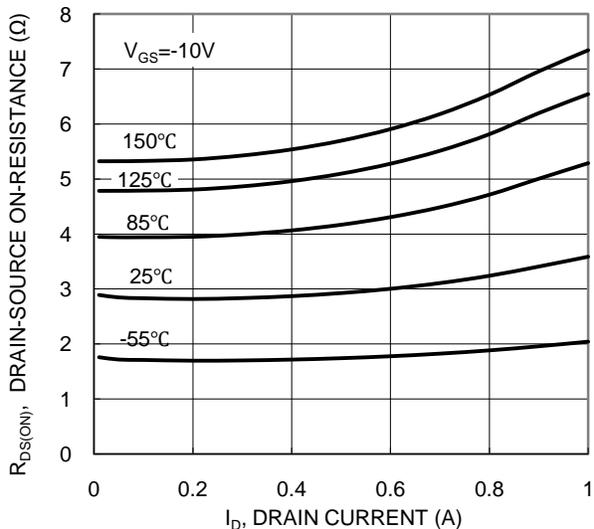


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

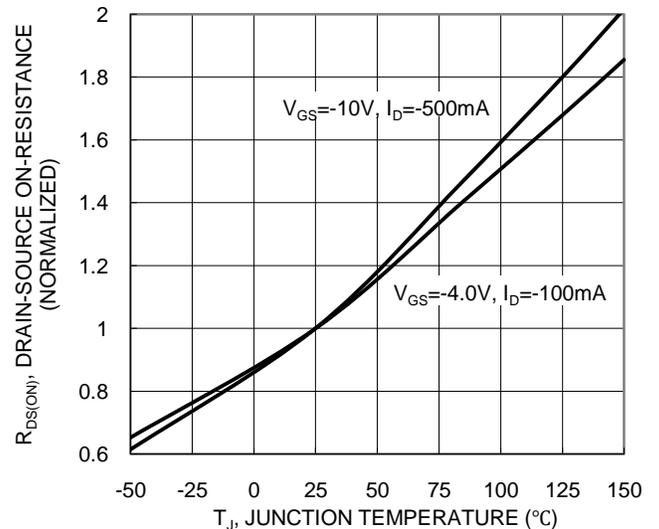


Figure 6. On-Resistance Variation with Junction Temperature

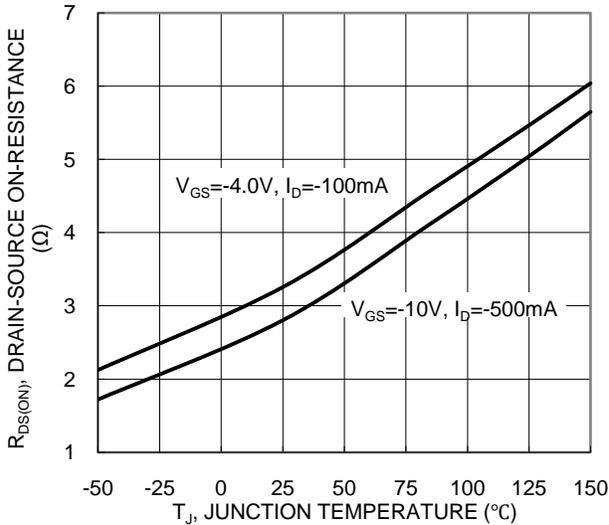


Figure 7. On-Resistance Variation with Junction Temperature

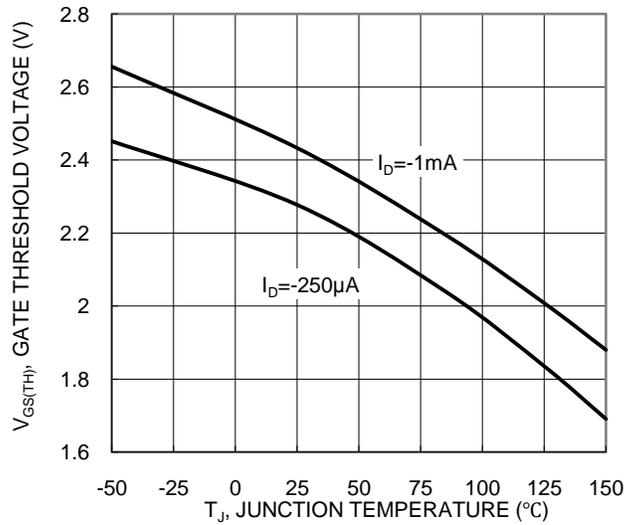


Figure 8. Gate Threshold Variation vs. Junction Temperature

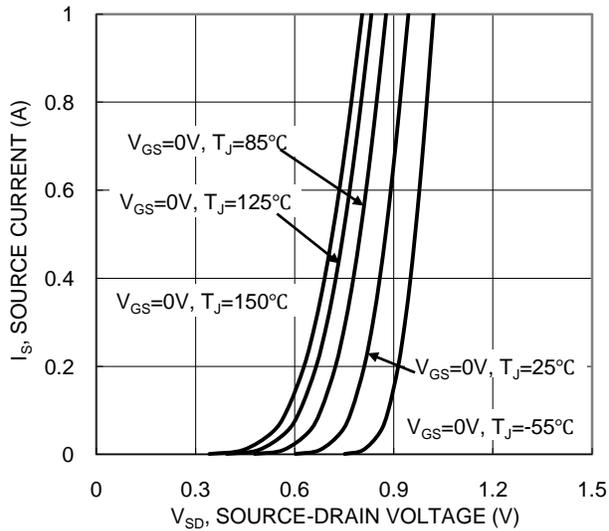


Figure 9. Diode Forward Voltage vs. Current

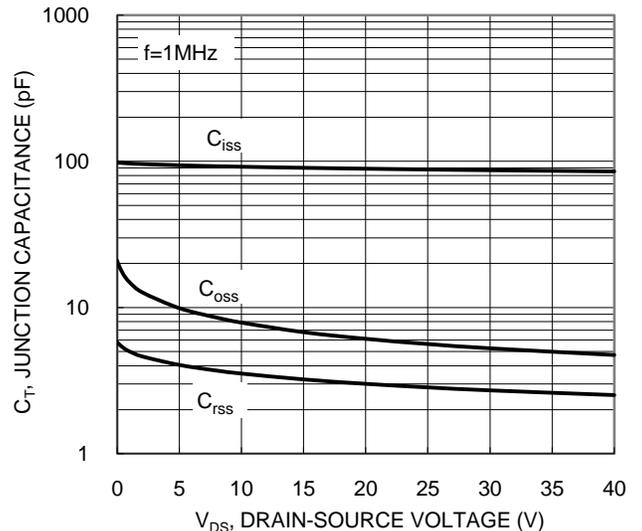


Figure 10. Typical Junction Capacitance

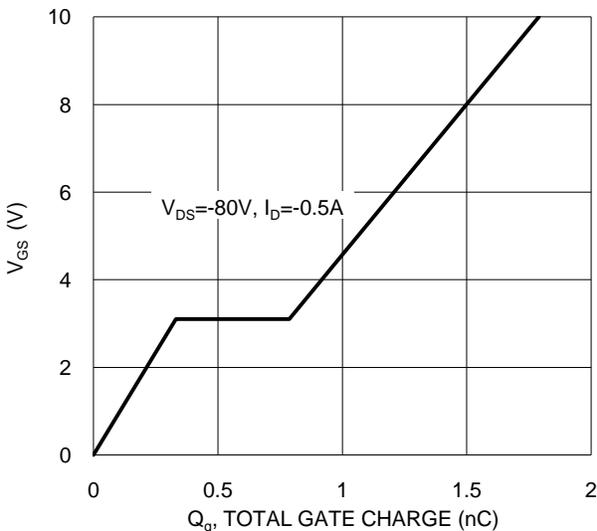


Figure 11. Gate Charge

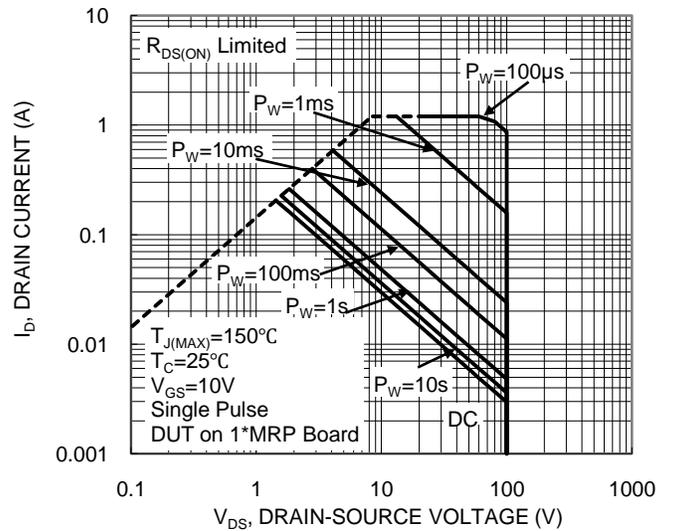


Figure 12. SOA, Safe Operation Area

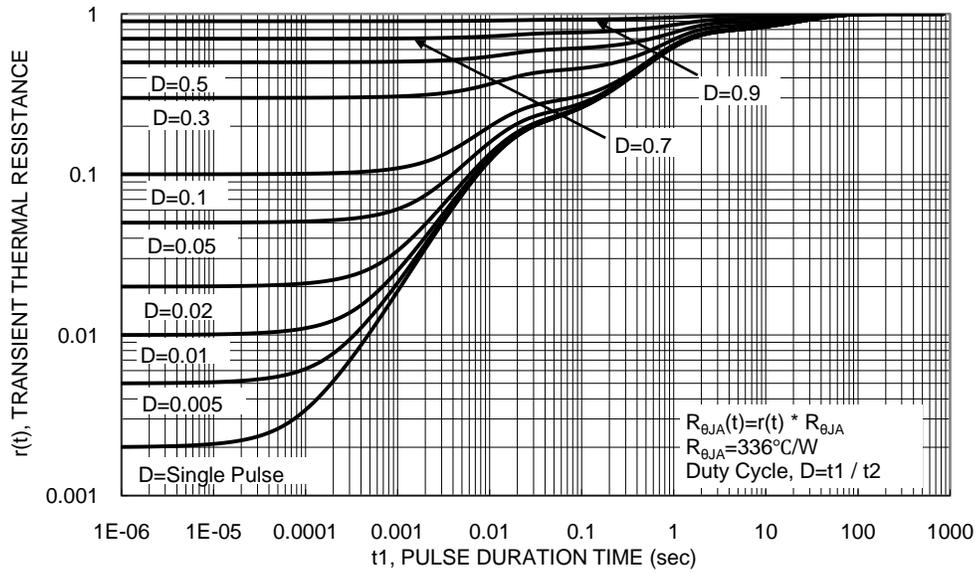
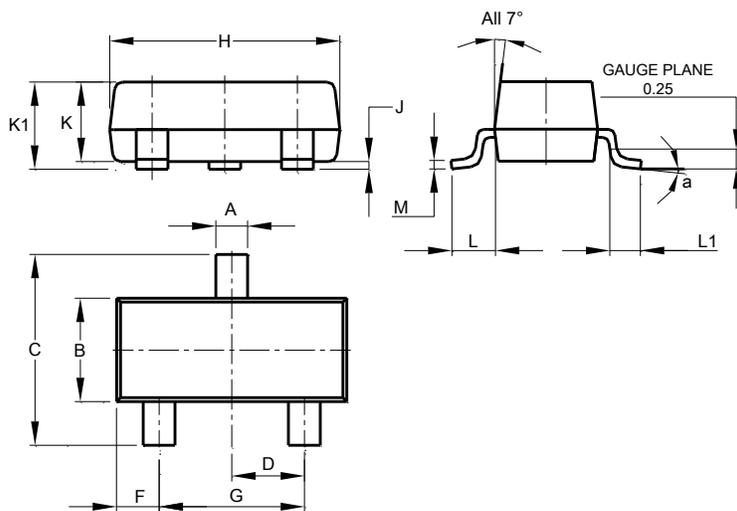


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23**

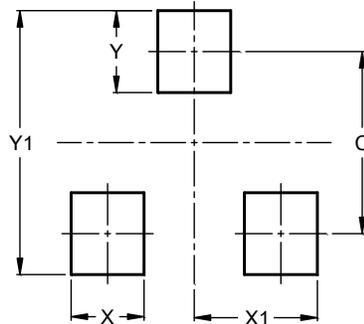


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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