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Jameco Part Number 1592263

5.0V HCMOS SMD OSCILLATOR **MODEL: F3345 SERIES**



FEATURES

- 5.0V Operation
- HCMOS/TTL Output
- Tri-State Enable/Disable
- Tape and Reel (2,000 pcs. STD)

OPTIONS

- 1.05mm Height Max
- Standby Function (F4101)



Learn more about:
Part Marking Identification
Tape and Reel Specification
Mechanical Specification
 Internet required



• PART NUMBER SELECTION Learn More - Internet Required				
Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
092-Frequency-xxxxx	F3345	±100PPM	-10 ~ +70	1.000 ~ 80.000
093-Frequency-xxxxx	F3345R	±100PPM	-40 ~ +85	1.000 ~ 80.000
090-Frequency-xxxxx	F3340	±50PPM	-10 ~ +70	1.000 ~ 80.000
091-Frequency-xxxxx	F3340R	±50PPM	-40 ~ +85	1.000 ~ 80.000
094-Frequency-xxxxx	F3346	±25PPM	-10 ~ +70	1.000 ~ 80.000
095-Frequency-xxxxx	F3346R	±25PPM*	-40 ~ +85	1.000 ~ 80.000
096-Frequency-xxxxx	F3348	±20PPM*	-10 ~ +70	1.000 ~ 80.000

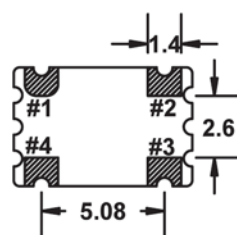
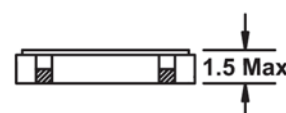
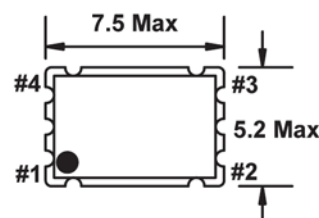
• ELECTRICAL CHARACTERISTICS	
PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.000 ~ 80.000 MHz
Storage Temperature Range (T _{STG})	-55°C ~ +125°C
Supply Voltage (V _{DD})	5.0V ± 10%
Input Current (I _{DD})	
1.000 ~ 25.000 MHz	25mA
25.000+ ~ 50.000 MHz	40mA
50.000+ ~ 67.000 MHz	60mA
67.000+ ~ 80.000 MHz	73mA
Output Symmetry (50% V _{DD})	45% ~ 55%
Rise Time (10% ~ 90% V _{DD}) (T _R)	7nS
Fall Time (90% ~ 10% V _{DD}) (T _F)	7nS
Output Voltage (V _{OL})	10% V _{DD}
(V _{OH})	90% V _{DD} Min
Output Current (I _{OL})	16mA Min
(I _{OH})	-16mA Min
Output Load	
TTL	10TTL
HCMOS	50pF
Start-up Time (T _s)	10mS
Enable/Disable Time ²	100nS
Maximum Soldering Temp / Time	260°C / 10 Seconds
Moisture Sensitivity Level (MSL)	1
Termination Finish	Au

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration. *Excludes Shock/Vibration

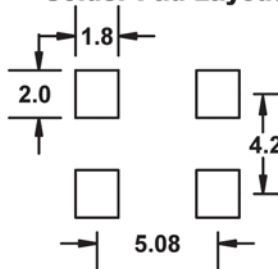
² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

Note: A 0.01μF bypass capacitor should be placed between VDD (Pin 4) and GND (Pin 2) to minimize power supply line noise.

All specifications subject to change without notice. Rev. 8/2/05



Recommended Solder Pad Layout



Pin Connections

#1 E/D #3 Output
 #2 GND #4 V_{DD}

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION	
INH (Pin 1)	OUTPUT (Pin 3)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 2.2 V	ACTIVE
'0' Level V _{IL} ≤ 0.8 V	High Z