

Device Information

DG508A

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CMOS Analog Multiplexers

DS Datasheet & Related Docs	Description	Key Features	PT Parametric Data
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Ordering Information

 **RoHS/Pb-Free/Green Device**

Part No.	Status	Temp.	Package	Price MSL US \$
DG508AAK	Active	Mil	16 Ld CerDIP	N/A 5.76 Buy
DG508ABK	Active	Ind	16 Ld CerDIP	N/A 5.84 Buy
DG508ACJ	Active	Comm	16 Ld PDIP	N/A 1.13 Buy
DG508ACJZ 	Active	Comm	16 Ld PDIP	N/A 1.13 Buy
DG508ACJZA 	InActive	Comm	16 Ld PDIP	N/A

The price listed is the manufacturer's suggested retail price for quantities between 100 and 999 units. However, prices in today's market are fluid and may change without notice.

MSL = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

SMD = Standard Microcircuit Drawing

 Description

The DG508A is a CMOS Monolithic 8-Channel Analog Multiplexer, which can also be used as a demultiplexer. An enable input is provided. When the enable input is high, a channel is selected by the address inputs, and when low, all channels are off.

A channel in the ON state conducts current equally well in both directions. In the OFF state each channel blocks voltages up to the supply rails. The address inputs and the enable input are TTL and CMOS compatible over the full specified operating temperature range.

The DG508A is pinout compatible with the industry standard devices.

 Key Features

- Low Power Consumption
- TTL and CMOS-Compatible Address and Enable Inputs
- 44V Maximum Power Supply Rating
- High Latch-Up Immunity
- Break-Before-Make Switching
- Alternate Source
- Pb-free Available

Related Documentation

- DS** Datasheet(s):
- [CMOS Analog Multiplexers](#)

PT Parametric Data

Switch or MUX	MUX
Config-uration	Single 8x1
$r_{DS(ON)}$ (Ω)	270
$T_{(ON)}$ (ns)	1000
$T_{(OFF)}$ (ns)	400
CHG INJ (pC)	4
Leakage (nA)	.03
SRC Cap (pF)	5
DRN Cap (ON) (pF)	25
I_{CC} (A)	1.3m
V_{CC} Range ($\pm V$)	± 5 to ± 20

Applications

- Data Acquisition Systems
- Communication Systems
- Signal Multiplexing/Demultiplexing
- Audio Signal Multiplexing