

Features

- 9A Peak Source/Sink Drive Current
- Wide Operating Voltage Range: 4.5V to 35V
- -40°C to +125°C Extended Operating Temperature Range
- Logic Input Withstands Negative Swing of up to 5V
- Matched Rise and Fall Times
- Low Propagation Delay Time: 19ns
- Low, 10 μ A Supply Current
- Low Output Impedance

Applications

- Efficient Power MOSFET and IGBT Switching
- Switch Mode Power Supplies
- Motor Controls
- DC to DC Converters
- Class-D Switching Amplifiers
- Pulse Transformer Driver



Ordering Information

Description

The IXDD609/IXDI609/IXDN609 high-speed gate drivers are especially well suited for driving the latest IXYS MOSFETs and IGBTs. The IXD_609 high-current output can source and sink 9A of peak current while producing voltage rise and fall times of less than 25ns. The input is CMOS compatible, and is virtually immune to latch up. Proprietary circuitry eliminates cross-conduction and current “shoot-through.” Low propagation delay and fast, matched rise and fall times make the IXD_609 family ideal for high-frequency and high-power applications.

The IXDD609 is configured as a non-inverting driver with an enable, the IXDN609 is configured as a non-inverting driver, and the IXDI609 is configured as an inverting driver.

The IXD_609 family is available in a standard 8-pin DIP (PI); 8-lead SOIC (SIA); 8-lead SOIC with an exposed, grounded metal back (SI); an 8-lead DFN (D2) package; and a 5-pin TO-263 package.

Part Number	Logic Configuration	Package Type	Packing Method	Quantity
IXDD609D2T/R		8-Lead DFN	Tape & Reel	2500
IXDD609SI		8-Lead SOIC with Exposed Grounded Metal Back	Tube	94
IXDD609SIT/R		8-Lead SOIC with Exposed Grounded Metal Back	Tape & Reel	2500
IXDD609SIA		8-Lead SOIC	Tube	94
IXDD609SIAT/R		8-Lead SOIC	Tape & Reel	2500
IXDD609PI		8-Pin DIP	Tube	50
IXDD609YI		5-Pin TO-263	Tube	50
IXDI609SI		8-Lead SOIC with Exposed Grounded Metal Back	Tube	94
IXDI609SIT/R		8-Lead SOIC with Exposed Grounded Metal Back	Tape & Reel	2500
IXDI609SIA		8-Lead SOIC	Tube	94
IXDI609SIAT/R		8-Lead SOIC	Tape & Reel	2500
IXDI609PI		8-Pin DIP	Tube	50
IXDI609YI		5-Pin TO-263	Tube	50
IXDN609SI		8-Lead SOIC with Exposed Grounded Metal Back	Tube	94
IXDN609SIT/R		8-Lead SOIC with Exposed Grounded Metal Back	Tape & Reel	2500
IXDN609SIA		8-Lead SOIC	Tube	94
IXDN609SIAT/R		8-Lead SOIC	Tape & Reel	2500
IXDN609PI		8-Pin DIP	Tube	50
IXDN609YI		5-Pin TO-263	Tube	50

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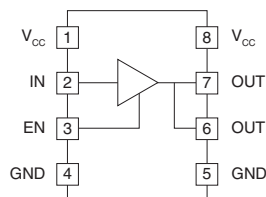
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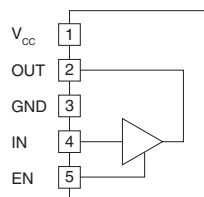
1 Specifications

1.1 Pin Configurations

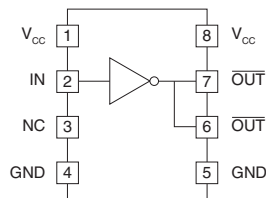
IXDD609 D2 / PI / SI / SIA



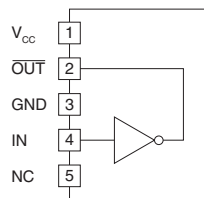
IXDD609 Y1



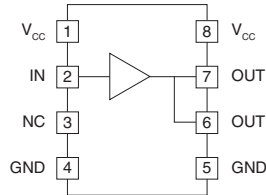
IXDI609 PI / SI / SIA



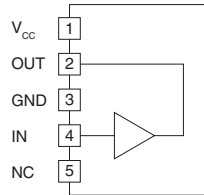
IXDI609 Y1



IXDN609 PI / SI / SIA



IXDN609 Y1



1.2 Pin Definitions

Pin Name	Description
IN	Logic Input
EN	Output Enable - Drive pin low to disable output, and force output to a high impedance state
OUT	Output - Sources or sinks current to turn-on or turn-off a discrete MOSFET or IGBT
$\overline{\text{OUT}}$	Inverted Output - Sources or sinks current to turn-on or turn-off a discrete MOSFET or IGBT
V _{CC}	Supply Voltage - Provides power to the device
GND	Ground - Common ground reference for the device
NC	Not connected

1.3 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Supply Voltage	V _{CC}	-	40	V
All Other Pins	-	-0.3	V _{CC} +0.3	V
Output Current	I _{OUT}	-	±9	A
Junction Temperature	T _J	-55	+150	°C
Storage Temperature	T _{STG}	-65	+150	°C

Unless stated otherwise, absolute maximum electrical ratings are at 25°C

Absolute maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

1.4 Recommended Operating Conditions

Parameter	Symbol	Minimum	Maximum	Units
Supply Voltage	V_{CC}	4.5	35	V
Operating Temperature Range	T_A	-40	+125	°C

1.5 Electrical Characteristics

Test Conditions: $T_A=25^{\circ}\text{C}$, $4.5\text{V} \leq V_{CC} \leq 35\text{V}$.

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Input Voltage, High	$4.5\text{V} \leq V_{CC} \leq 18\text{V}$	V_{IH}	3.0	-	-	V
Input Voltage, Low	$4.5\text{V} \leq V_{CC} \leq 18\text{V}$	V_{IL}	-	-	0.8	
Input Voltage Range	-	V_{IN}	-5	-	$V_{CC}+0.3$	
Input Current	$0\text{V} \leq V_{IN} \leq V_{CC}$	I_{IN}	-10	-	10	μA
EN Input Voltage, High	IXDD609 only	V_{ENH}	$2/3V_{CC}$	-	-	V
EN Input Voltage, Low	IXDD609 only	V_{ENL}	-	-	$1/3V_{CC}$	
Output Voltage, High	-	V_{OH}	$V_{CC}-0.025$	-	-	V
Output Voltage, Low	-	V_{OL}	-	-	0.025	
Output Resistance, High State	$V_{CC}=18\text{V}$, $I_{OUT}=-10\text{mA}$	R_{OH}	-	0.6	1	Ω
Output Resistance, Low State	$V_{CC}=18\text{V}$, $I_{OUT}=10\text{mA}$	R_{OL}	-	0.4	0.8	
Output Current, Continuous	Limited by package power dissipation	I_{DC}	-	-	± 2	A
Rise Time	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_R	-	25	45	ns
Fall Time	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_F	-	23	40	
On-Time Propagation Delay	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_{ONDLY}	-	18	35	
Off-Time Propagation Delay	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_{OFFDLY}	-	19	30	
Enable to Output-High Delay Time	IXDD609 only	t_{ENOH}	-	25	50	
Disable to High Impedance State Delay Time	IXDD609 only	t_{DOLD}	-	60	80	
Enable Pull-Up Resistor	-	R_{EN}	-	200	-	$\text{k}\Omega$
Power Supply Current	$V_{CC}=18\text{V}$, $V_{IN}=3.5\text{V}$	I_{CC}	-	1	3	mA
	$V_{CC}=18\text{V}$, $V_{IN}=0\text{V}$		-	-	10	
	$V_{CC}=18\text{V}$, $V_{IN}=V_{CC}$		-	-	10	

1.6 Electrical Characteristics

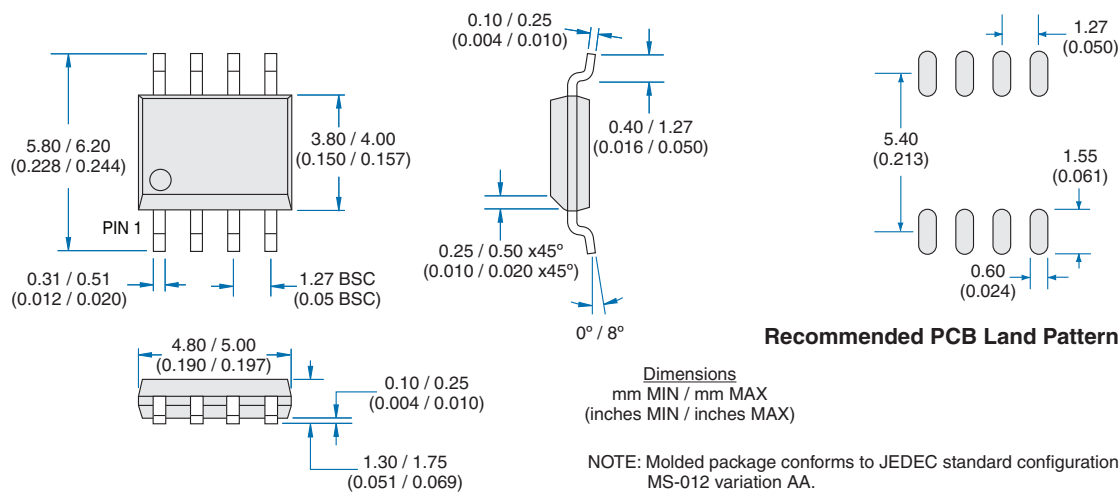
Test Conditions: $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, $4.5\text{V} \leq V_{CC} \leq 35\text{V}$, $T_J < 150^{\circ}\text{C}$.

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Input Voltage, High	$4.5\text{V} \leq V_{CC} \leq 18\text{V}$	V_{IH}	3.0	-	-	V
Input Voltage, Low	$4.5\text{V} \leq V_{CC} \leq 18\text{V}$	V_{IL}	-	-	0.8	
Input Voltage Range	-	V_{IN}	-5	-	$V_{CC}+0.3$	
Input Current	$0\text{V} \leq V_{IN} \leq V_{CC}$	I_{IN}	-10	-	10	μA
Output Voltage, High	-	V_{OH}	$V_{CC}-0.025$	-	-	V
Output Voltage, Low	-	V_{OL}	-	-	0.025	
Output Resistance, High State	$V_{CC}=18\text{V}$, $I_{OUT}=-10\text{mA}$	R_{OH}	-	-	2	Ω
Output Resistance, Low State	$V_{CC}=18\text{V}$, $I_{OUT}=10\text{mA}$	R_{OL}	-	-	1.5	
Output Current, Continuous	Limited by package power dissipation	I_{DC}	-	-	± 1	A
Rise Time	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_R	-	-	60	ns
Fall Time	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_F	-	-	60	
On-Time Propagation Delay	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_{ONDLY}	-	-	55	
Off-Time Propagation Delay	$C_{LOAD}=1000\text{pF}$, $V_{CC}=18\text{V}$	t_{OFFDLY}	-	-	40	
Enable to Output-High Delay Time	IXDD609 only	t_{ENOH}	-	-	60	
Disable to High Impedance State Delay Time	IXDD609 only	t_{DOLD}	-	-	100	
Power Supply Current	$V_{CC}=18\text{V}$, $V_{IN}=3.5\text{V}$	I_{CC}	-	1	3	mA
	$V_{CC}=18\text{V}$, $V_{IN}=0\text{V}$		-	-	10	μA
	$V_{CC}=18\text{V}$, $V_{IN}=V_{CC}$		-	-	10	

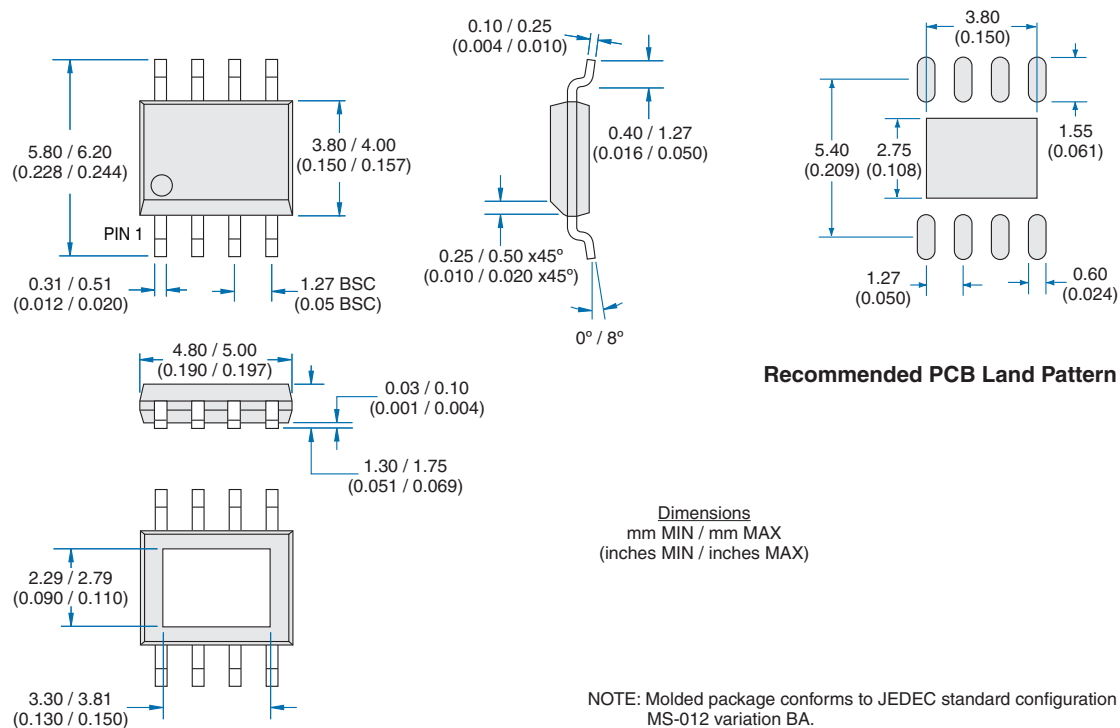
2 Manufacturing Information

2.1 Mechanical Dimensions

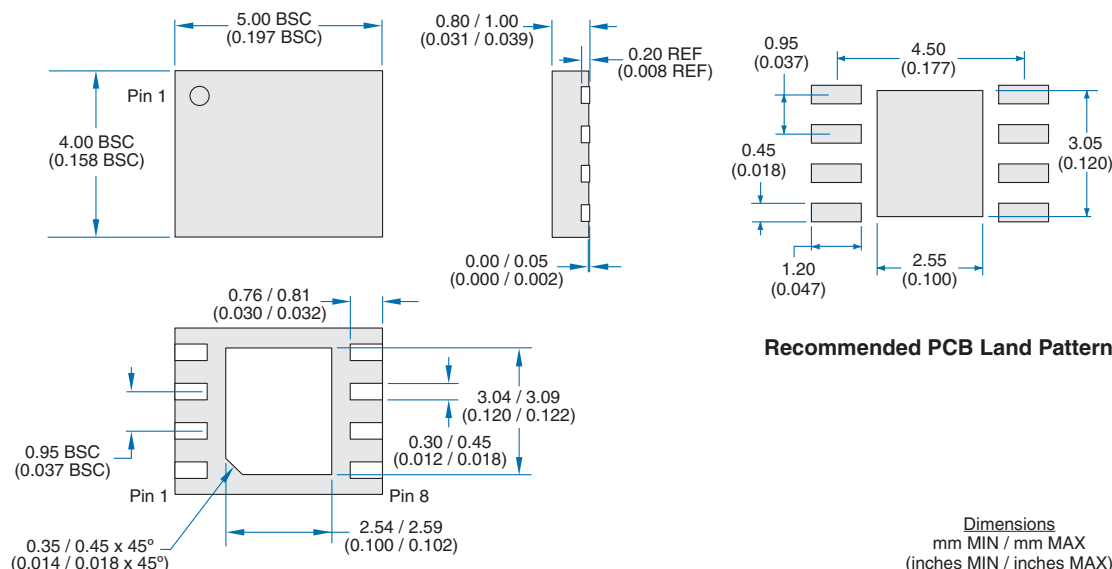
2.1.1 8-Pin SOIC - SIA Package



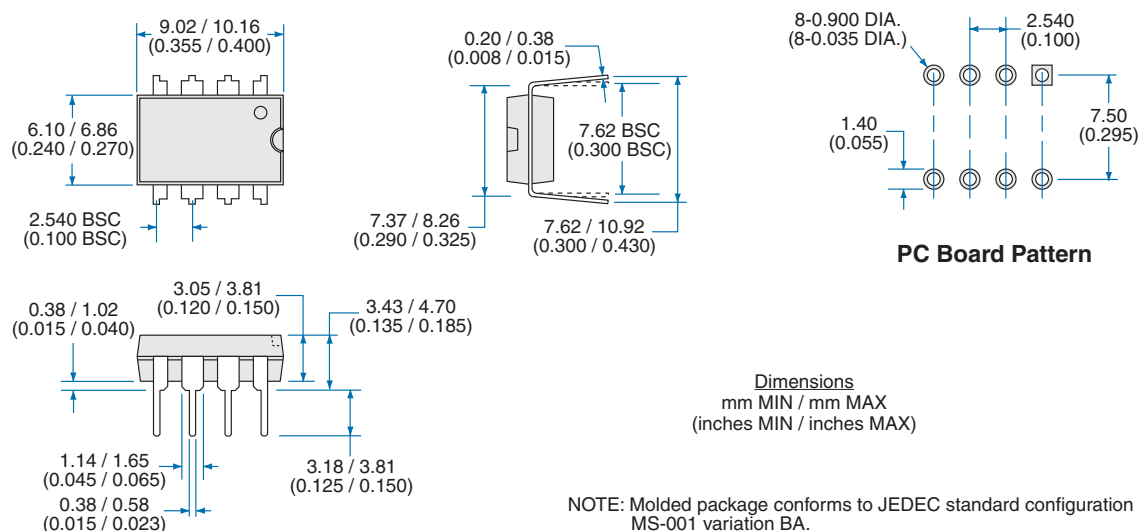
2.1.2 8-Pin SOIC - SI Package



2.1.3 8-Pin DFN - D2 Package



2.1.4 8-Pin DIP - PI Package



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