



# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## General Description

The MAX6412–MAX6420 low-power microprocessor supervisor circuits monitor system voltages from 1.6V to 5V. These devices are designed to assert a reset signal whenever the  $V_{CC}$  supply voltage or RESET IN falls below its reset threshold or the manual reset input is asserted. The reset output remains asserted for the reset timeout period after  $V_{CC}$  and RESET IN rise above the reset threshold and the manual reset input is deasserted. The reset timeout is externally set by a capacitor to provide more flexibility.

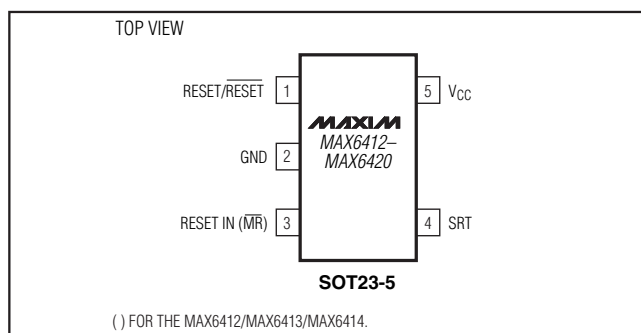
The MAX6412/MAX6413/MAX6414 feature fixed thresholds from 1.575V to 5V in approximately 100mV increments and a manual reset input. The MAX6415/MAX6416/MAX6417 are offered with an adjustable reset input that can monitor voltages down to 1.26V and the MAX6418/MAX6419/MAX6420 are offered with one fixed input and one adjustable input to monitor dual-voltage systems.

The MAX6412/MAX6415/MAX6418 have an active-low, push-pull reset output. The MAX6413/MAX6416/MAX6419 have an active-high, push-pull reset output and the MAX6414/MAX6417/MAX6420 have an active-low, open-drain reset output. All of these devices are offered in a SOT23-5 package and are fully specified from  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ .

## Applications

Automotive  
Medical Equipment  
Intelligent Instruments  
Portable Equipment  
Battery-Powered Computers/Controllers  
Embedded Controllers  
Critical  $\mu$ P Monitoring  
Set-Top Boxes  
Computers

## Pin Configuration



## Features

- ◆ Monitor System Voltages from 1.6V to 5V
- ◆ Capacitor-Adjustable Reset Timeout Period
- ◆ Manual Reset Input (MAX6412/MAX6413/MAX6414)
- ◆ Adjustable Reset Input Option (MAX6415–MAX6420)
- ◆ Dual-Voltage Monitoring (MAX6418/MAX6419/MAX6420)
- ◆ Low Quiescent Current (1.7 $\mu$ A, typ)
- ◆ 3 RESET Output Options  
Push-Pull RESET  
Push-Pull RESET  
Open-Drain RESET
- ◆ Guaranteed Reset Valid to  $V_{CC} = 1\text{V}$
- ◆ Power-Supply Transient Immunity
- ◆ Small SOT23-5 Packages

## Ordering Information

| PART          | TEMP RANGE                                      | PIN-PACKAGE |
|---------------|-------------------------------------------------|-------------|
| MAX6412UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6413UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6414UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6415UK-T   | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6416UK-T   | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6417UK-T   | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6418UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6419UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |
| MAX6420UK__-T | $-40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$ | 5 SOT23-5   |

**Note:** The MAX6412/MAX6413/MAX6414 and MAX6418/MAX6419/MAX6420 are available with factory-set  $V_{CC}$  reset thresholds from 1.575V to 5.0V in approximately 0.1V increments. Insert the desired nominal reset threshold suffix (from Table 1) into the blanks following the letters UK. There are 33 standard versions with a required order increment of 2500 pieces. Sample stock is generally held on standard versions only (see Standard Versions Table). Required order increment is 10,000 pieces for nonstandard versions. Contact factory for availability. All devices are available in tape-and-reel only. Devices are available in both leaded and lead-free packaging. Specify lead-free by replacing “-T” with “+T” when ordering.

Typical Operating Circuit appears at end of data sheet.

Selector Guide appears at end of data sheet.



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## ABSOLUTE MAXIMUM RATINGS

All Voltages Referenced to GND

|                                                    |                              |
|----------------------------------------------------|------------------------------|
| $V_{CC}$ .....                                     | -0.3V to +6.0V               |
| SRT, $\overline{MR}$ , RESET IN .....              | -0.3V to ( $V_{CC} + 0.3V$ ) |
| RESET, $\overline{RESET}$ (Push-Pull) .....        | -0.3V to ( $V_{CC} + 0.3V$ ) |
| RESET (Open-Drain) .....                           | -0.3V to +6.0V               |
| Input Current (All Pins) .....                     | $\pm 20mA$                   |
| Output Current ( $\overline{RESET}$ , RESET) ..... | $\pm 20mA$                   |

Continuous Power Dissipation ( $T_A = +70^\circ C$ )

|                                                                     |                                 |
|---------------------------------------------------------------------|---------------------------------|
| 5-Pin SOT23-5 (derate 7.1mW/ $^\circ C$ above $+70^\circ C$ ) ..... | 571mW                           |
| Operating Temperature Range .....                                   | $-40^\circ C$ to $+125^\circ C$ |
| Junction Temperature .....                                          | $+150^\circ C$                  |
| Storage Temperature Range .....                                     | $-65^\circ C$ to $+150^\circ C$ |
| Lead Temperature (soldering, 10s) .....                             | $+300^\circ C$                  |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $V_{CC} = 1V$  to  $5.5V$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise specified. Typical values are at  $V_{CC} = 5V$  and  $T_A = +25^\circ C$ .) (Note 1)

| PARAMETER                                               | SYMBOL        | CONDITIONS                                         | MIN                 | TYP               | MAX              | UNITS   |
|---------------------------------------------------------|---------------|----------------------------------------------------|---------------------|-------------------|------------------|---------|
| Supply Voltage Range                                    | $V_{CC}$      |                                                    | 1.0                 |                   | 5.5              | V       |
| Supply Current                                          | $I_{CC}$      | $V_{CC} \leq 5.0V$                                 |                     | 2.7               | 4.5              | $\mu A$ |
|                                                         |               | $V_{CC} \leq 3.3V$                                 |                     | 2                 | 3.5              |         |
|                                                         |               | $V_{CC} \leq 2.0V$                                 |                     | 1.7               | 2.5              |         |
| $V_{CC}$ Reset Threshold Accuracy                       | $V_{TH}$      | $T_A = +25^\circ C$                                | $V_{TH} - 1.5\%$    |                   | $V_{TH} + 1.5\%$ | V       |
|                                                         |               | $T_A = 0^\circ C$ to $+125^\circ C$                | $V_{TH} - 2.5\%$    |                   | $V_{TH} + 2.5\%$ |         |
|                                                         |               | $T_A = -40^\circ C$ to $0^\circ C$                 | $V_{TH} - 3.5\%$    |                   | $V_{TH} + 3.5\%$ |         |
| Hysteresis                                              | $V_{HYST}$    |                                                    |                     | $3 \times V_{TH}$ |                  | mV      |
| $V_{CC}$ to Reset Delay                                 | $t_{RD}$      | $V_{CC}$ falling at $1mV/\mu s$                    |                     | 100               |                  | $\mu s$ |
| Reset Timeout Period                                    | $t_{RP}$      | $C_{SRT} = 1500pF$                                 | 3.35                | 4.375             | 5.40             | ms      |
|                                                         |               | $C_{SRT} = 0$                                      |                     | 0.275             |                  |         |
| $V_{SRT}$ Ramp Current                                  | $I_{RAMP}$    | $V_{SRT} = 0$ to $0.65V$ ; $V_{CC} = 1.6V$ to $5V$ |                     | 240               |                  | nA      |
| $V_{SRT}$ Ramp Threshold                                | $V_{TH-RAMP}$ | $V_{CC} = 1.6V$ to $5V$ ( $V_{RAMP}$ rising)       |                     | 0.65              |                  | V       |
| RAMP Threshold Hysteresis                               |               | $V_{RAMP}$ falling threshold                       |                     | 33                |                  | mV      |
| $\overline{RESET}$ Output Voltage LOW                   | $V_{OL}$      | $V_{CC} \geq 1.0V$ , $I_{SINK} = 50\mu A$          |                     |                   | 0.3              | V       |
|                                                         |               | $V_{CC} \geq 2.7V$ , $I_{SINK} = 1.2mA$            |                     |                   | 0.3              |         |
|                                                         |               | $V_{CC} \geq 4.5V$ , $I_{SINK} = 3.2mA$            |                     |                   | 0.4              |         |
| $\overline{RESET}$ Output Voltage HIGH, (Push-Pull)     | $V_{OH}$      | $V_{CC} \geq 1.8V$ , $I_{SOURCE} = 200\mu A$       | $0.8 \times V_{CC}$ |                   |                  | V       |
|                                                         |               | $V_{CC} \geq 2.25V$ , $I_{SOURCE} = 500\mu A$      | $0.8 \times V_{CC}$ |                   |                  |         |
|                                                         |               | $V_{CC} \geq 4.5V$ , $I_{SOURCE} = 800\mu A$       | $0.8 \times V_{CC}$ |                   |                  |         |
| $\overline{RESET}$ Output Leakage Current, (Open-Drain) | $I_{LKG}$     | $V_{CC} > V_{TH}$ , reset not asserted             |                     |                   | 1.0              | $\mu A$ |
| RESET Output Voltage HIGH                               | $V_{OH}$      | $V_{CC} \geq 1.0V$ , $I_{SOURCE} = 1\mu A$         | $0.8 \times V_{CC}$ |                   |                  | V       |
|                                                         |               | $V_{CC} \geq 1.8V$ , $I_{SOURCE} = 150\mu A$       | $0.8 \times V_{CC}$ |                   |                  |         |
|                                                         |               | $V_{CC} \geq 2.7V$ , $I_{SOURCE} = 500\mu A$       | $0.8 \times V_{CC}$ |                   |                  |         |
|                                                         |               | $V_{CC} \geq 4.5V$ , $I_{SOURCE} = 800\mu A$       | $0.8 \times V_{CC}$ |                   |                  |         |

# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## ELECTRICAL CHARACTERISTICS (continued)

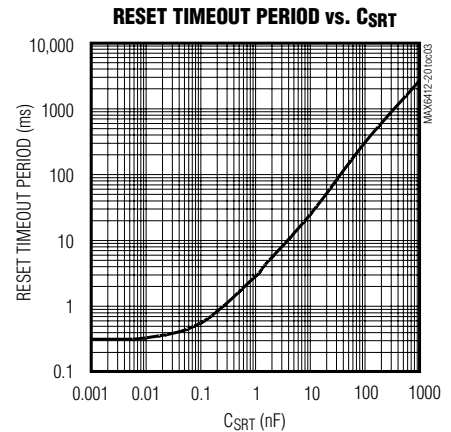
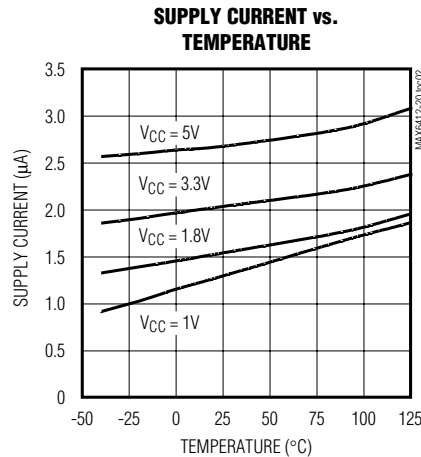
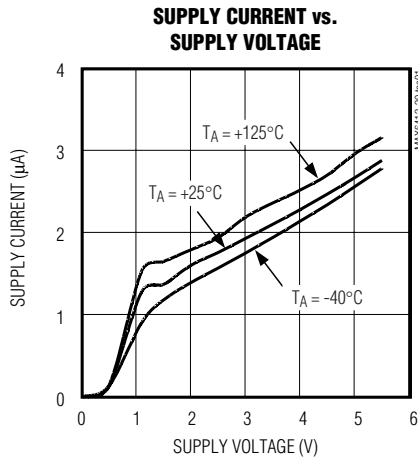
( $V_{CC} = 1V$  to  $5.5V$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise specified. Typical values are at  $V_{CC} = 5V$  and  $T_A = +25^\circ C$ .) (Note 1)

| PARAMETER                           | SYMBOL    | CONDITIONS                                   | MIN                 | TYP   | MAX                 | UNITS     |
|-------------------------------------|-----------|----------------------------------------------|---------------------|-------|---------------------|-----------|
| RESET Output Voltage LOW            | $V_{OL}$  | $V_{CC} \geq 1.8V$ , $I_{SINK} = 500\mu A$   |                     |       | 0.3                 | V         |
|                                     |           | $V_{CC} \geq 2.7V$ , $I_{SINK} = 1.2mA$      |                     |       | 0.3                 |           |
|                                     |           | $V_{CC} \geq 4.5V$ , $I_{SINK} = 3.2mA$      |                     |       | 0.4                 |           |
| RESET IN Leakage Current            |           |                                              |                     |       | 10                  | nA        |
| RESET IN Threshold                  | $V_{RST}$ | $V_{RST}$ falling, $V_{CC} = 1.6V$ to $5.0V$ | 1.212               | 1.263 | 1.313               | V         |
| RESET IN Hysteresis                 |           |                                              |                     | 25    |                     | mV        |
| $\overline{MR}$ Input               | $V_{IL}$  | $V_{CC} > 4.0V$                              |                     |       | 0.8                 | V         |
|                                     | $V_{IH}$  |                                              | 2.4                 |       |                     |           |
|                                     | $V_{IL}$  | $V_{CC} < 4.0V$                              |                     |       | $0.3 \times V_{CC}$ |           |
|                                     | $V_{IH}$  |                                              | $0.7 \times V_{CC}$ |       |                     |           |
| $\overline{MR}$ Minimum Pulse Width |           |                                              | 1                   |       |                     | $\mu s$   |
| $\overline{MR}$ Glitch Rejection    |           |                                              |                     | 75    |                     | ns        |
| $\overline{MR}$ to RESET Delay      |           |                                              |                     | 50    |                     | $\mu s$   |
| $\overline{MR}$ Pullup Resistance   |           | Pull up to $V_{CC}$                          | 12                  | 20    | 28                  | $k\Omega$ |

**Note 1:** Devices production tested at  $+25^\circ C$ . Over temperature limits are guaranteed by design.

## Typical Operating Characteristics

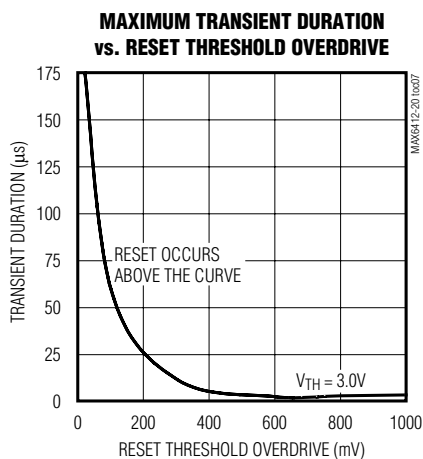
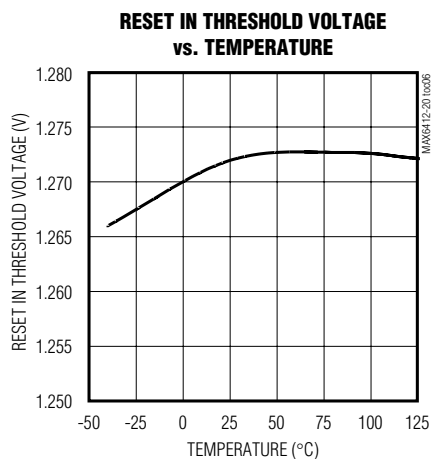
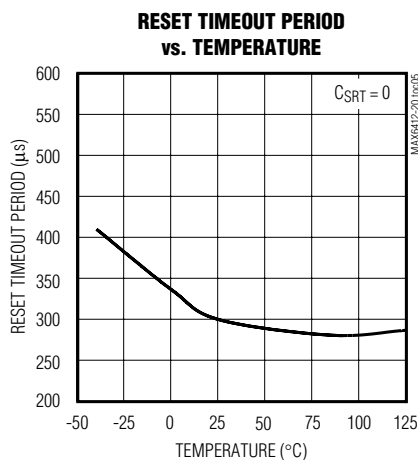
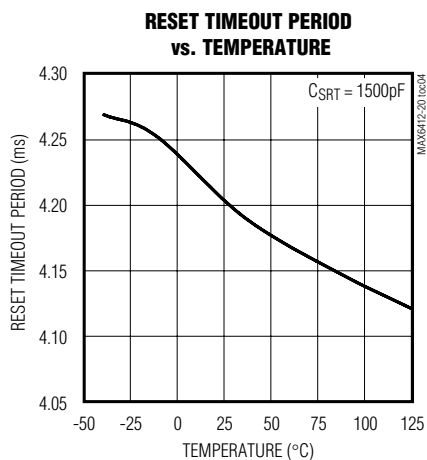
( $V_{CC} = 5V$ ,  $C_{SRT} = 1500pF$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## Typical Operating Characteristics (continued)

( $V_{CC} = 5V$ ,  $C_{SRT} = 1500pF$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## Pin Description

| PIN                             |                                 |                                 | NAME                      | FUNCTION                                                                                                                                                                                                                                                                                                                                        |
|---------------------------------|---------------------------------|---------------------------------|---------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| MAX6412/<br>MAX6413/<br>MAX6414 | MAX6415/<br>MAX6416/<br>MAX6417 | MAX6418/<br>MAX6419/<br>MAX6420 |                           |                                                                                                                                                                                                                                                                                                                                                 |
| 1                               | 1                               | 1                               | $\overline{\text{RESET}}$ | $\overline{\text{RESET}}$ changes from high to low whenever $V_{CC}$ or RESET IN drops below the selected reset threshold voltage ( $V_{TH}$ or $V_{RESET\ IN}$ , respectively) or manual reset is pulled low. $\overline{\text{RESET}}$ remains low for the reset timeout period after all reset conditions are deasserted and then goes high. |
|                                 |                                 |                                 | RESET                     | RESET changes from low to high whenever the $V_{CC}$ or RESET IN drops below the selected reset threshold voltage ( $V_{TH}$ or $V_{RESET\ IN}$ ) or manual reset is pulled low. RESET remains high for the reset timeout period after all reset conditions are deasserted and then goes low.                                                   |
| 2                               | 2                               | 2                               | GND                       | Ground                                                                                                                                                                                                                                                                                                                                          |
| —                               | 3                               | 3                               | RESET IN                  | Reset Input. High-impedance input to the adjustable reset comparator. Connect RESET IN to the center point of an external resistor-divider network to set the threshold of the externally monitored voltage. See <i>Reset Threshold</i> section.                                                                                                |
| 3                               | —                               | —                               | $\overline{\text{MR}}$    | Manual Reset Input. Pull this pin low to manually reset the device. Reset remains asserted for the reset timeout period after $\overline{\text{MR}}$ is released.                                                                                                                                                                               |
| 4                               | 4                               | 4                               | SRT                       | Set Reset Timeout Input. Connect a capacitor between SRT and ground to set the timeout period. Determine the period as follows:<br>$t_{RP} = (2.73 \times 10^6) \times C_{SRT} + 275\mu s$ with $t_{RP}$ in seconds and $C_{SRT}$ in Farads.                                                                                                    |
| 5                               | 5                               | 5                               | $V_{CC}$                  | Supply Voltage and Input for Fixed-Threshold $V_{CC}$ Monitor                                                                                                                                                                                                                                                                                   |

## Detailed Description

The MAX6412–MAX6420 low-power microprocessor ( $\mu$ P) supervisory circuits provide maximum adjustability for supply-voltage monitoring and reset functionality. In addition, the MAX6412–MAX6420 reset timeout period is adjustable using an external capacitor.

The MAX6412/MAX6413/MAX6414 have factory-trimmed reset threshold voltages in approximately 100mV increments from 1.575V to 5.0V with a manual reset input. The MAX6415/MAX6416/MAX6417 contain a reset threshold that can be adjusted to any voltage above 1.26V using external resistors. The MAX6418/MAX6419/MAX6420 offer both a factory-trimmed reset threshold and an adjustable reset threshold input for dual-voltage monitoring.

A reset signal is asserted when  $V_{CC}$  and/or RESET IN falls below the preset values or when  $\overline{\text{MR}}$  is asserted. The reset remains asserted for an externally programmed interval after  $V_{CC}$  and/or RESET IN has risen above the reset threshold or  $\overline{\text{MR}}$  is deasserted.

### Reset Output

The reset output is typically connected to the reset input of a  $\mu$ P. A  $\mu$ P's reset input starts or restarts the  $\mu$ P

in a known state. The MAX6412–MAX6420  $\mu$ P supervisory circuits provide the reset logic to prevent code-execution errors during power-up, power-down, and brownout conditions (see *Typical Operating Circuit*).

For the MAX6413, MAX6416, and MAX6419, RESET changes from low to high whenever  $V_{CC}$  or RESET IN drops below the reset threshold voltages. Once RESET IN and  $V_{CC}$  exceed their respective reset threshold voltage(s), RESET remains high for the reset timeout period, then goes low.

On power-up, once  $V_{CC}$  reaches 1V, RESET is guaranteed to be a logic high. For applications requiring valid reset logic when  $V_{CC}$  is less than 1V, see the section *Ensuring a Valid RESET/RESET Output Down to  $V_{CC} = 0$* .

The active-low  $\overline{\text{RESET}}$  output of the remaining supervisors is the inverse of the MAX6413, MAX6416, and MAX6419 active-high RESET output and is guaranteed valid for  $V_{CC} \geq 1V$ .

### Reset Threshold

The MAX6415–MAX6420 monitor the voltage on RESET IN with an external resistor voltage-divider (Figure 1).

## Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

Use the following formula to calculate the externally monitored voltage ( $V_{MON\_TH}$ ):

$$V_{MON\_TH} = V_{RST} \times (R1 + R2) / R2$$

where  $V_{MON\_TH}$  is the desired reset threshold voltage and  $V_{RST}$  is the reset input threshold (1.26V). Resistors R1 and R2 can have very high values to minimize current consumption due to low leakage currents. Set R2 to some conveniently high value (1M $\Omega$ , for example) and calculate R1 based on the desired monitored voltage, using the following formula:

$$R1 = R2 \times (V_{MON\_TH} / V_{RST} - 1) (\Omega)$$

### Manual Reset Input (MAX6412/MAX6413/MAX6414)

Many  $\mu$ P based products require manual reset capability, allowing the operator, a technician, or external logic circuitry to initiate a reset. A logic low on  $\overline{MR}$  asserts reset. Reset remains asserted while  $\overline{MR}$  is low and for the reset timeout period after  $\overline{MR}$  returns high.

The  $\overline{MR}$  has an internal 20k $\Omega$  pullup resistor so it can be left open if not used. Connect a normally open momentary switch from  $\overline{MR}$  to ground to create a manual reset function (external debounce circuitry is not required for long reset timeout periods).

A manual reset option can easily be implemented with the MAX6415-MAX6420 by connecting a normally open momentary switch in parallel with R2 (Figure 2). When the switch is closed, the voltage on RESET IN goes to zero, initiating a reset. Similar to the MAX6412/MAX6413/MAX6414 manual reset, reset remains asserted while the switch is closed and for the reset timeout period after the switch is opened.

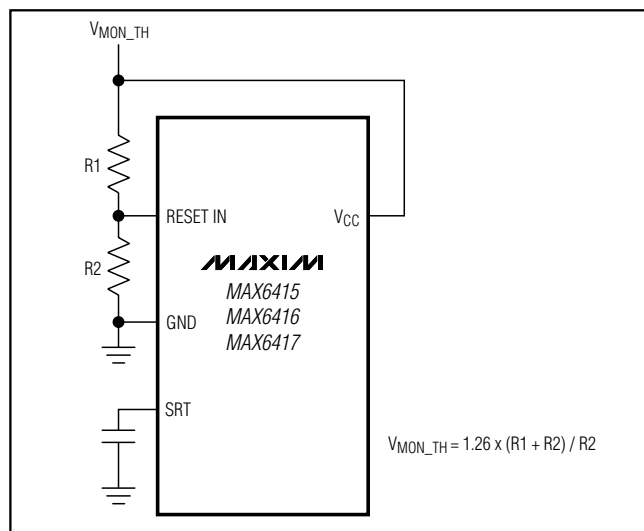


Figure 1. Calculating the Monitored Threshold Voltage ( $V_{MON\_TH}$ )

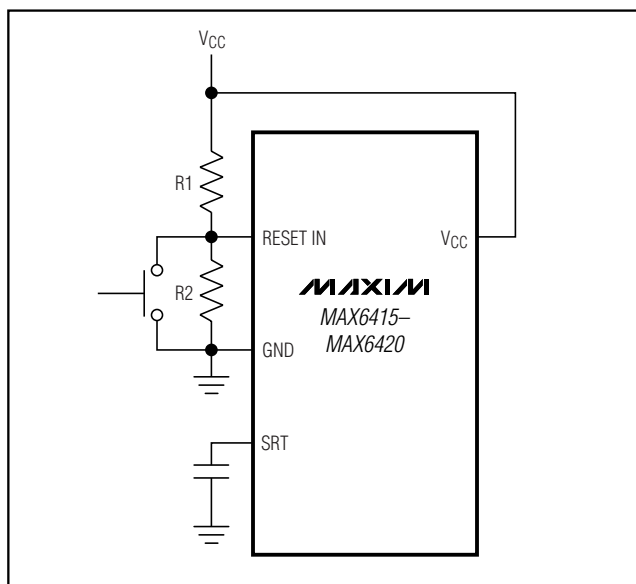


Figure 2. Adding an External Manual Reset Function to the MAX6415-MAX6420

### Monitoring Voltages Other than VCC (MAX6415/MAX6416/MAX6417)

The MAX6415/MAX6416/MAX6417 contain an adjustable reset threshold input. These devices can be used to monitor voltages other than VCC. Calculate  $V_{MON\_TH}$  as shown in the *Reset Threshold* section. (See Figure 3.)

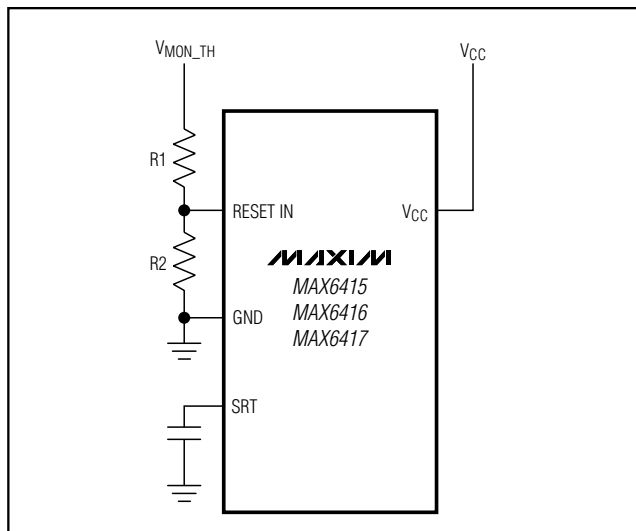


Figure 3. Monitoring External Voltages

**MAX6412-MAX6420**



The MAX6418/MAX6419/MAX6420 contain both factory-trimmed threshold voltages and an adjustable reset threshold input, allowing the monitoring of two voltages, VCC and V<sub>MON\_TH</sub> (see Figure 4). Reset is asserted when either of the voltages falls below its respective threshold voltage.

### Selecting a Reset Capacitor

where  $t_{BP}$  is in seconds and  $C_{SPT}$  is in Farads

### **Operating as a Voltage Detector**

The MAX6412–MAX6420 can be operated in a voltage detector mode by floating the SRT pin. The reset delay times for V<sub>CC</sub> rising above or falling below the threshold are not significantly different. The reset output is deasserted smoothly without false pulses.

## Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

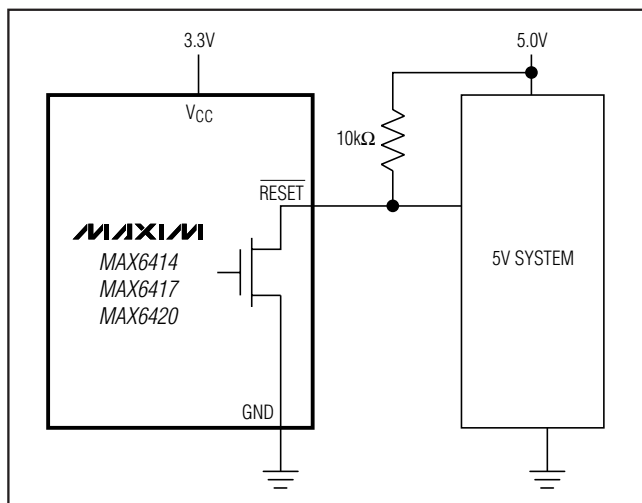


Figure 5. MAX6414/MAX6417/MAX6420 Open-Drain  $\overline{\text{RESET}}$  Output Allows use with Multiple Supplies

### Interfacing to Other Voltages for Logic Compatibility

The open-drain outputs of the MAX6414/MAX6417/MAX6420 can be used to interface to  $\mu$ Ps with other logic levels. As shown in Figure 5, the open-drain output can be connected to voltages from 0 to 5.5V. This allows for easy logic compatibility to various microprocessors.

### Negative-Going $V_{CC}$ Transients

In addition to issuing a reset to the  $\mu$ P during power-up, power-down, and brownout conditions, these supervisors are relatively immune to short-duration negative-going transients (glitches). The Maximum Transient Duration vs. Reset Threshold Overdrive graph in the *Typical Operating Characteristics* shows this relationship.

The area below the curve of the graph is the region in which these devices typically do not generate a reset pulse. This graph was generated using a negative-going pulse applied to  $V_{CC}$ , starting above the actual reset threshold ( $V_{TH}$ ) and ending below it by the magnitude indicated (reset-threshold overdrive). As the magnitude of the transient decreases (farther below the reset threshold), the maximum allowable pulse width decreases. Typically, a  $V_{CC}$  transient that goes 100mV below the reset threshold and lasts 50 $\mu$ s or less will not cause a reset pulse to be issued.

### Ensuring a Valid $\overline{\text{RESET}}$ or $\text{RESET}$ Down to $V_{CC} = 0$

When  $V_{CC}$  falls below 1V,  $\overline{\text{RESET}}$ / $\text{RESET}$  current sinking (sourcing) capabilities decline drastically. In the case of the MAX6412, MAX6415, and MAX6418, high-

impedance CMOS-logic inputs connected to  $\overline{\text{RESET}}$  can drift to undetermined voltages. This presents no problems in most applications, since most  $\mu$ Ps and other circuitry do not operate with  $V_{CC}$  below 1V.

In those applications where  $\overline{\text{RESET}}$  must be valid down to 0, adding a pulldown resistor between  $\overline{\text{RESET}}$  and ground sinks any stray leakage currents, holding  $\overline{\text{RESET}}$  low (Figure 6). The value of the pulldown resistor is not critical; 100k $\Omega$  is large enough not to load  $\overline{\text{RESET}}$  and small enough to pull  $\overline{\text{RESET}}$  to ground. For applications using the MAX6413, MAX6416, and MAX6419, a 100k $\Omega$  pullup resistor between  $\text{RESET}$  and  $V_{CC}$  will hold  $\text{RESET}$  high when  $V_{CC}$  falls below 1V (Figure 7). Open-drain  $\overline{\text{RESET}}$  versions are not recommended for applications requiring valid logic for  $V_{CC}$  down to 0.

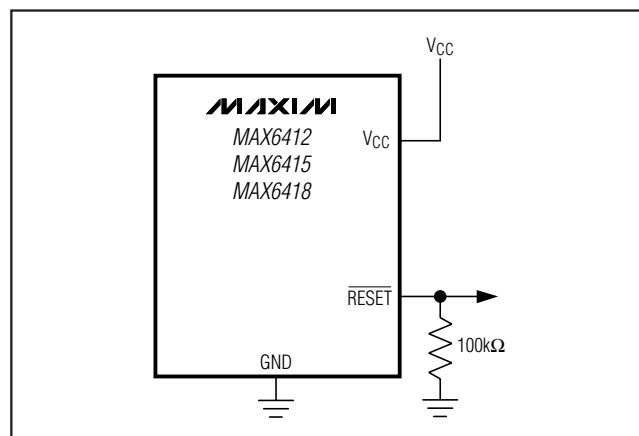


Figure 6. Ensuring  $\overline{\text{RESET}}$  Valid to  $V_{CC} = 0$

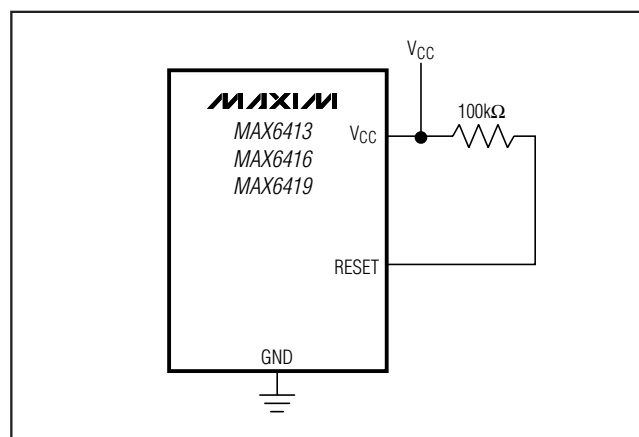


Figure 7. Ensuring  $\text{RESET}$  Valid to  $V_{CC} = 0$

## Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

### Layout Consideration

SRT is a precise current source. When developing the layout for the application, be careful to minimize board capacitance and leakage currents around this pin. Traces connected to SRT should be kept as short as possible. Traces carrying high-speed digital signals and traces with large voltage potentials should be routed as far from SRT as possible. Leakage current and stray capacitance (e.g., a scope probe) at this pin could cause errors in the reset timeout period. When evaluating these parts, use clean prototype boards to ensure accurate reset periods.

RESET IN is a high-impedance input, which is typically driven by a high-impedance resistor-divider network (e.g.,  $1\text{M}\Omega$  to  $10\text{M}\Omega$ ). Minimize coupling to transient signals by keeping the connections to this input short. Any DC leakage current at RESET IN (e.g., a scope probe) causes errors in the programmed reset threshold.

### Chip Information

TRANSISTOR COUNT: 325

PROCESS: BiCMOS

Table 1. Reset Voltages Suffix Table

| SUFFIX | MIN   | TYP   | MAX   |
|--------|-------|-------|-------|
| 16     | 1.536 | 1.575 | 1.614 |
| 17     | 1.623 | 1.665 | 1.707 |
| 18     | 1.755 | 1.800 | 1.845 |
| 19     | 1.853 | 1.900 | 1.948 |
| 20     | 1.950 | 2.000 | 2.050 |
| 21     | 2.048 | 2.100 | 2.153 |
| 22     | 2.133 | 2.188 | 2.243 |
| 23     | 2.313 | 2.313 | 2.371 |
| 24     | 2.340 | 2.400 | 2.460 |
| 25     | 2.438 | 2.500 | 2.563 |
| 26     | 2.559 | 2.625 | 2.691 |
| 27     | 2.633 | 2.700 | 2.768 |
| 28     | 2.730 | 2.800 | 2.870 |
| 29     | 2.852 | 2.925 | 2.998 |
| 30     | 2.925 | 3.000 | 3.075 |
| 31     | 2.998 | 3.075 | 3.152 |
| 32     | 3.120 | 3.200 | 3.280 |
| 33     | 3.218 | 3.300 | 3.383 |
| 34     | 3.315 | 3.400 | 3.485 |
| 35     | 3.413 | 3.500 | 3.558 |
| 36     | 3.510 | 3.600 | 3.690 |
| 37     | 3.608 | 3.700 | 3.793 |
| 38     | 3.705 | 3.800 | 3.895 |
| 39     | 3.803 | 3.900 | 3.998 |
| 40     | 3.900 | 4.000 | 4.100 |
| 41     | 3.998 | 4.100 | 4.203 |
| 42     | 4.095 | 4.200 | 4.305 |
| 43     | 4.193 | 4.300 | 4.408 |
| 44     | 4.266 | 4.375 | 4.484 |
| 45     | 4.388 | 4.500 | 4.613 |
| 46     | 4.509 | 4.625 | 4.741 |
| 47     | 4.583 | 4.700 | 4.818 |
| 48     | 4.680 | 4.800 | 4.920 |
| 49     | 4.778 | 4.900 | 5.023 |
| 50     | 4.875 | 5.000 | 5.125 |

# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

**Standard Versions Table**

| PART*         | TOP MARK |
|---------------|----------|
| MAX6412UK16-T | ADVY     |
| MAX6412UK22-T | ADWA     |
| MAX6412UK26-T | ADWC     |
| MAX6412UK29-T | ADWD     |
| MAX6412UK46-T | ADWG     |
| MAX6413UK16-T | ADWI     |
| MAX6413UK22-T | ADWK     |
| MAX6413UK26-T | ADWM     |
| MAX6413UK29-T | ADWN     |
| MAX6413UK46-T | ADWQ     |
| MAX6414UK16-T | ADWS     |
| MAX6414UK22-T | ADWU     |
| MAX6414UK26-T | ADWW     |
| MAX6414UK29-T | ADWX     |
| MAX6414UK46-T | ADXA     |
| MAX6415UK-T   | ADZO     |
| MAX6416UK-T   | ADZP     |
| MAX6417UK-T   | ADZQ     |
| MAX6418UK16-T | ADYG     |
| MAX6418UK22-T | ADYI     |
| MAX6418UK26-T | ADYK     |
| MAX6418UK29-T | ADYL     |
| MAX6418UK46-T | ADYO     |
| MAX6419UK16-T | ADYQ     |
| MAX6419UK22-T | ADYS     |
| MAX6419UK26-T | ADYU     |
| MAX6419UK29-T | ADYV     |
| MAX6419UK46-T | ADYY     |
| MAX6420UK16-T | ADZA     |
| MAX6420UK22-T | ADZC     |
| MAX6420UK26-T | ADZE     |
| MAX6420UK29-T | ADZF     |
| MAX6420UK46-T | ADZI     |

\*Sample Stock is generally held on all standard versions.  
Contact factory for availability of nonstandard versions.

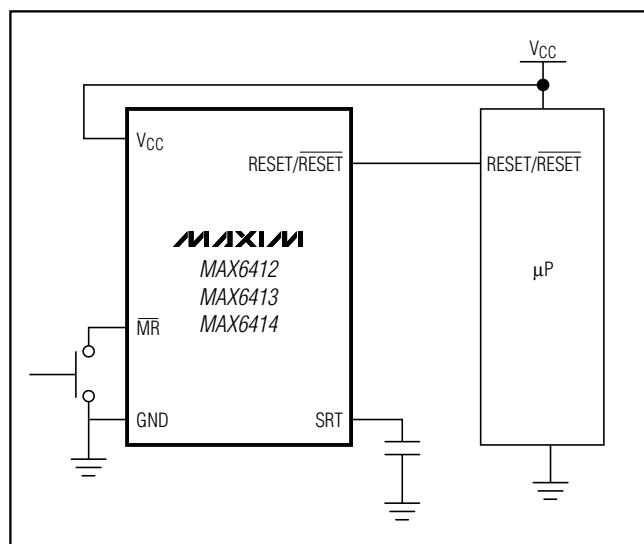
# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## Selector Guide

| PART    | FIXED<br>$V_{TH}$ | MANUAL<br>RESET | RESET IN | PUSH-PULL<br>RESET | PUSH-PULL<br>RESET | OPEN-DRAIN<br>RESET |
|---------|-------------------|-----------------|----------|--------------------|--------------------|---------------------|
| MAX6412 | ✓                 | ✓               | —        | ✓                  | —                  | —                   |
| MAX6413 | ✓                 | ✓               | —        | —                  | ✓                  | —                   |
| MAX6414 | ✓                 | ✓               | —        | —                  | —                  | ✓                   |
| MAX6415 | —                 | —               | ✓        | ✓                  | —                  | —                   |
| MAX6416 | —                 | —               | ✓        | —                  | ✓                  | —                   |
| MAX6417 | —                 | —               | ✓        | —                  | —                  | ✓                   |
| MAX6418 | ✓                 | —               | ✓        | ✓                  | —                  | —                   |
| MAX6419 | ✓                 | —               | ✓        | —                  | ✓                  | —                   |
| MAX6420 | ✓                 | —               | ✓        | —                  | —                  | ✓                   |

**MAX6412-MAX6420**

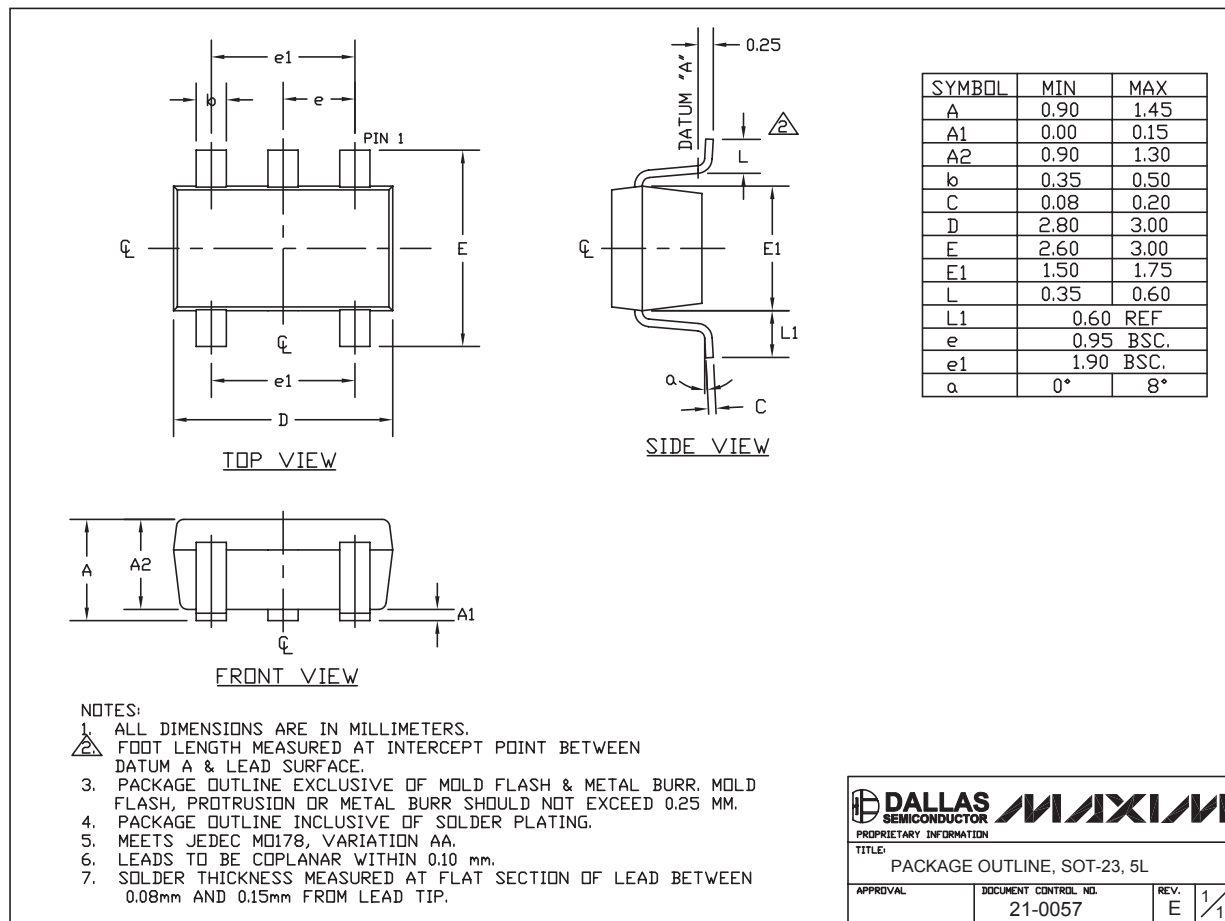
## Typical Operating Circuit



# Low-Power, Single/Dual-Voltage $\mu$ P Reset Circuits with Capacitor-Adjustable Reset Timeout Delay

## Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)



SOT-23 5L EPS

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12 Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600