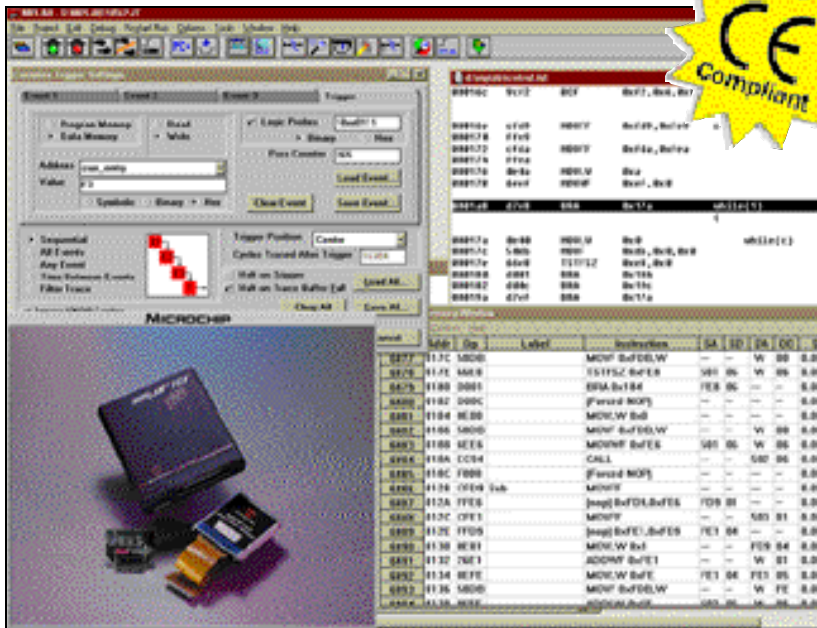


# MPLAB® ICE 2000 In-Circuit Emulator



## Features:

- High-performance PC-based development system for PICmicro MCUs
- Includes MPLAB IDE
- Assembly and C source level debugging
- Real-time in-circuit emulation to maximum speed of PICmicro MCUs
- Program memory emulation and memory mapping capability up to 64K words
- Real-time trace with up to 32K deep by 128-bit wide buffer
- Low voltage emulation (as low as 2.0 volts)
- Unlimited software breakpoints
- Trigger/break/trace on program address and data; internal register address and data; eight external inputs; bus cycle type
- Complex breakpoints with up to four levels of advanced trigger features, including sequential events, AND/OR events, filtered trace, time between two events, and pass counts
- External trigger input and output allows logic analyzer/scope interface
- Time-stamp trace feature
- Software programmable processor clock (32 kHz to 40 MHz)
- Code coverage
- Parallel port (printer) interface
- Supports all PICmicro package types, including all through-hole and surface-mount packages
- Interchangeable processor modules

## MPLAB ICE 2000 is a high-performance, real-time In-Circuit Emulator.

MPLAB ICE 2000 is Microchip's Universal In-Circuit Emulator (ICE) for the PICmicro® microcontrollers (MCU). Designed with the user requirements in mind, the MPLAB ICE 2000 system is small, portable and light weight, and offers improved performance and value. For quick hook-up to portable or desktop PCs, MPLAB ICE 2000 easily connects to the parallel (printer) port.

Interchangeable processor modules allow the system to be easily configured to emulate different processors. This modular system consists of an emulator pod, a processor module, a device adapter, and a translation socket. Also included is Microchip's MPLAB Integrated Development Environment (IDE) featuring MPLAB Editor, MPASM™ Universal Assembler, MPLAB SIM Software Simulator, and MPLAB Project Manager with built-in support for high-level languages that supports the Common Object Description format (i.e., MPASM, MPLAB C17, and MPLAB C18).

MPLAB ICE 2000 is a full-featured emulator system providing full-speed emulation, low-voltage operation, 32K by 128-bit trace, and unlimited breakpoints. Complex triggering of the MPLAB ICE 2000 provides sophisticated trace analysis and precision breakpoints. The trace analyzer captures real-time execution addresses, opcodes, and read/writes of external data. It also traces all file register RAM usage showing internal addresses and data values, as well as all accesses to special function registers, including I/O, timers, and peripherals. Triggers and breakpoints can be set on single events, multiple events, and sequences of events. The MPLAB ICE 2000 analyzer is fully transparent and does not require halting the processor to view the trace. In addition, MPLAB ICE 2000 supports code coverage profiling on program memory accesses.



# MPLAB<sup>®</sup>ICE 2000

## Ordering Information:

ICE 2000 System requires three components to be ordered (Emulator Pod, Processor Module & Device adapter) based on the specific PICmicro<sup>®</sup> microcontroller (transition socket is optional based on the package used). See the *Microchip Development Systems Ordering Guide* (DS30177) or [www.microchip.com](http://www.microchip.com) for specific part numbers. To order or obtain more information, please contact the Microchip Sales office, Representative or Distributor nearest you.

## Host System Requirements:

PC with Pentium<sup>®</sup> processor or greater  
Microsoft<sup>®</sup> Windows<sup>®</sup> operating system;  
(1) Parallel Port

## Customer Support:

Microchip maintains a worldwide network of distributors, representatives, local sales offices, Field Application Engineers, and Corporate Application Engineers. Microchip's internet home page can be reached at: [www.microchip.com](http://www.microchip.com)

## System Description:

Features	MPLAB ICE 2000
Real-Time Emulation	Full Speed
Low-Voltage Emulation	2.0 to 5.5 volts
Trace Memory	32K x 128-bit
Break/Trigger on Internal Registers	Yes
Software Breakpoints	Program Address
Complex Break/Trigger	Program Address and Data; on Logic Internal Register Address and Data; Access Type; and 8 External Inputs
Logic Analyzer Trigger	1 External Input and Output
Multi-level Trigger	Yes (four levels)
Pass Counter	Yes
Delay Counter	Yes
Time Stamp	Yes
Programmable Clock	32 kHz to 40 MHz
Logic Probes	Yes
Communications	Parallel (printer) Port
Code Coverage Profiling	Yes

Development Tools from Microchip	
MPLAB <sup>®</sup> IDE	Integrated Development Environment
MPASM <sup>™</sup> Assembler	Universal PICmicro macro-assembler
MPLINK <sup>™</sup> Object Linker	Linker
MPLIB <sup>™</sup> Object Librarian	Librarian
MPLAB C17	C compiler for PIC17CXXX MCUs
MPLAB C18	C compiler for PIC18CXXX MCUs
C compilers	Sold by third-party vendors (HI-TECH, IAR, CCS)
MPLAB SIM Simulator	Software Simulator
MPLAB ICD	In-circuit Debugger
ICEPIC <sup>™</sup> Emulator	Low-cost in-circuit emulator
MPLAB ICE 2000	Full-featured modular in-circuit emulator
PICSTART <sup>®</sup> Plus Programmer	Entry-level development kit with programmer
PRO MATE <sup>®</sup> II Device Programmer	Full-featured, modular device programmer
KEELOQ <sup>®</sup> Evaluation Kit	Encoder/Decoder evaluator
KEELOQ Transponder Evaluation Kit	Transmitter/Transponder evaluator
microID <sup>™</sup> Developer's Kit	125 kHz and 13.56 MHz RFID development tools
MCP2510 CAN Developer's Kit	MCP2510 CAN evaluation/development tool
MXDEV <sup>™</sup> 1 Analog Evaluation System	Evaluation kit for MCP devices

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