



ATBTLC1000-XR/ZR

Ultra-Low Power BLE SiP Errata

ATBTLC1000XR/ZR Family Errata

The ATBTLC1000-XR/ZR family of devices that you have received conform functionally to the current device data sheet located on the Microchip website: <http://www.microchip.com/wwwproducts/en/ATBTLC1000-ZR>, except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Chip ID listed in the table below.

The errata described in this document will be addressed in future revisions of the ATBTLC1000-XR/ZR family silicon.

Table 1. ATBTLC1000-XR/ZR Family Silicon Device Identification

Part Number	Chip ID - 0x4000B000[23:0]	Silicon Rev
ATBTLC1000-XR1100A	0x2000B1	B0

Note: Bits 0 to 23 of the register with address 0x4000B000 contain the chip ID of the device.

Affected Devices

Erratum	Affected Devices
Efuse Default Value	ATBTLC1000-XR1100A SiP and ATBTLC1000-ZR110CA module

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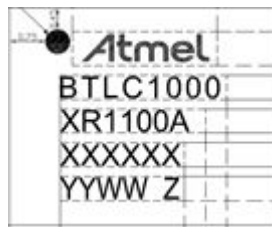
1. Efuse Default Value

UART Hardware Flow Control pin selection bits 28, 27 of Bank 5 Block 3 (UART FLOW [1], UART FLOW [0]) in eFuse memory determine the LP_GPIO pins to be used as the hardware flow control pins (RTS and CTS) of the UART interface with host MCU. For both ATBTLC1000-XR1100A SiP and ATBTLC1000-ZR110CA module, these bits must have a default value of 0b10.

This default value is being programmed in ATBTLC1000-XR1100A SiP production from date code 1832 (Inclusive). The ATBTLC1000-XR1100A SiP or ATBTLC1000-ZR110CA module before this date code will not have this default value.

For example, the date code on the ATBTLC1000-XR1100A IC package is as shown in the following figure.

Figure 1-1. Example of Date Code on Module



Legend:

XXXXXX – Wafer Lot number

YY – Year code (last 2 digits of calendar year)

WW – Week code (for example: week of January 1 is week “01”)

YYWW together forms the date code for the IC

Work around

The ATBTLC1000-XR1100A SiP or ATBTLC1000-ZR110CA module before 1832 date code to be eFused with 0b10 in customer production by following the instructions provided in [ATBTLC1000 BluSDK Example Profiles Application User's Guide](#).

Affected Silicon Revisions

B0							
X							

Note: Date code before 1832.

2. Document Revision History

Revision	Date	Section	Description
DS80000790C	12/2018	Affected Devices, Efuse Default Value	<ul style="list-style-type: none">• Added Affected Devices section• Updated the date code
DS80000790B	07/2018	Document	<ul style="list-style-type: none">• Updated Table 1• Updated Errata Details
DS80000790A	05/2018	Document	Initial revision

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