



SPECIFICATION FOR SMT OSCILLATOR

MtronPTI P/N: M2002S704

(Adtran P/N: TBD)

Effective Date: March 30, 2007

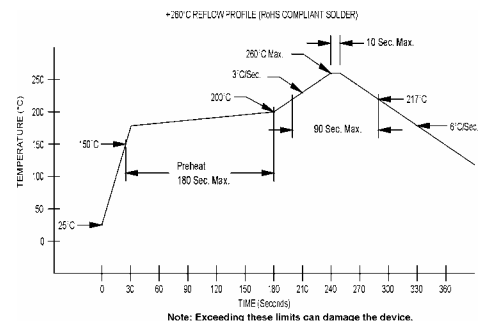
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 100.000000 MHz
2. FREQUENCY STABILITY @ +25°C: ± 25 ppm (Includes initial calibration, deviation over operating temperature, load, supply, and aging).
3. FREQUENCY VS. AGING: ± 5 ppm/year max. at +25°C.
4. OPERATING TEMPERATURE RANGE: -40°C to +85°C
5. OPERATING VOLTAGE (Vdd): 3.3 V $\pm 10\%$
6. OPERATING CURRENT: 50 mA max.
7. OUTPUT TYPE: HCMOS/TTL Compatible.
8. SYMMETRY: 45/55% ref. to $\frac{1}{2}$ Vdd.
9. RISE/FALL TIME: 2.5 nS max. ref. between 20% and 80% Vdd
10. OUTPUT LOGIC LEVELS: $V_{OL} = 0.4$ V max. $V_{OH} = 90\%$ Vdd min.
11. PERIOD JITTER (1-Sigma): 25 ps RMS max.
12. OUTPUT LOAD: 15 pF/5 TTL max.
13. START-UP TIME: 10 msec. max.
14. STANDBY FUNCTION (Pad 1): Logic "1" or "floating", clock signal output
Logic "0", output disables to high impedance state
15. STANDBY CURRENT: 15 μ A max.

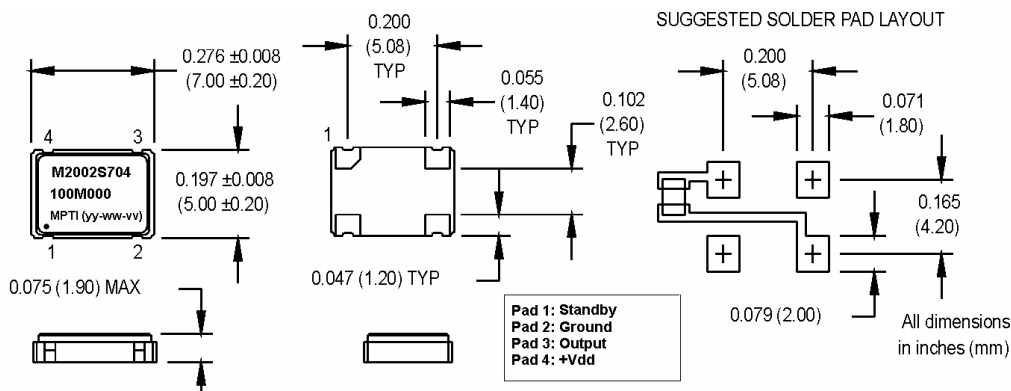
II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY: 1×10^{-8} atm cc/sec min.
4. STORAGE TEMPERATURE: -55°C to +125°C
5. SOLDERABILITY: Per EIAJ-STD-002
6. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
7. PACKAGE: 4 - pad leadless ceramic package. RoHS compliant.

Figure 1



III. DIMENSIONS:



IV. DATA SHEET REVISION TABLE:

Date	Rev.	PCN	Details of Revision
3/30/07	0	N/A	Original release.