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ELECTRONICS

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Jameco Part Number 2010253

Preferred Devices

High Voltage Switching Diode

Features

• Pb-Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage BAS19 BAS20 BAS21	V _R	120 200 250	Vdc
Repetitive Peak Reverse Voltage BAS19 BAS20 BAS21	V _{RRM}	120 200 250	Vdc
Continuous Forward Current	I _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	625	mAdc
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C
Power Dissipation (Note 1)	P _D	385	mW
Electrostatic Discharge	ESD	HM < 500	V
		MM < 400	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

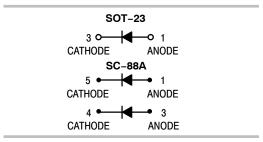
1. Mounted on FR-5 Board = 1.0 x 0.75 x 0.062 in.



ON Semiconductor®

http://onsemi.com

HIGH VOLTAGE SWITCHING DIODE



MARKING DIAGRAMS



SOT-23 (TO-236) CASE 318 STYLE 8





SC-88A (SOT-353) CASE 419A



x = P, R, or S P = BAS19LT1 R = BAS20LT1

= BAS21LT1 or BAS21DW5T1

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon the manufacturing location.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

THERMAL CHARACTERISTICS (SOT-23)

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board	P_{D}	225	mW
(Note 2) T _A = 25°C Derate above 25°C		1.8	mW/°C
Thermal Resistance Junction-to-Ambient (SOT-23)	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 3)	P _D	300	mW
T _A = 25°C Derate above 25°C		2.4	mW/°C
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS (SC-88A)

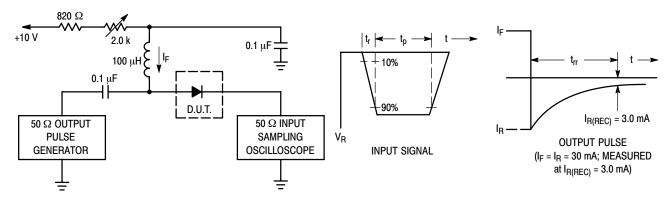
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 4)	P_{D}	385	mW
Thermal Resistance – Junction-to-Ambient Derate Above 25°C	$R_{ heta JA}$	328 3.0	°C/W mW/°C
Maximum Junction Temperature	T _{Jmax}	150	°C
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}C \ unless \ otherwise \ noted)$

Characteristic	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current	I _R			μAdc
(V _R = 100 Vdc) BAS19		_	0.1	
(V _R = 150 Vdc) BAS20		_	0.1	
(V _R = 200 Vdc) BAS21		_	0.1	
$(V_R = 100 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ BAS19		_	100	
$(V_R = 150 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ BAS20		_	100	
$(V_R = 200 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ BAS21		-	100	
Reverse Breakdown Voltage	V _(BR)			Vdc
$(I_{BR} = 100 \mu\text{Adc})$ BAS19	, ,	120	_	
$(I_{BR} = 100 \mu\text{Adc})$ BAS20		200	-	
$(I_{BR} = 100 \mu\text{Adc})$ BAS21		250	-	
Forward Voltage	V _F			Vdc
(I _F = 100 mAdc)		_	1.0	
(I _F = 200 mAdc)		-	1.25	
Diode Capacitance (V _R = 0, f = 1.0 MHz)	C _D	-	5.0	pF
Reverse Recovery Time ($I_F = I_R = 30 \text{ mAdc}$, $I_{R(REC)} = 3.0 \text{ mAdc}$, $R_L = 100$)	t _{rr}	-	50	ns

^{2.} FR-5 = $1.0 \times 0.75 \times 0.062$ in. 3. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

^{4.} Mounted on FR-5 Board = $1.0 \times 0.75 \times 0.062$ in.



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 30 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 30 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

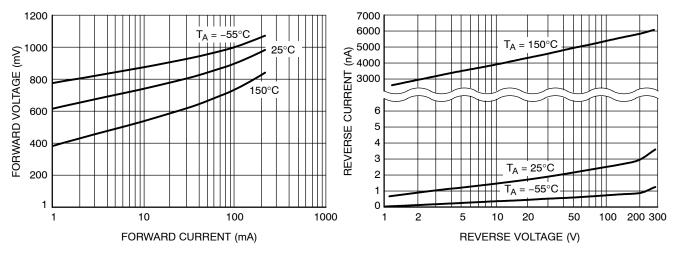


Figure 2. Forward Voltage

Figure 3. Reverse Leakage

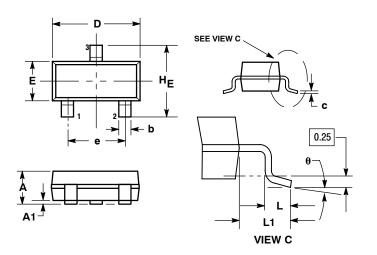
ORDERING INFORMATION

Device	Package	Shipping [†]
BAS19LT1	SOT-23	3000 / Tape & Reel
BAS19LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
BAS19LT3	SOT-23	10000 / Tape & Reel
BAS19LT3G	SOT-23 (Pb-Free)	10000 / Tape & Reel
BAS20LT1	SOT-23	3000 / Tape & Reel
BAS20LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
BAS21LT1	SOT-23	3000 / Tape & Reel
BAS21LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
BAS21LT3	SOT-23	10000 / Tape & Reel
BAS21LT3G	SOT-23 (Pb-Free)	10000 / Tape & Reel
BAS21DW5T1	SC-88A	3000 / Tape & Reel
BAS21DW5T1G	SC-88A (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN**



- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

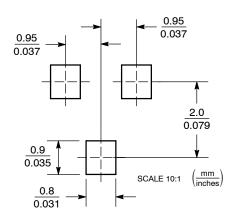
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF PASE MATERIAL BASE MATERIAL.
- 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

	М	MILLIMETERS		INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 8:

- PIN 1. ANODE 2. NO CON
 - NO CONNECTION CATHODE

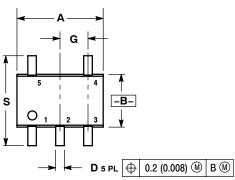
SOLDERING FOOTPRINT*

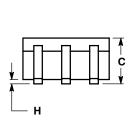


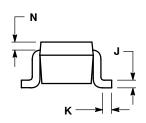
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

SC-88A, SOT-353, SC-70 CASE 419A-02 ISSUE J





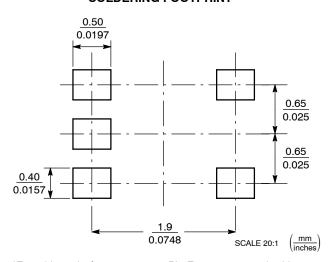


NOTES:

- 1. DIMENSIONING AND TOLERANCING
- PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 419A-01 OBSOLETE. NEW STANDARD 419A-02.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
С	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008	0.008 REF		REF
S	0.079	0.087	2.00	2.20

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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