

FSA223 — USB2.0 High-Speed (480Mbps) and Audio Switches with Negative Signal Capability

Features

- HS-USB: 4Ω Typical On Resistance
- HS-USB: 4.5pF Typical On Capacitance
- Audio: 3Ω Typical On Resistance
- -3db Bandwidth: > 720MHz
- Low Power Consumption
- Power-off Protection on Common D+/R, D-/L Ports
- Automatically Detects V_{CC} for Switch Path Selection

Applications

- Cell Phone, PDA, Digital Camera, and Notebook
- LCD Monitor, TV, and Set-Top Box

Related Resources

- Please refer to tape and reel specifications on www.fairchildsemi.com; <http://www.fairchildsemi.com/packaging>.

Description

The FSA223 is a Double-Pole, Double Throw (DPDT) multiplexer that combines a low-distortion audio and a USB2.0 High-Speed (HS) switch path. This configuration enables audio and USB data to share a common connector port. The architecture is designed to allow both audio and USB signals to swing below ground. This means a common USB and headphone jack can be used for personal media players and portable peripheral devices.

Since USB2.0 is an industry standard for shared data-path in portable devices, the FSA223 also incorporates a V_{CC} detection capability. The FSA223 includes a power-off feature to minimize current consumption when V_{CC} is not present. This power-off circuitry is available for the common D+/R, D-/L ports only. Typical applications involve switching in portables and consumer applications, such as cell phones, digital cameras, and notebooks with hubs or controllers.

IMPORTANT NOTE:

For additional performance information, please contact analogswitch@fairchildsemi.com.

Ordering Information

Part Number	Package Number	Top Mark	Package Description
FSA223L10X	MAC010A	GN	10-Lead MicroPak™, JEDEC MO-255, 1.6 x 2.1mm
FSA223MUX	MUA010A	FSA223	10-Lead MSOP, JEDEC MO-187, 3.0mm Wide
FSA223UMX	MLP010A	GP	10-Lead Quad, Ultrathin MLP, 1.4 x 1.8mm

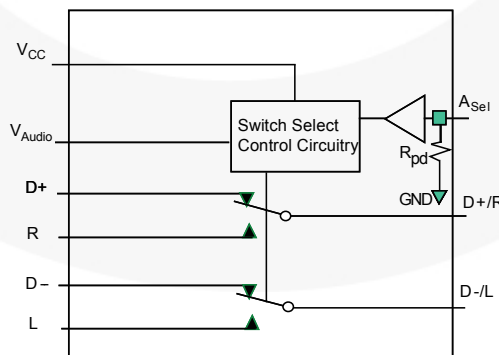


Figure 1. FSA223 Analog Symbol

Pin Configuration

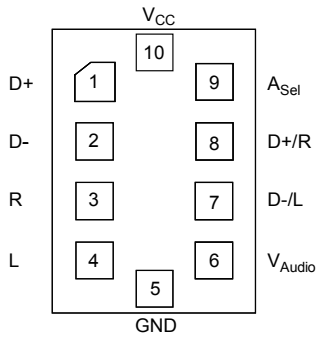


Figure 2. 10-Pin MicroPak™

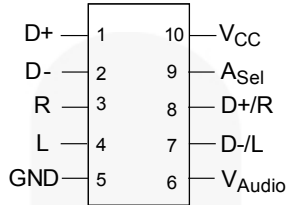


Figure 3. 10-Pin MSOP

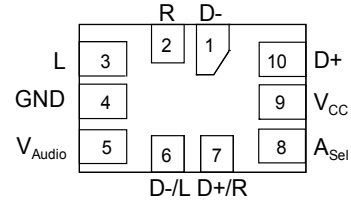


Figure 4. 10-Pin UMLP

Pin Definitions

Name	Description
V _{audio}	Power supply (audio)
V _{CC}	Power supply (USB) and auto USB switch-path select
A _{Sel}	Audio select to override auto USB detect when V _{AUDIO} supply is present
D+, D-	USB data bus input sources
R, L	Audio right and left input sources
D+/R, D-/L	USB and audio common connector ports

Truth Table

A _{Sel} ⁽¹⁾	V _{audio}	V _{CC}	L, R	D+, D-
— ⁽²⁾	—	LOW	OFF	OFF
—	LOW	HIGH	OFF	ON
LOW	HIGH	HIGH	OFF	ON
HIGH	HIGH	HIGH	ON	OFF

Note:

1. A_{Sel} - Internal resistor to GND provides auto-V_{CC} detect if there is no external connection. Forcing A_{Sel} HIGH when V_{AUDIO} is present overrides the USB path.
2. The dash (—) indicates “Don’t Care” state.

Functional Description

The FSA223 is a combined USB and audio switch that enables sharing the D+/D- lines of a USB connector with stereo audio CODEC outputs. The switch is optimized for high-speed USB signals and includes an automatic V_{CC} -detection circuit. The FSA223 detects the presence of V_{CC} and defaults to USB mode. Both the USB and audio switch paths

also handle negative signals, which eliminates the need for large coupling capacitors.

The A_{Sel} pin is internally terminated by a resistor to GND (typical value: $3M\Omega$) and results in a default USB connection. For optimal performance, V_{CC} should be connected directly to the device battery.

Application Diagram

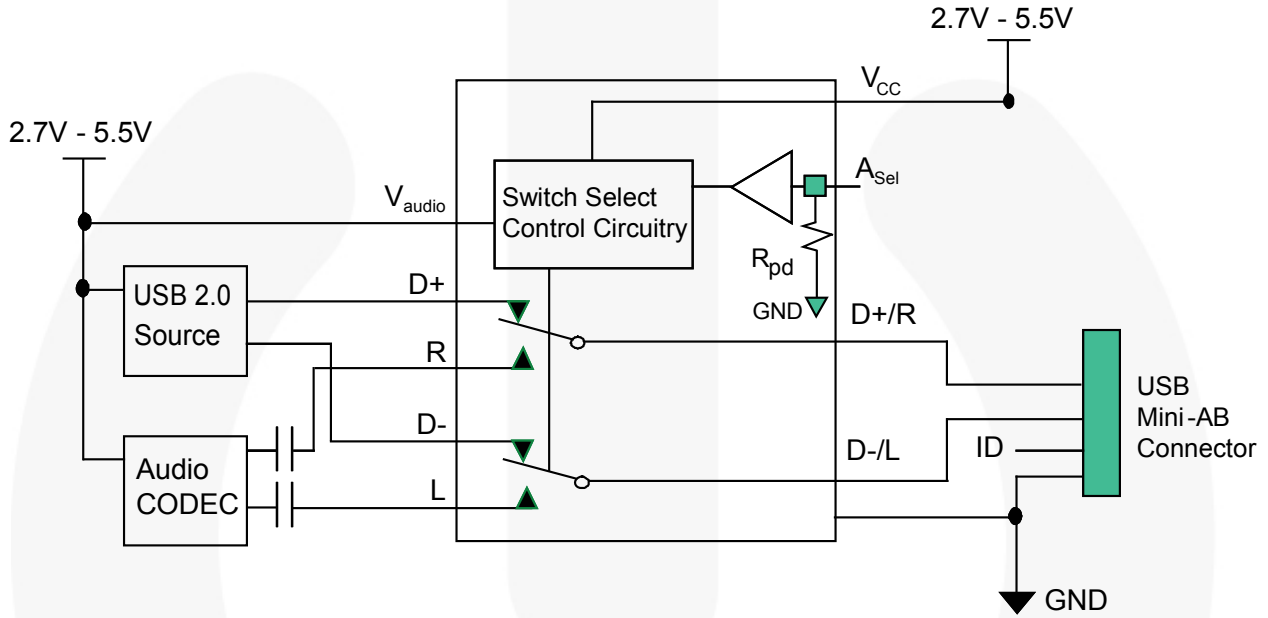


Figure 5. Typical Application

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Max.	Unit
V _{Audio}	Supply Voltage		-0.5	6.0	V
V _{CC}	Supply Voltage		-0.5	6.0	V
V _{SW}	Switch I/O Voltage ^(3,4)	R, L Pins	V _{audio} - 7.0V	V _{audio} + 0.3V	V
		D+, D-, D+/R, D-/L Pins	V _{CC} - 7.0V	V _{CC} + 0.3V	V
A _{Sel}	Control Input Voltage ⁽³⁾		-0.5	6.0	V
I _{IK}	Input Clamp Diode Current			-50	mA
I _{SW}	Switch I/O Current (Continuous)	USB		50	mA
		Audio		50	mA
I _{SWPEAK}	Peak Switch Current (Pulsed at 1ms Duration, <10% Duty Cycle)	USB		100	mA
		Audio		100	mA
T _{STG}	Storage Temperature Range		-65	+150	°C
T _J	Maximum Junction Temperature			+150	°C
T _L	Lead Temperature (Soldering, 10 seconds)			+260	°C
ESD	Human Body Model, JESD22-A114	I/O to GND		7.5	kV
		All Other Pins		3.0	
		V _{Audio} V _{CC} to GND		12.0	
	Charged Device Model, JESD22-C101				2.0

Notes:

- The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.
- V_{SW} maximum values can be exceeded ONLY if I_{SW} maximum values are observed. For example, V_{SW}=V_{CC} + 0.6V is acceptable if I_{SW} is limited externally to ≤ 50mA.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter		Min.	Max.	Units
V _{Audio}	Supply Voltage		2.7	5.5	V
V _{CC}	Supply Voltage		2.7	5.5	V
A _{Sel}	Control Input Voltage		0	V _{CC}	V
V _{SW}	Switch I/O Voltage	V _{Audio} - 6.5V		V _{Audio} - 0.3V	V
		V _{CC} - 6.5V		V _{CC}	V
T _A	Operating Temperature		-40	+85	°C
θ _{JA}	Thermal Resistance (free air)	MicroPak™		330 (estimated)	°C/W

DC Electrical Characteristics

V_{Audio} supply=2.7V and typical values are at 25°C unless otherwise specified.

Symbol	Parameter	V _{CC} (V)	Condition	T _A =- 40°C to +85°C			Unit
				Min.	Typ.	Max.	
Common Pins							
V _{IK}	Clamp Diode Voltage	3.0	I _{IK} =-18mA			-1.2	V
V _{IH}	Control Input Voltage HIGH	3.0 to 4.3 ⁽⁵⁾		1.2			
V _{IL}	Control Input Voltage LOW	3.0 to 4.3 ⁽⁵⁾				0.5	
I _{IN}	A _{SEL} Input HIGH Current	V _{audio} =4.3V V _{CC} =3.0V	V _{IN} =3.6V	-1		10	μA
			V _{IN} =0V	-1		1	
I _{OFF}	Power Off Leakage Current (Common Port Only D+/R, D-/L)	V _{audio} =V _{CC} =0V	Common Port (D+/R, D-/L) V _{SW} =0V, 5.5V			10	μA
I _{NO(OFF)}	Off-Leakage Current of Port D+, D-, R, L	3.6	D+/R, D-/L=0.3V, V _{CC} - 0.3V D+, D-, R, L=0.3V, V _{CC} - 0.3V or Floating	-50	1	50	nA
I _{NC(ON)}	On-Leakage Current of Port D+/R or D-/L	3.6	D+/R, D-/L=0.3V, V _{CC} - 0.3V D+, D-, R, L=Floating	-50	1	50	nA
R _{PD}	A _{SEL} Internal Pull-Down Resistor				3		MΩ
USB Switch Path							
	USB Analog Signal Range			0		3.6	V
R _{ONUSB}	HS Switch On Resistance ⁽⁶⁾	3.0	V _{D+/D-} =0V, 0.4V, I _{ON} =-8mA		4	6	Ω
Δ R _{ONUSB}	HS Delta R _{ON} ^(7,8)	3.0	V _{D+/D-} =0V, I _{ON} =-8mA		0.4		Ω
Audio Switch Path							
	Audio Analog Signal Range			V _{audio} - 5.5		V _{audio}	V
R _{ONAudio}	Audio Switch On Resistance ⁽⁶⁾	3.0 to 4.3 ⁽⁵⁾	V _{L/R} =-2V, 0V, 0.7V, 2.0V; I _{ON} =-26mA		3	6	Ω
Δ R _{ONAudio}	Audio Delta R _{ON} ⁽⁷⁾	3.0 to 4.3 ⁽⁵⁾	V _{L/R} =0.7V I _{ON} =-26mA		0.4		Ω
R _{FLAT(Audio)}	Audio R _{ON} Flatness ⁽⁹⁾	3.0 to 4.3 ⁽⁵⁾	V _{L/R} =-2V, 0V, 0.7V, 2.0V; I _{ON} =-26mA		1.5	2.5	Ω
Power Supply							
I _{CC(Audio)}	Quiescent Supply Current (Audio)	V _{audio} =5.5V	V _{ASEL} =0 and V _{CC} , I _{OUT} =0		4	10	μA
I _{CC(VCC)}	Quiescent Supply Current (V _{CC})	V _{CC} =5.5V	I _{OUT} =0, V _{audio} =0		12	20	μA
I _{CCT}	Increase in I _{CC} Current per Control Voltage and V _{CC}	V _{audio} =3.6V, 4.3V ⁽⁵⁾	V _{ASEL} =2.6V, V _{CC} =Floating		10	15	μA
			V _{ASEL} =1.8V, V _{CC} =Floating		14	18	

Notes:

- 4.3 V is guaranteed by characterization, not production tested.
- On resistance is determined by the voltage drop between the A and B pins at the indicated current through the switch.
- Δ R_{ON}=R_{ON} max – R_{ON} min measured at identical V_{CC}, temperature, and voltage. Worst-case signal path, audio or USB channel, is characterized.
- Guaranteed by characterization, not production tested.
- Flatness is defined as the difference between the maximum and minimum values of on resistance over the specified range of conditions.

AC Electrical Characteristics

V_{Audio} supply=2.7V unless otherwise specified.

Symbol	Parameter	V _{CC} (V)	Conditions	T _A =- 40°C to +85°C			Unit
				Min.	Typ.	Max.	
t _{ONAUDIO2}	Turn-On Time A _{SEL} to Output	3.0 to 4.3 ⁽¹⁰⁾	V _{D+/R, D-/L} =1.0V; R _L =50Ω; C _L =50pF, Figure 14, Figure 15			2	μs
t _{OFFAUDIO2}	Turn-Off Time A _{SEL} to Output	3.0 to 4.3 ⁽¹⁰⁾	V _{D+/R, D-/L} =1.0V; R _L =50Ω; C _L =50pF, Figure 14, Figure 15			2	μs
t _{PDUSB}	USB Switch Propagation Delay ⁽¹⁰⁾	3.6	R _L =50Ω; C _L =0pF Figure 17		0.25		ns
Xtalk _A	Non-Adjacent Channel Crosstalk - Audio	3.0 to 4.3 ⁽¹⁰⁾	f=20kHz; R _T =32Ω; C _L =0pF Figure 22		-110		dB
BW	-3db Bandwidth - USB	3.0 to 4.3 ⁽¹⁰⁾	R _T =50Ω, C _L =0pF, Signal 0dBm Figure 20		720		MHz
THD	Total Harmonic Distortion	3.0 to 4.3 ⁽¹⁰⁾	f=20Hz to 20 kHz; R _L =32Ω, V _{IN} =2V _{pp} , Figure 25		0.1		%

Note:

10. Guaranteed by characterization, not production tested.

USB High-Speed-Related AC Electrical Characteristics

V_{Audio} supply=2.7V unless otherwise specified.

Symbol	Parameter	V _{CC} (V)	Conditions	T _A =- 40°C to +85°C			Unit
				Min.	Typ.	Max.	
t _{SK(O)}	Channel-to-Channel Skew ⁽¹¹⁾	3.0 to 4.3 ⁽¹¹⁾	t _r =t _f =750ps (10-90%) at 240MHz C _L =0pF, R _L =50Ω Figure 18, Figure 19		35		ps
t _{SK(P)}	Skew of Opposite Transitions of the Same Output ⁽¹¹⁾	3.0 to 4.3 ⁽¹¹⁾	t _r =t _f =750ps (10-90%) at 240MHz C _L =0pF, R _L =50Ω Figure 18, Figure 19		35		
t _J	Total Jitter ⁽¹¹⁾	3.0 to 4.3 ⁽¹¹⁾	R _L =50Ω, C _L =50pF, t _r =t _f =500ps (10-90%) at 480Mbps (PRBS=2 ¹⁵ -1)		130		ps

Note:

11. Guaranteed by characterization, not production tested.

Capacitance⁽¹²⁾

V_{Audio} supply=3.0V unless otherwise specified.

Symbol	Parameter	V _{CC} (V)	Condition	T _A =- 40°C to +85°C			Unit
				Min.	Typ.	Max.	
C _{IN (ASel)}	Control Pin Input Capacitance (A _{SEL})	V _{CC} =3.0V, 4.3V	V _{Bias} =0.05V		2.0		pF
C _{ON(D+/R, D-/L)}	D+/R, D-/L (Source Port) On Capacitance	V _{CC} =3.0V, 4.3V A _{SEL} =0V (C _{ONUSB})	V _{Bias} =0.2V; f=240MHz Figure 24		4.5	6.0	pF
		V _{CC} =3.0V, 4.3V A _{SEL} =3.0V (C _{ONAudio})	V _{Bias} =0.2V; f=1MHz Figure 24		9.0		
C _{OFF(D+, D-)}	USB Input Source Off Capacitance	V _{CC} =3.0V, 4.3V A _{SEL} =3.0V	f=1MHz Figure 23		1.5		pF
C _{OFF(R/L)}	Audio Input Source Off Capacitance	V _{CC} =3.0V, 4.3V A _{SEL} =0V	f=1MHz Figure 23		3.0		pF

Note:

12. Guaranteed by characterization, not production tested.

Applications Information

In applications where V_{sw} could exceed the absolute maximum rating of $V_{CC}+0.3V$, the following recommendations help maintain low power consumption and protect the part.

The addition of the series diode in the V_{CC} supply line blocks any current that might leak back into V_{CC} for over-voltage input cases.

Because the deselected channel may no longer provide guaranteed off isolation, consider the following:

- During USB transfer, the audio amplifier should be powered down.
- During audio transfer, the USB pull-up resistor supply must be removed (as specified on page 141 of the USB 2.0 specification).

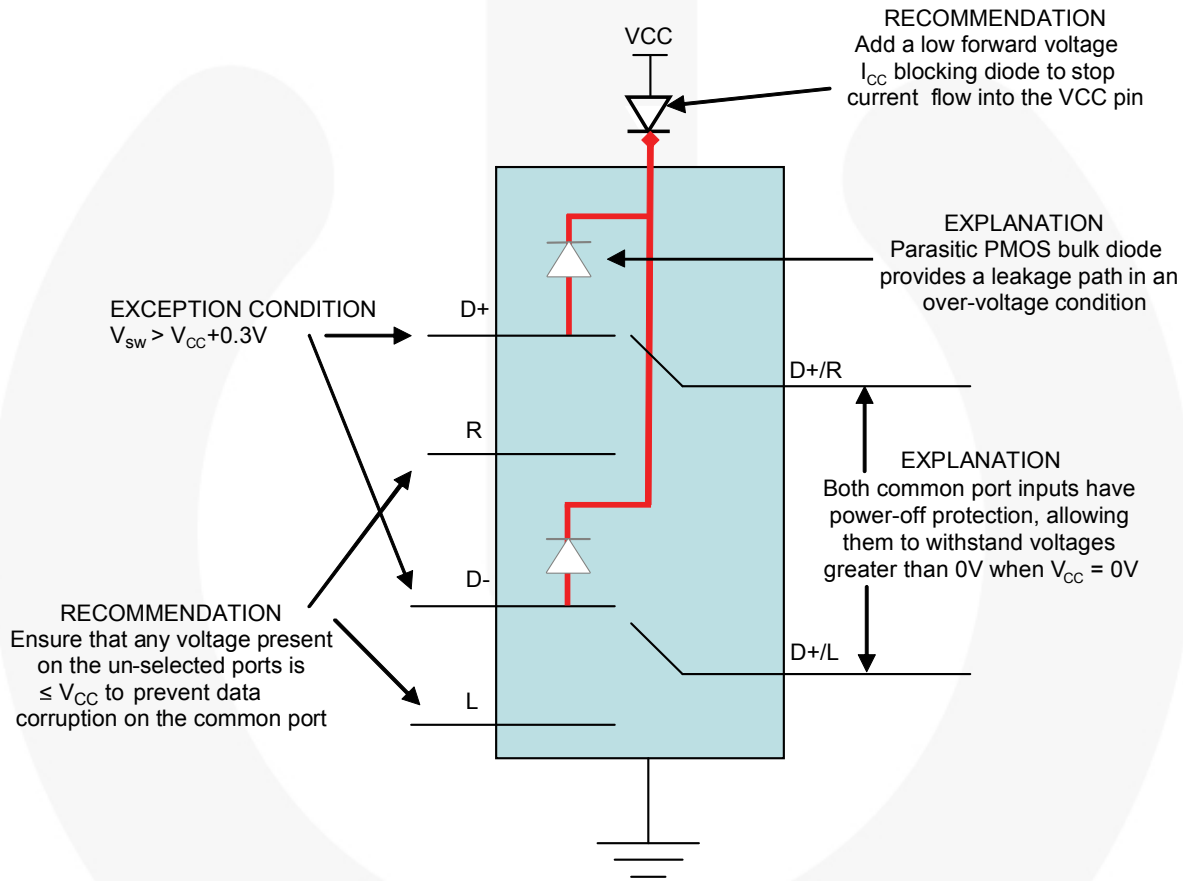


Figure 6. Application Suggestions

Typical Performance Characteristics

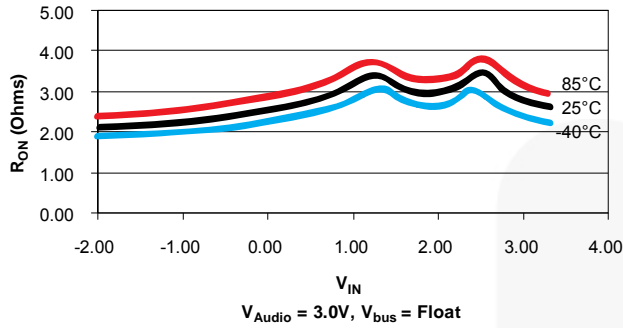


Figure 7. R_{ON} Audio, $V_{Audio}=3.0V$

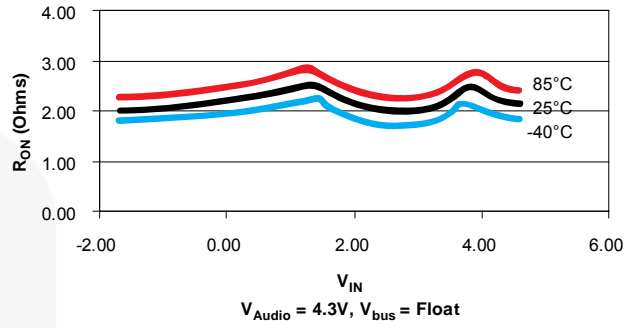


Figure 8. R_{ON} Audio, $V_{Audio}=4.3V$

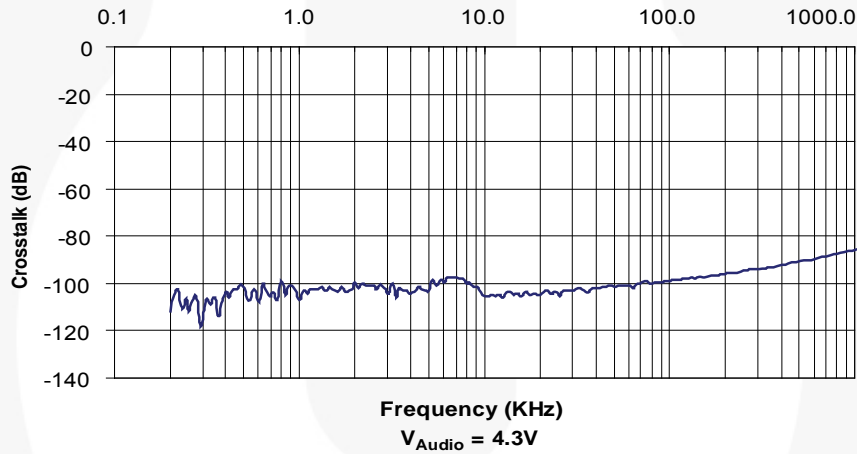


Figure 9. Crosstalk

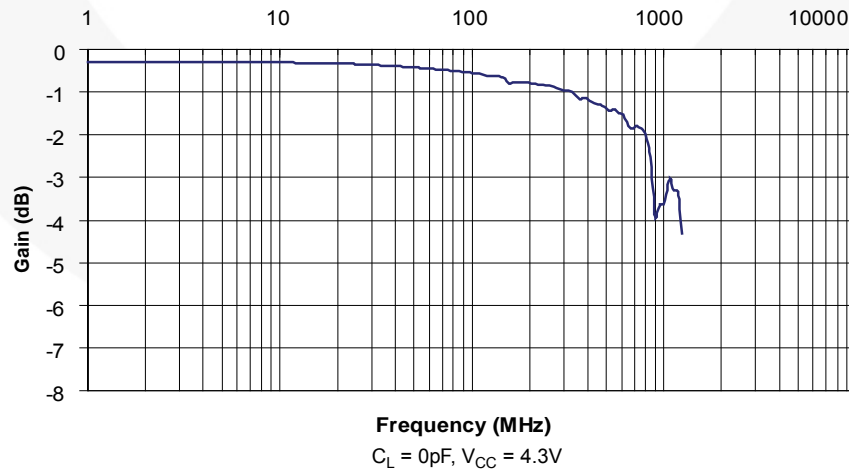


Figure 10. USB Bandwidth

Test Diagrams

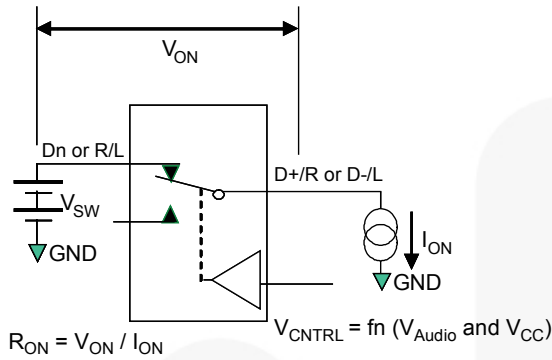


Figure 11. On Resistance

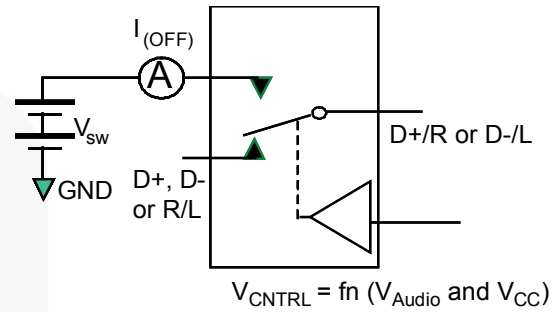


Figure 12. Off Leakage

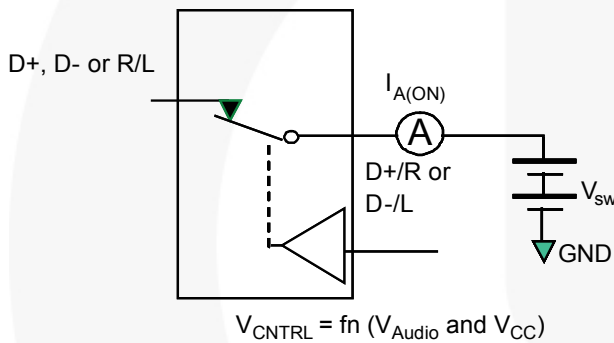
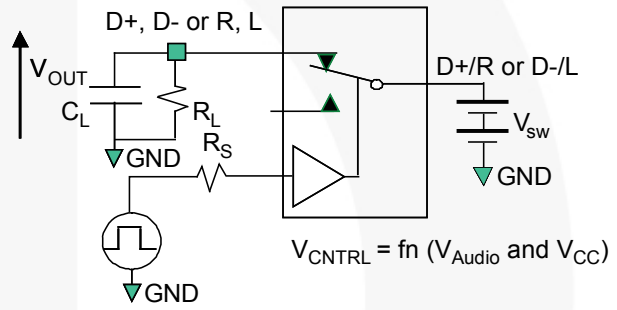


Figure 13. On Leakage



R_L , R_S , and C_L are functions of the application environment (see AC tables for specific values). C_L includes test fixture and stray capacitance.

Figure 14. AC Test Circuit Load

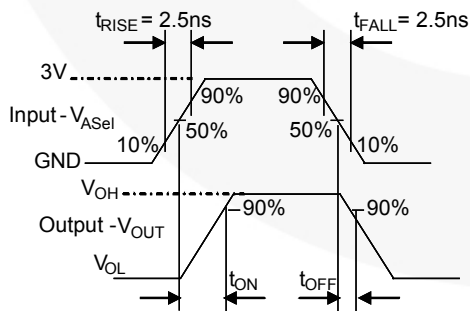


Figure 15. Turn-On / Turn-Off Waveforms (A_{Sel})

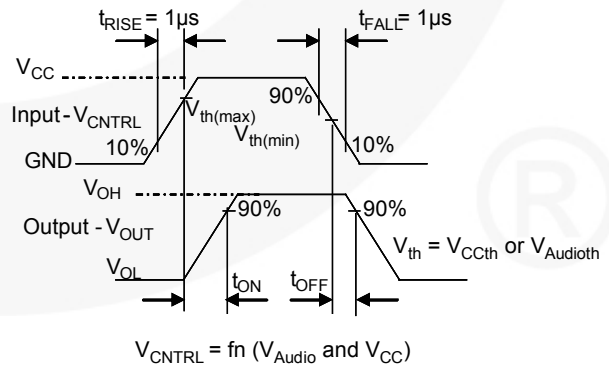


Figure 16. Turn-On / Turn-Off Waveforms (USB/Audio)

Test Diagrams (Continued)

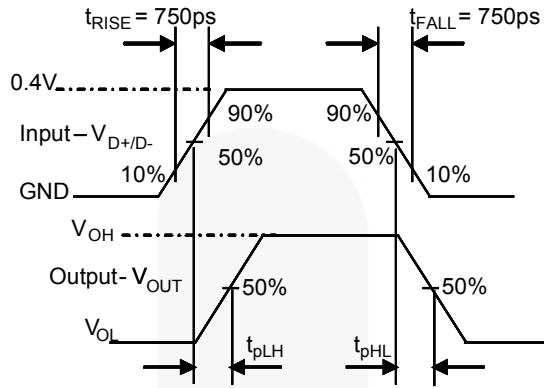


Figure 17. USB Switch Propagation Delay Waveforms

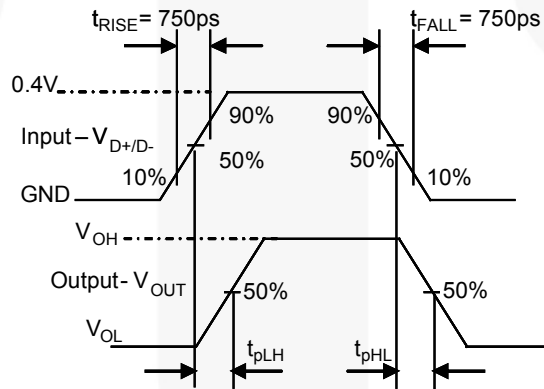


Figure 18. Pulse Skew: $t_{SK(P)} = | t_{pHL} - t_{pLH} |$

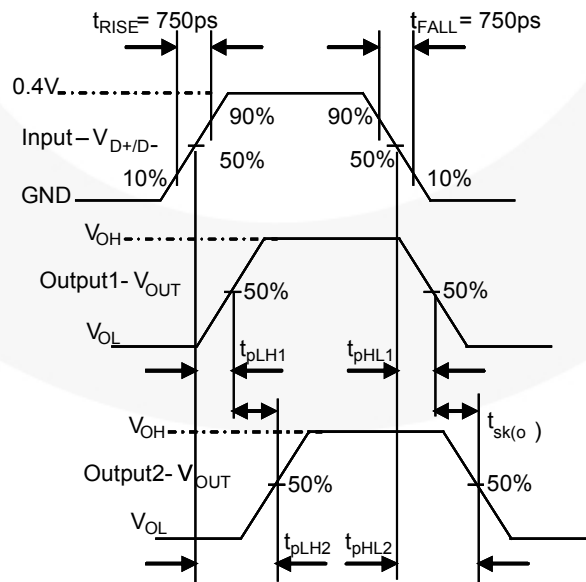


Figure 19. Output Skew: $t_{SK(O)} = | t_{pLH1} - t_{pLH2} |$ or $| t_{pHL1} - t_{pHL2} |$

Test Diagrams (Continued)

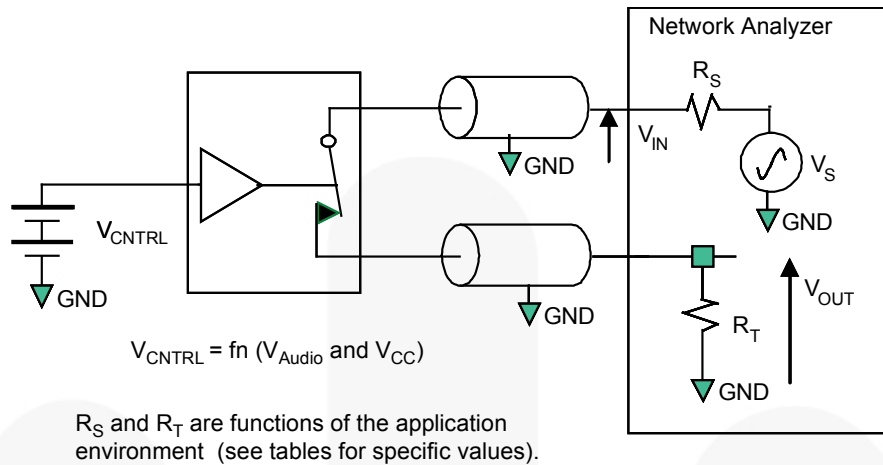


Figure 20. USB Bandwidth

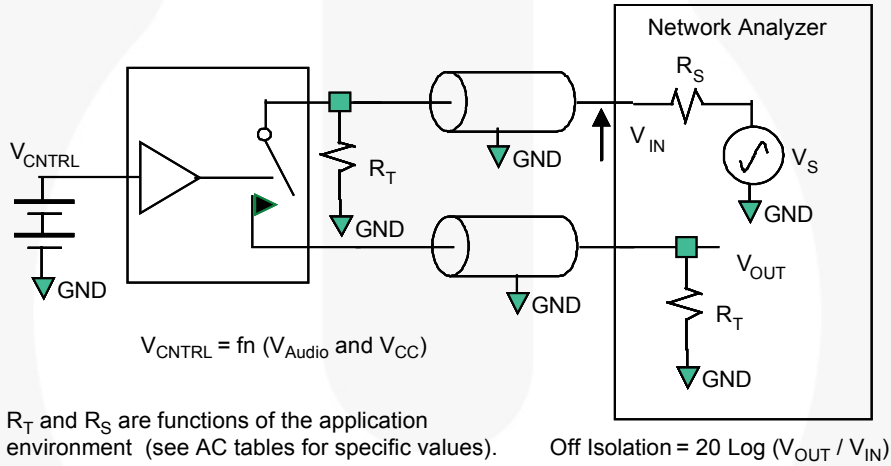


Figure 21. Channel Off Isolation

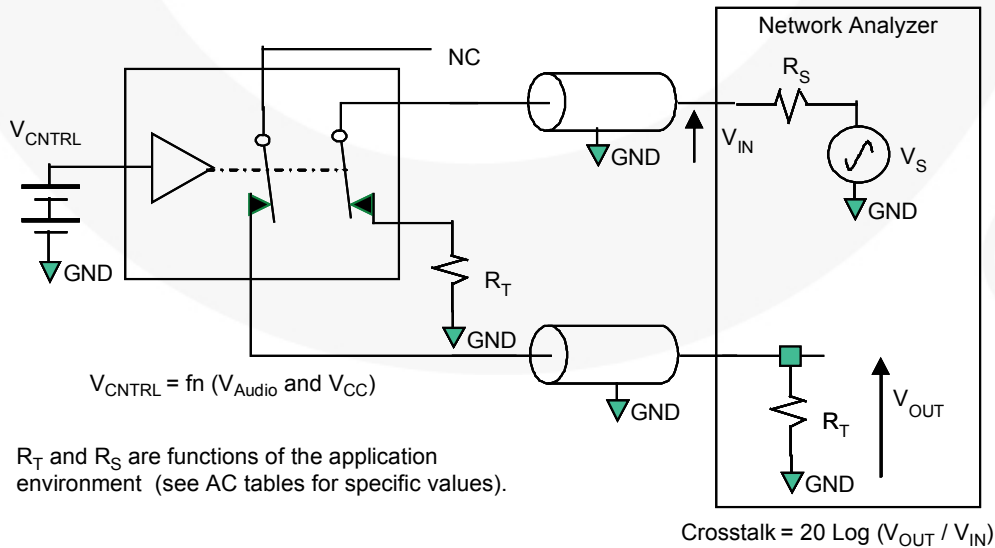


Figure 22. Non-Adjacent Channel-to-Channel Crosstalk

Test Diagrams (Continued)

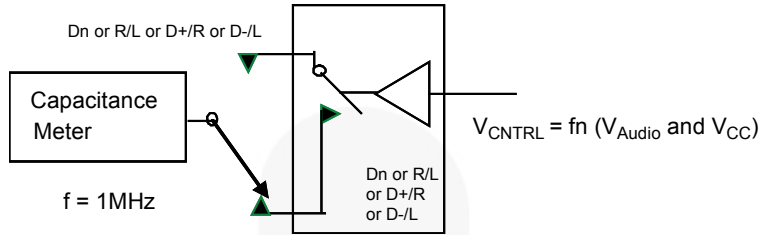


Figure 23. Channel Off Capacitance

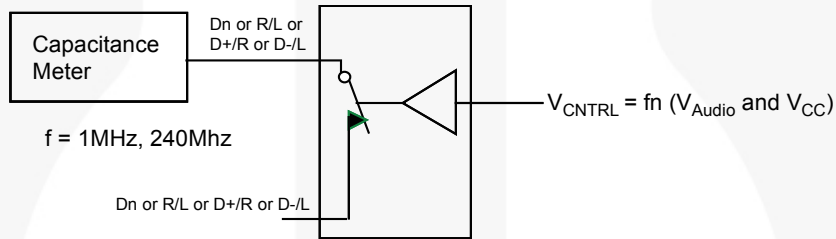


Figure 24. Channel On Capacitance

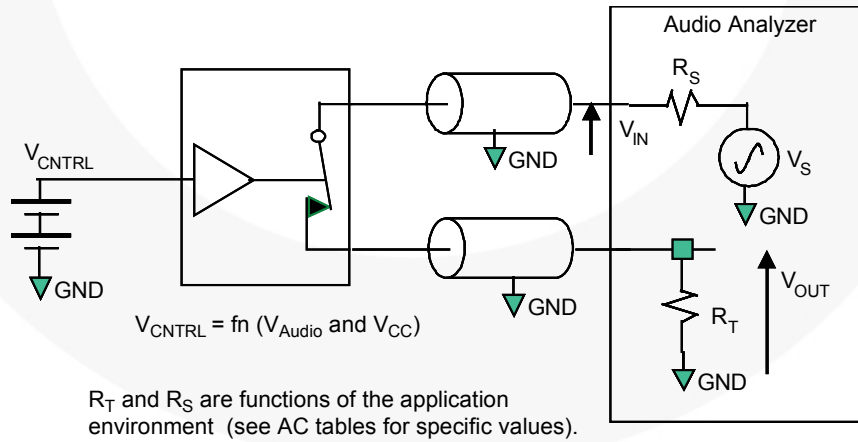


Figure 25. Total Harmonic Distortion

Physical Dimensions

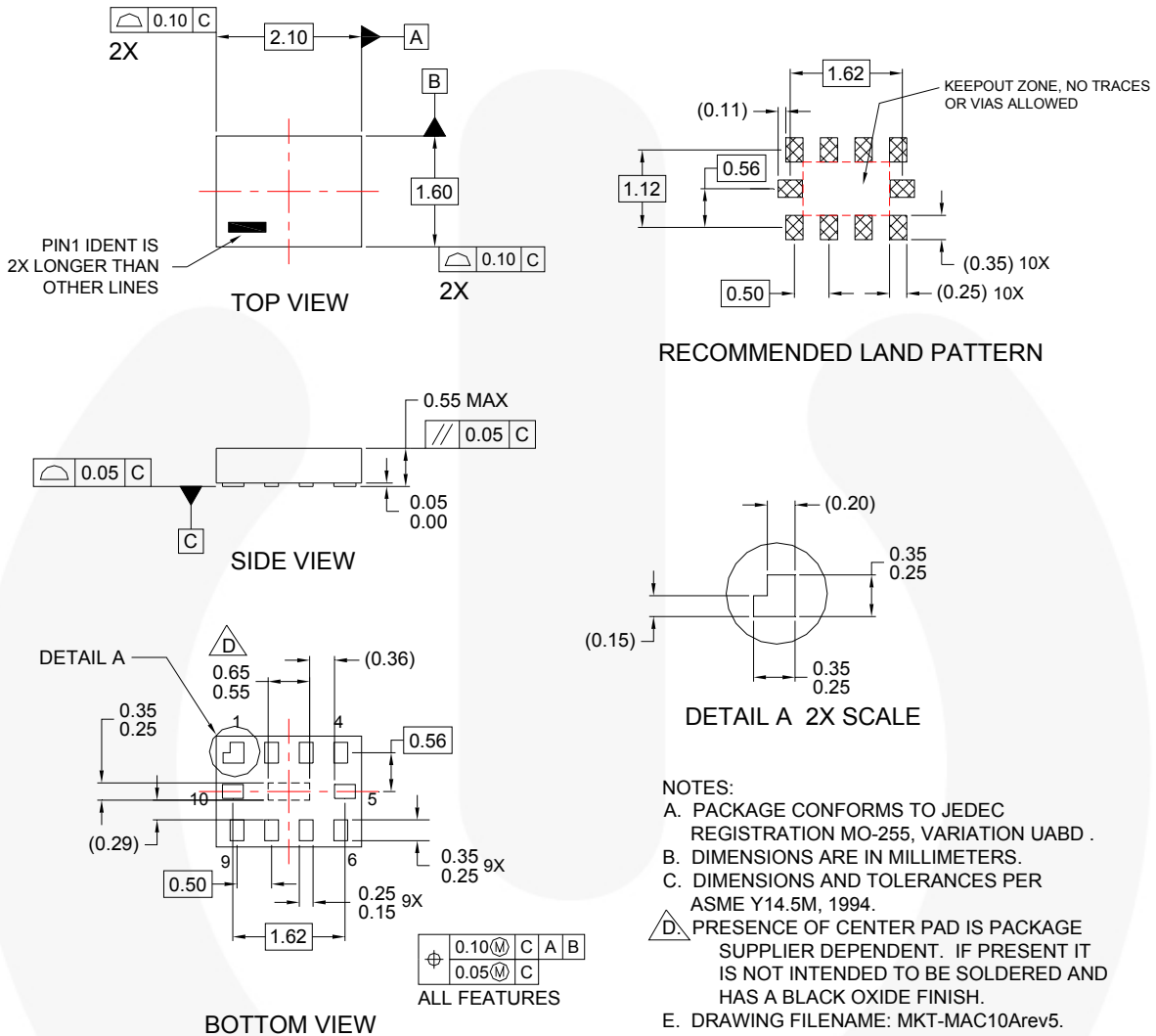


Figure 26. 10-Lead MicroPak™

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Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:
<http://www.fairchildsemi.com/packaging/>.

For tape & reel specifications, please visit, http://www.fairchildsemi.com/products/logic/pdf/micropak_tr.pdf.

Physical Dimensions (Continued)

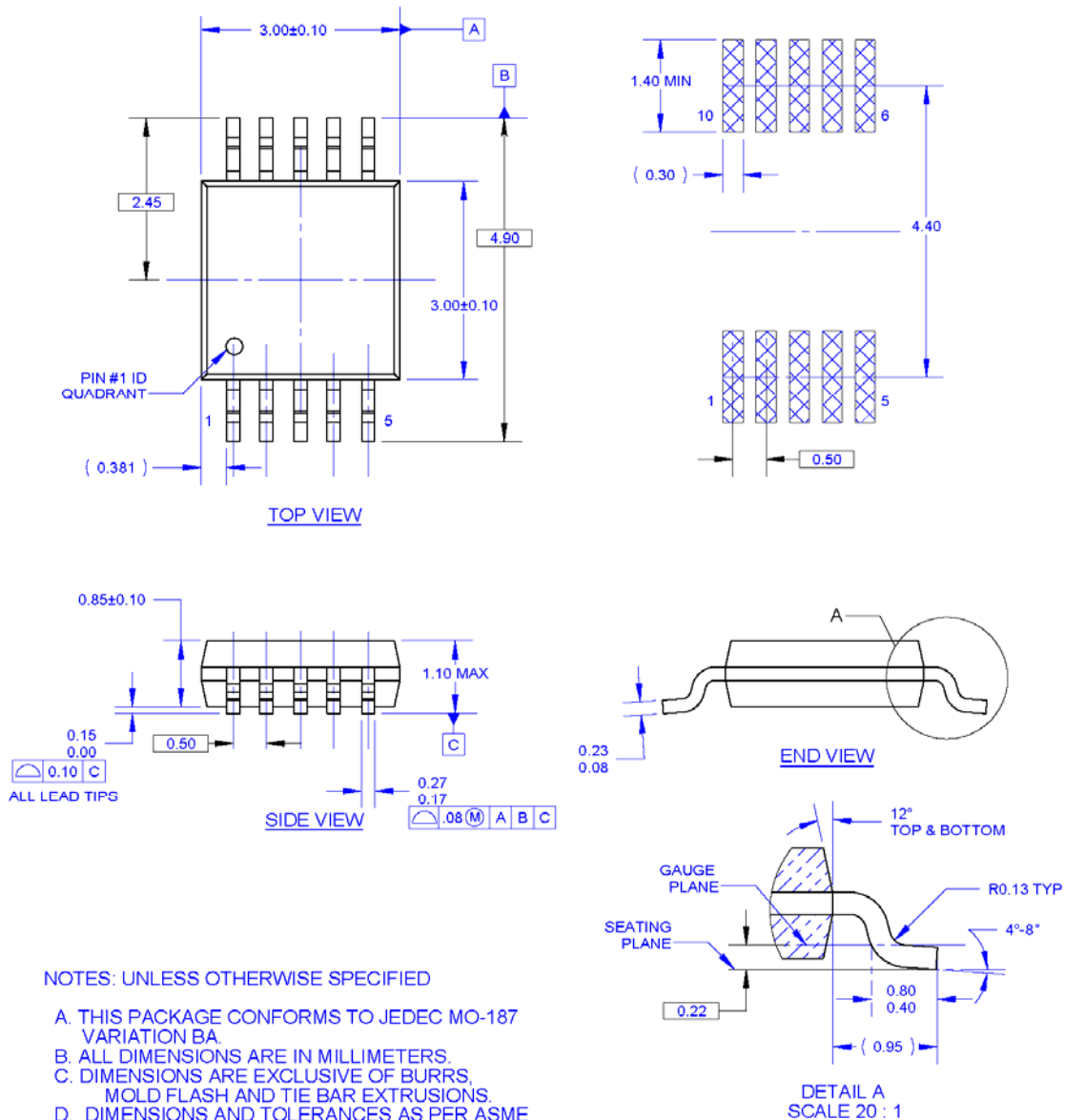


Figure 27. 10-Lead Molded Small Outline Package (MSOP)

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For tape & reel specifications, please visit http://www.fairchildsemi.com/products/analog/pdf/msop10_tr.pdf.

Physical Dimensions (Continued)

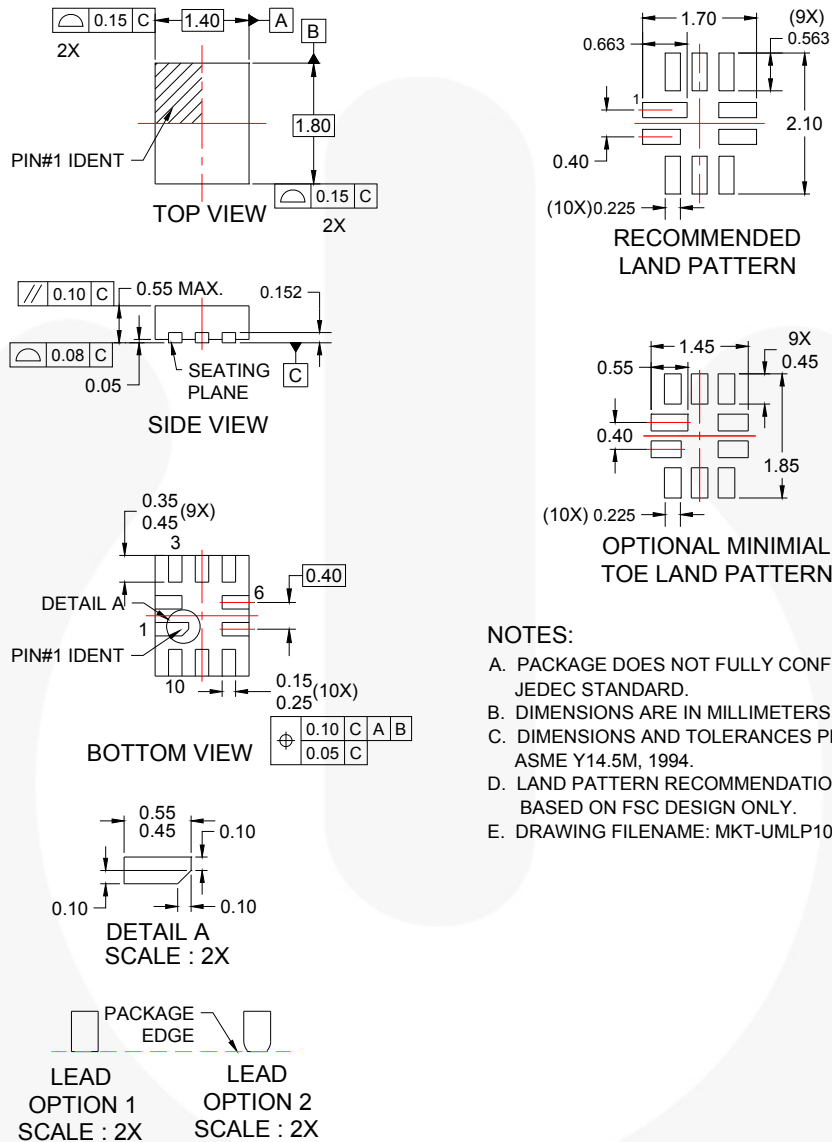


Figure 28. 10-Lead Quad Ultrathin MLP

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<http://www.fairchildsemi.com/packaging/>.

For tape & reel specifications, please visit, http://www.fairchildsemi.com/products/analog/pdf/umlp10_tr.pdf.



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| AX-CAP™* | Global Power Resource™ | Programmable Active Droop™ | TinyBoost™ |
| BitSiC™ | GreenBridge™ | QFET® | TinyBuck™ |
| Build it Now™ | Green FPS™ | QS™ | TinyCalc™ |
| CorePLUS™ | Green FPS™ e-Series™ | Quiet Series™ | TinyLogic® |
| CorePOWER™ | Gmax™ | RapidConfigure™ | TINYOPTO™ |
| CROSSVOL™ | GTO™ |  ™ | TinyPower™ |
| CTL™ | IntelliMAX™ | Saving our world, 1mW/W/kW at a time™ | TinyPWM™ |
| Current Transfer Logic™ | ISOPLANAR™ | SignalWise™ | TinyWire™ |
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| EfficientMax™ | MicroFET™ | SPM® | µSerDes™ |
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|  Fairchild® | MicroPak2™ | SuperFET® | UHC® |
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| FACT Quiet Series™ | MotionMax™ | SuperSOT™-6 | UniFET™ |
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Definition of Terms

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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