

March 2001

ISL9R1560G2, ISL9R1560P2, ISL9R1560S2, ISL9R1560S3S

Features

- Stealth Recovery t_{rr} = 29.4 ns (@ I_F = 15 A)
- Max Forward Voltage, V_F = 2.2 V (@ T_C = 25°C)
- 600 V Reverse Voltage and High Reliability
- · Avalanche Energy Rated
- RoHS Compliant

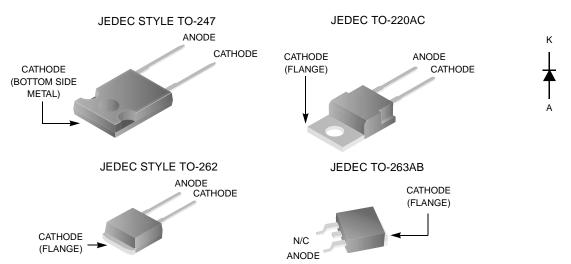
Applications

- Switch Mode Power Supplies
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- · Snubber Diode

15A, 600V, STEALTH™ Diode

The ISL9R1560G2, ISL9R1560P2, ISL9R1560S2, ISL9R1560S3S is a STEALTH diode optimized for low loss performance in high frequency hard switched applications. The STEALTH family exhibits low reverse recovery current (I_{RM(REC)}) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package Symbol



Device Maximum Ratings T_C = 25°C unless otherwise noted

| Symbol | Parameter | Rating | Unit |
|--------------------|--|--------|------|
| V_{RRM} | Repetitive Peak Reverse Voltage | 600 | V |
| V _{RWM} | Working Peak Reverse Voltage | 600 | V |
| V _R | DC Blocking Voltage | 600 | V |
| I _{F(AV)} | Average Rectified Forward Current (T _C = 145°C) | 15 | Α |
| I _{FRM} | Repetitive Peak Surge Current (20kHz Square Wave) | 30 | Α |
| I _{FSM} | Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz) | 200 | Α |

| Symbol | Parameter | Rating | Unit |
|------------------------------------|--|------------|------|
| P _D | Power Dissipation | 150 | W |
| E _{AVL} | Avalanche Energy (1A, 40mH) | 20 | mJ |
| T _J , T _{STG} | Operating and Storage Temperature Range | -55 to 175 | °C |
| T _L T _{PKG} | Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Techbrief TB334 | 300 260 | °C |

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Information

| Device Marking | Device | Package | Tape Width | Quantity |
|----------------|--------------|----------|------------|----------|
| R1560G2 | ISL9R1560G2 | TO-247 | N/A | 30 |
| R1560P2 | ISL9R1560P2 | TO-220AC | N/A | 50 |
| R1560S2 | ISL9R1560S2 | TO-262 | N/A | 50 |
| R1560S3S | ISL9R1560S3S | TO-263AB | 24 mm | 800 |

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

| Symbol | Parameter | Test Conditions | | Min | Тур | Max | Unit |
|----------------|---|------------------------|--|-----|-----|-----|------|
| ff State | e Characteristics | | | | | | |
| I _R | Instantaneous Reverse Current | V _R = 600 V | T _C = 25°C | - | - | 100 | μΑ |
| | | | T _C = 125°C | - | - | 1.0 | mΑ |
| n State | Characteristics | | | | | | |
| n State | e Characteristics | | | _ | | | |
| n State | e Characteristics Instantaneous Forward Voltage | I _F = 15 A | $T_{C} = 25^{\circ}C$ $T_{C} = 125^{\circ}C$ | - | 1.8 | 2.2 | V |

Switching Characteristics

| t _{rr} | Reverse Recovery Time | $I_F = 1 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$ | - | 25 | 30 | ns |
|---------------------|---|---|---|------|----|------|
| | | $I_F = 15 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ V}_R = 30 \text{ V}$ | - | 35 | 40 | ns |
| t _{rr} | Reverse Recovery Time | I _F = 15 A, | - | 29.4 | - | ns |
| I _{rr} | Reverse Recovery Current | $dI_F/dt = 200 A/\mu s$, | - | 3.5 | - | Α |
| Q _{rr} | Reverse Recovered Charge | V _R = 390 V, T _C = 25°C | - | 57 | - | nC |
| T _{rr} | Reverse Recovery Time | I _F = 15 A, | - | 90 | - | ns |
| S | Softness Factor (t _b /t _a) | $dI_F/dt = 200 A/\mu s,$ | - | 2.0 | - | |
| I _{rr} | Reverse Recovery Current | $V_R = 390 \text{ V},$ | - | 5.0 | - | Α |
| Q _{rr} | Reverse Recovered Charge | T _C = 125°C | - | 275 | - | nC |
| t _{rr} | Reverse Recovery Time | I _F = 15 A, | - | 52 | - | ns |
| S | Softness Factor (t _b /t _a) | $dI_F/dt = 800 A/\mu s,$ | - | 1.36 | - | |
| I _{rr} | Reverse Recovery Current | V _R = 390 V, | - | 13.5 | - | Α |
| Q _{rr} | Reverse Recovered Charge | $T_C = 125^{\circ}C$ | - | 390 | - | nC |
| dl _M /dt | Maximum di/dt during t _h | | - | 800 | - | A/µs |

Thermal Characteristics

| $R_{\theta JC}$ | Thermal Resistance Junction to Case | | - | - | 1.0 | °C/W |
|-----------------|--|--------|---|---|-----|------|
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | TO-247 | - | - | 30 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | TO-220 | - | - | 62 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | TO-262 | - | - | 62 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | TO-263 | ı | ı | 62 | °C/W |

25 175°C 175

Typical Performance Curves

Figure 1. Forward Current vs Forward Voltage

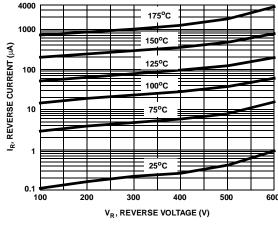


Figure 2. Reverse Current vs Reverse Voltage

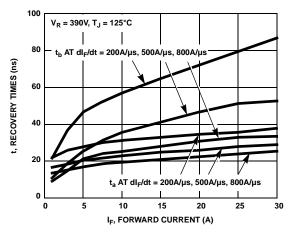


Figure 3. t_a and t_b Curves vs Forward Current

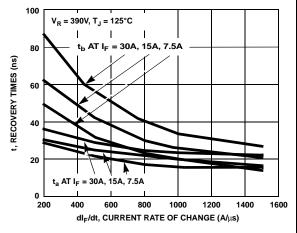


Figure 4. t_a and t_b Curves vs dl_F/dt

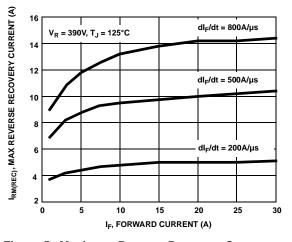


Figure 5. Maximum Reverse Recovery Current vs
Forward Current

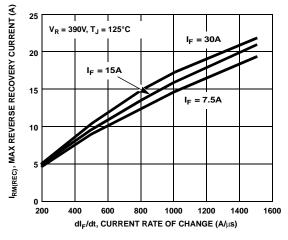


Figure 6. Maximum Reverse Recovery Current vs dl_F/dt

2.5 V_R = 390V, T_J = 125°C V_R = 390V, T_J = 125°C V_R = 390V, T_J = 125°C V_R = 390V, T_J = 125°C

Typical Performance Curves (Continued)

Figure 7. Reverse Recovery Softness Factor vs $\mathrm{dI_F/dt}$

 dI_F/dt , CURRENT RATE OF CHANGE (A/ μ s)

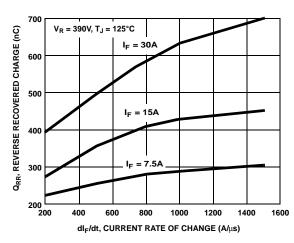


Figure 8. Reverse Recovered Charge vs dl_F/dt

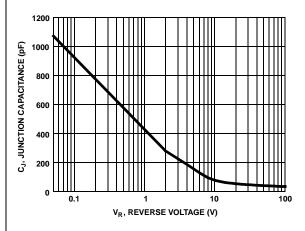


Figure 9. Junction Capacitance vs Reverse Voltage

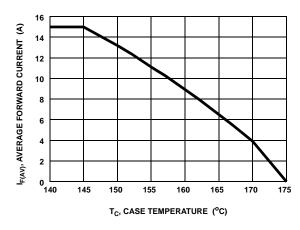


Figure 10. DC Current Derating Curve

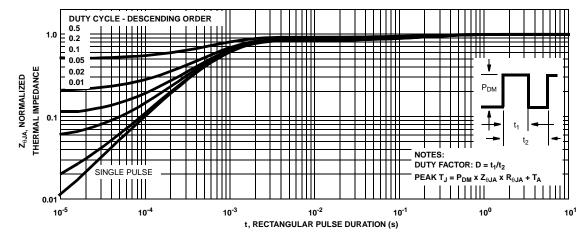
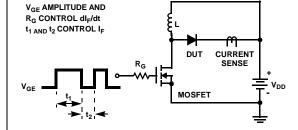


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



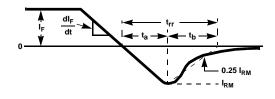
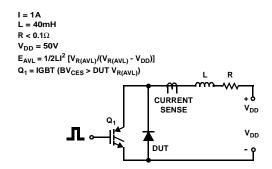


Figure 12. t_{rr} Test Circuit

Figure 13. t_{rr} Waveforms and Definitions



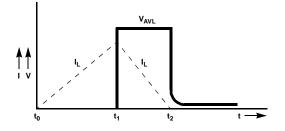


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms



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