

RJK0456DPB

Silicon N Channel Power MOS FET Power Switching

REJ03G1879-0200 Rev.2.00 Mar 04, 2010

Features

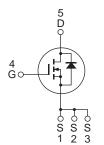
- High speed switching
- Low drive current
- Low on-resistance
- $R_{DS(on)} = 2.6 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V})$

- Pb-free
- Halogen-free
- High density mounting

Outline

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)





1, 2, 3 Source 4 Gate

Gate
Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	40	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	50	А
Drain peak current	I _{D(pulse)} Note1	200	А
Body-drain diode reverse drain current	I _{DR}	50	А
Avalanche current	I _{AP} Note 2	50	А
Avalanche energy	E _{AR} Note 2	20	mJ
Channel dissipation	Pch Note3	65	W
Channel to Case Thermal Resistance	θch-C	1.92	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at L=10uH, Tch = 25°C, Rg \geq 50 Ω

3. Tc = 25°C

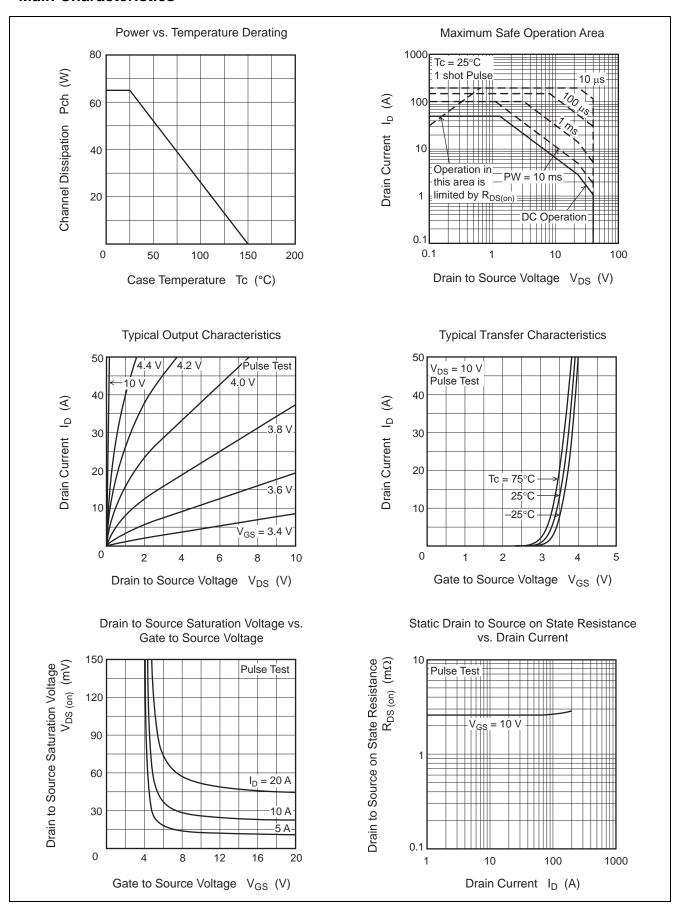
Electrical Characteristics

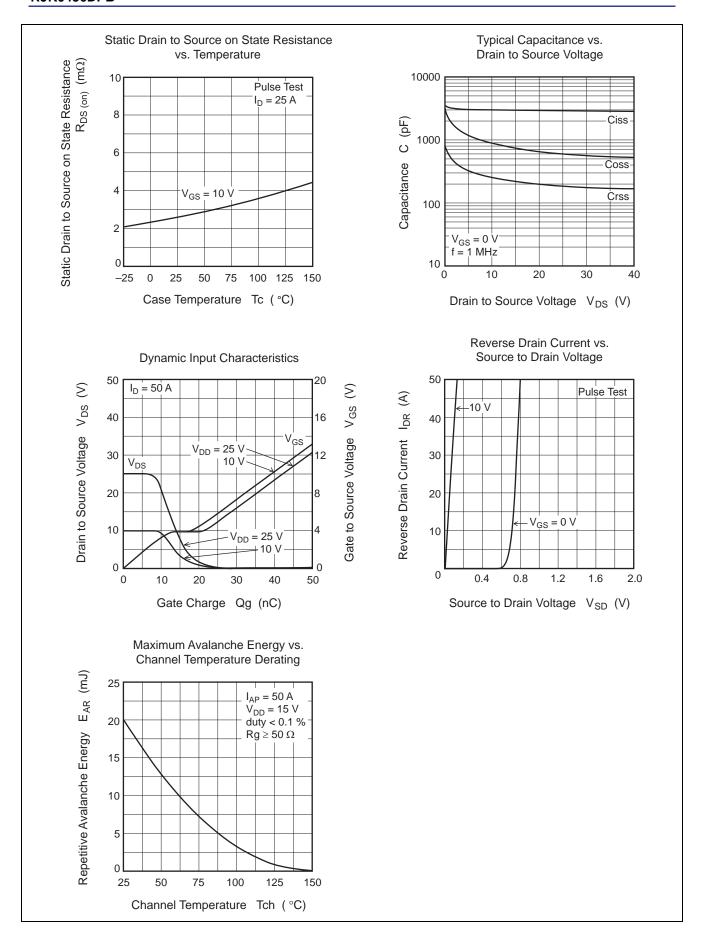
 $(Ta = 25^{\circ}C)$

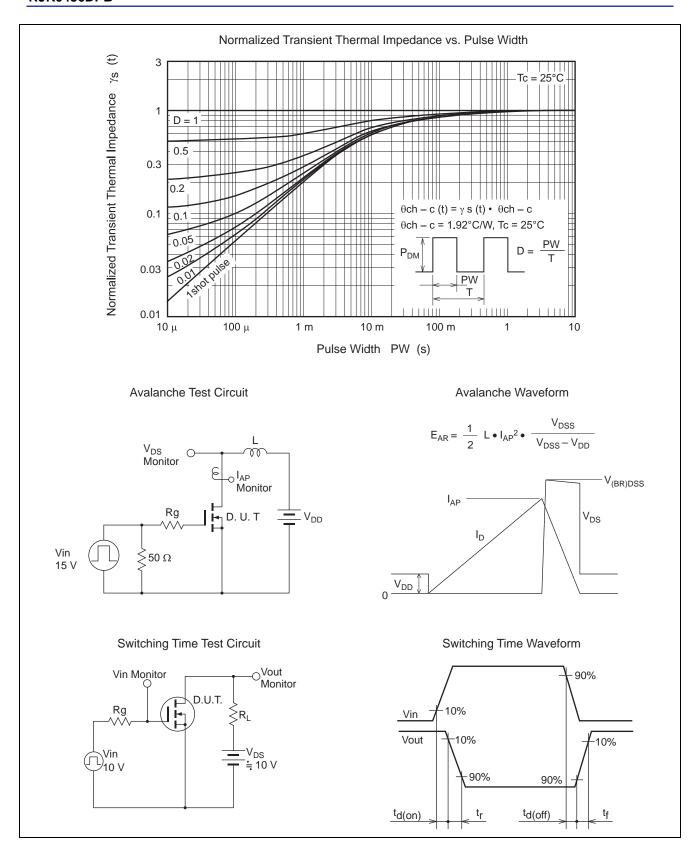
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$
Gate to source cutoff voltage	V _{GS(off)}	2.0	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	2.6	3.2	mΩ	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	67	_	S	$I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	3000	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$
Output capacitance	Coss	_	900	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	260	_	pF	
Gate Resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	39	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	13	_	nC	I _D = 50 A
Gate to drain charge	Qgd	_	6.0	_	nC	
Turn-on delay time	t _{d(on)}	_	14	_	ns	$V_{GS} = 10 \text{ V}, I_D = 25 \text{ A},$
Rise time	t _r	_	6.8	_	ns	$V_{DD} \cong 10 \text{ V}, R_L = 0.4 \Omega,$
Turn-off delay time	t _{d(off)}	_	34	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	8.0	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.8	1.1	V	$I_F = 50 \text{ A}, V_{GS} = 0 \text{ V}^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	41	_	ns	$I_F = 50 \text{ A}, V_{GS} = 0 \text{ V}$
						di _F / dt = 100 A/ μs

Notes: 4. Pulse test

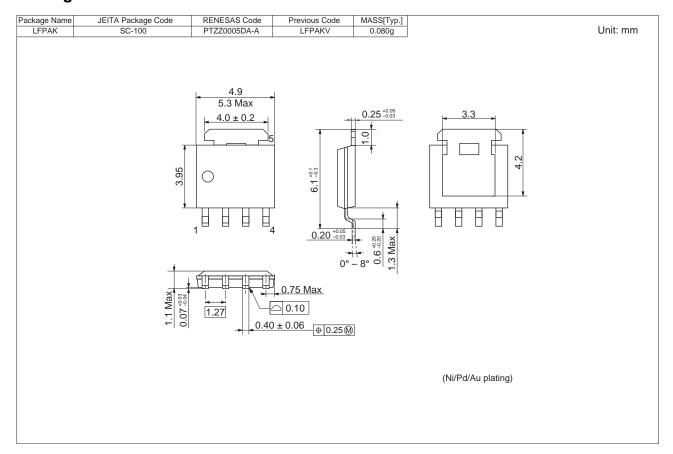
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0456DPB-00-J5	2500 pcs	Taping

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