HSD88

Silicon Schottky Barrier Diode for Detector, Mixer

REJ03G0602-0100
(Previous: ADE-208-1386)
Rev.1.00
Apr 26, 2005

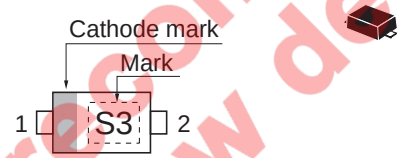
Features

- Low capacitance. (C = 0.8 pF max)
- Low forward voltage.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Cathode Mark	Package Name	Package Code (Previous Code)
HSD88	S3	SFP	PUSF0002ZB-A (SFP)

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Average rectified current	I_O	15	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	0.350	—	0.420	V	I_F = 1 mA
	V_{F2}	0.500	—	0.580		I_F = 10 mA
Reverse current	I_{R1}	—	—	0.2	μA	V_R = 2 V
	I_{R2}	—	—	10		V_R = 10 V
Capacitance	C	—	—	0.80	pF	V_R = 0 V, f = 1 MHz
ESD-Capability *[1]	—	30	—	—	Ω	C = 200 pF, Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ; I_R > 0.4 μA at V_R = 2 V

2. Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic

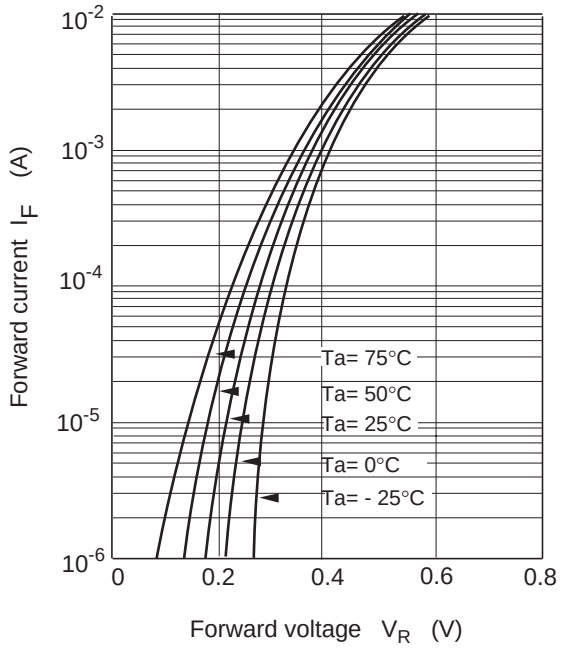


Fig.1 Forward current vs. Forward voltage

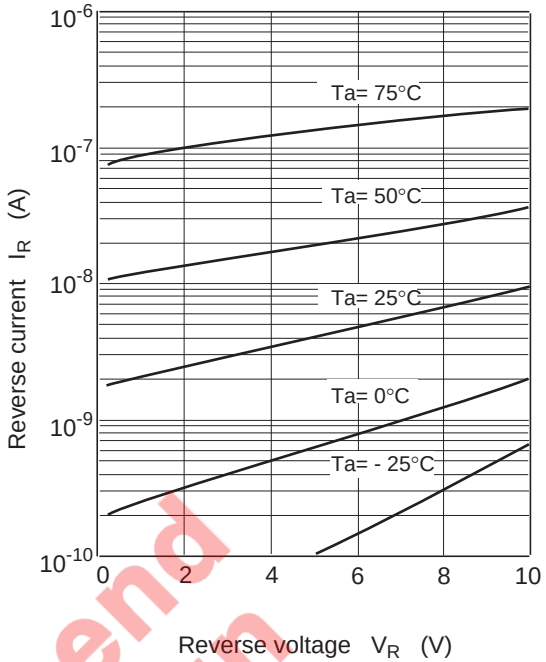


Fig.2 Reverse current vs. Reverse voltage

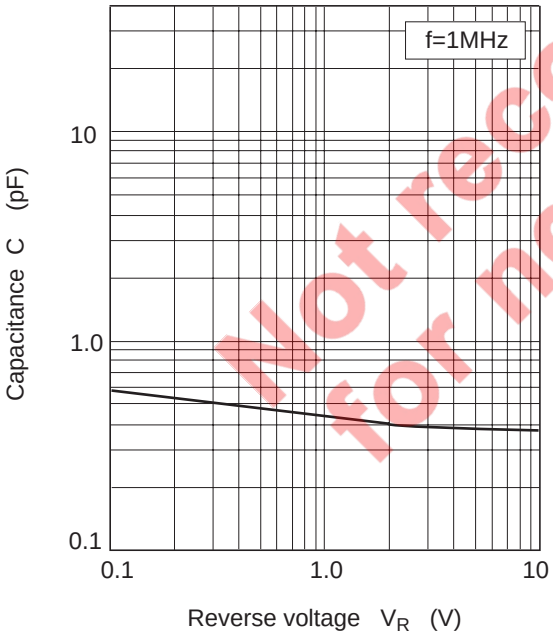
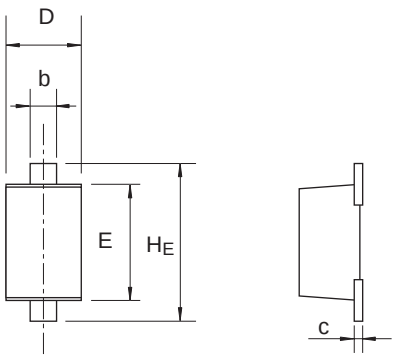
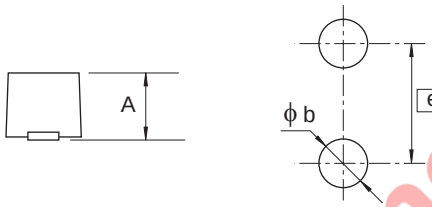


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
—	PUSF0002ZB-A	SFP / SFPV	0.0010g





Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	—	0.55
b	0.25	0.30	0.35
c	0.08	0.13	0.18
D	0.55	0.60	0.65
E	0.90	1.00	1.10
H_E	1.30	1.40	1.50
ϕ b	—	0.50	—
e_1	—	1.40	—

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Colophon 2.0

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April 1st, 2010
Renesas Electronics Corporation

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