

HCMOS 5x3.2mm SMD VCXO

Y5C

DATASHEET

VCXO

Supply Voltage: 3.3V

Specifications			
Parameter	Max unless otherwise specified		
Frequency Range	1.0~ 125.000MHz		
Temperature Range			
Operating (T _{OPR})	See Table		
Storage (T _{STG})	-55°C ~ +125°C		
Frequency Stability	See Table1 below		
Pullability Options ($Vc = 1.65V \pm 1.5V$)	See Table2 below		
Supply Voltage (V _{DD})	3.3V ± 5%		
Control Voltage (Vc)	1.65V ± 1.5V		
Input Current (IDD)			
1.0 ~ 30.000MHz	15 mA		
30+ ~ 52.000MHz	25 mA		
52+ ~ 125.000MHz	35 mA		
Output Symmetry (50%VDD)	40% ~ 60%		
Rise/Fall Time (20% ~ 80%V _{DD}) (T _R /T _F)	5 nS		
Output Voltage (VoL)	10% V _{DD}		
(V _{OH})	90% V _{DD} Min		
Output Load (HCMOS)	15 pF		
Start-up Time (T _S)	10 mS		
Frequency Linearity	±10%		
Modulation Bandwidth	20 kHz Min		
Vc Input Impedance	5 MΩ typ		
Enable Time	10 mS		
Disable Time	150 nS		

Table 1

Available Options by Stability & Operating Temp
 Frequency Stability
 Operating Temperature (°C)

Frequency Range

Stability	Temperature (°C)	Frequency Range (MHz)
±25PPM	-20 ~ +70	1.0 ~ 125.0
±25PPM	-40 ~ +85	1.0 ~ 125.0
±50PPM	-20 ~ +70	1.0 ~ 125.0
±50PPM	-40 ~ +85	1.0 ~ 125.0

Table 2

Frequency Pullability	Frequency Range (MHz)
±25PPM	1.0 ~ 125.0
±50PPM	1.0 ~ 125.0
±100PPM	1.0 ~ 100.0

Stability inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, shock, vibration, reflow, and one-year aging.

ENABLE / DISABLE FUNCTION				
Pin2	Output (pin 4)			
OPEN ¹	Active			
'1' Level V _{IH} ≥ 70%V _{DD}	Active			
'0' Level V _{IL} ≤ 30%V _{DD}	High Z			

 $^{^{1}}$ An internal pull-up resistor from pin 1 to pin 4 allows active output $\,$ if pin 1 is left open.

FÖX	
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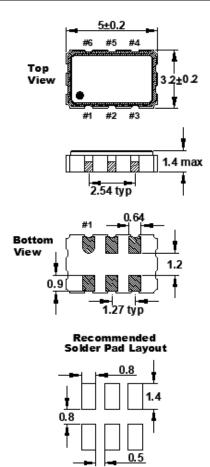
Title / Description: Y5C SERIES STANDARD SPECIFICATIONS							
Drawing Number: Size: A							
Part Number:	Cage: 61429						
Draftsperson: BEC	Approved: MAJ	Revision Date: 03/16/2020					



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Dimensions in millimeters

Pin Connections

#6 VDD #5 N.C. #4 Out #1 Vc #2 E/D #3 Gnd

Maximum Soldering Temp / Time	260°C / 10 Seconds x 2		
Moisture Sensitivity Level (MSL)	1		
Termination Finish	Au over Ni		
Seal Method	Seam		
Lead (Pb) Free	Yes		
ROHS/REACH Compliant	Yes		

Notes

*A 0.01µF capacitor should be placed between V_{DD} (Pin 6) and GND (Pin 3) to minimize power supply line noise.

*Dimensional drawing is for reference to critical specifications defined by size measurements.

Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary.



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	Ta	pe Spe	cificatio	ns (mil	llimeter	s)	Reel Specifications (millimeters)				eters)			
Α	В	С	D	Е	F	Reel Qty		G	Н	ı	J	K	L	М
Ø1.55	4.0	8.0	5.5	12.0	1.7	-T1 = 1,000		2.0	Ø13	Ø21	Ø62	Ø180	13.5	2.0
	1.75	Direction +		A + C	B + + + + + + + + + + + + + + + + + + +	D E		_	ØH G		ØI	120°	K	M

Available Options & Part Identification* Example: <u>F Y5C B J M 27.0</u>							
F Y5C B J M 27.0							
F = Fox	Model Number	Voltage	Stability/Pullability	Operating	Frequency (MHz)		
		$B = 3.3V \pm 5\%$	E = ±25ppm/±50ppm	Temperature			
			F = ±50ppm/±50ppm	F = -20 to +70°C			
H = ±25ppm/±100ppm							
J = ±50ppm/±100ppm							

^{*}Not all frequencies in the frequency range, or every combination of stability, temp range, and voltage available.



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