



Offline LED driver with primary-sensing and high power factor up to 7 W

Datasheet – preliminary data

Features

- High power factor capability (> 0.9)
- 800 V, avalanche rugged internal 11 Ω power MOSFET
- Internal high-voltage startup
- Primary sensing regulation (PSR)
- $\pm 5\%$ accuracy on constant LED output current
- Quasi-resonant (QR) operation
- Optocoupler not needed
- Open or short LED string management
- Automatic self supply

Applications

- AC-DC LED driver bulb replacement lamps up to 7 W, with high power factor
- AC-DC LED drivers up to 7 W

Description

The HVLED807PF is a high-voltage primary switcher intended to operate directly from the rectified mains with minimum external parts and enabling high power factor (> 0.90) to provide an efficient, compact and cost effective solution for LED driving. It combines a high-performance low-voltage PWM controller chip and an 800 V, avalanche-rugged Power MOSFET, in the same package. There is no need for an optocoupler thanks to the patented primary sensing regulation (PSR) technique. The device assures protection against LED string fault (open or short).

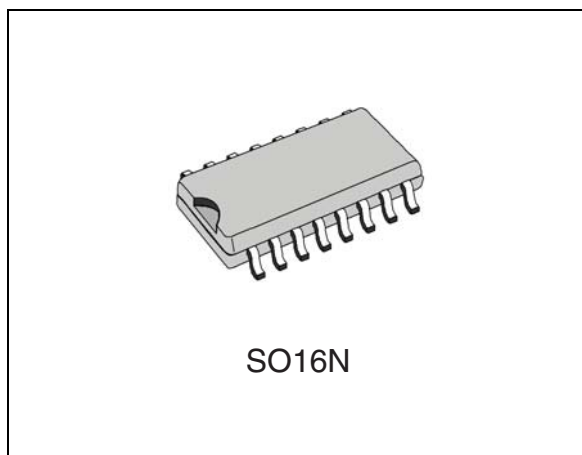


Table 1. Device summary

Order code	Package	Packaging
HVLED807PF	SO16N	Tube
HVLED807PFTR		Tape & Reel

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1 Principle application circuit and block diagram

1.1 Principle application circuit

Figure 1. Principle application circuit for high power factor LED driver

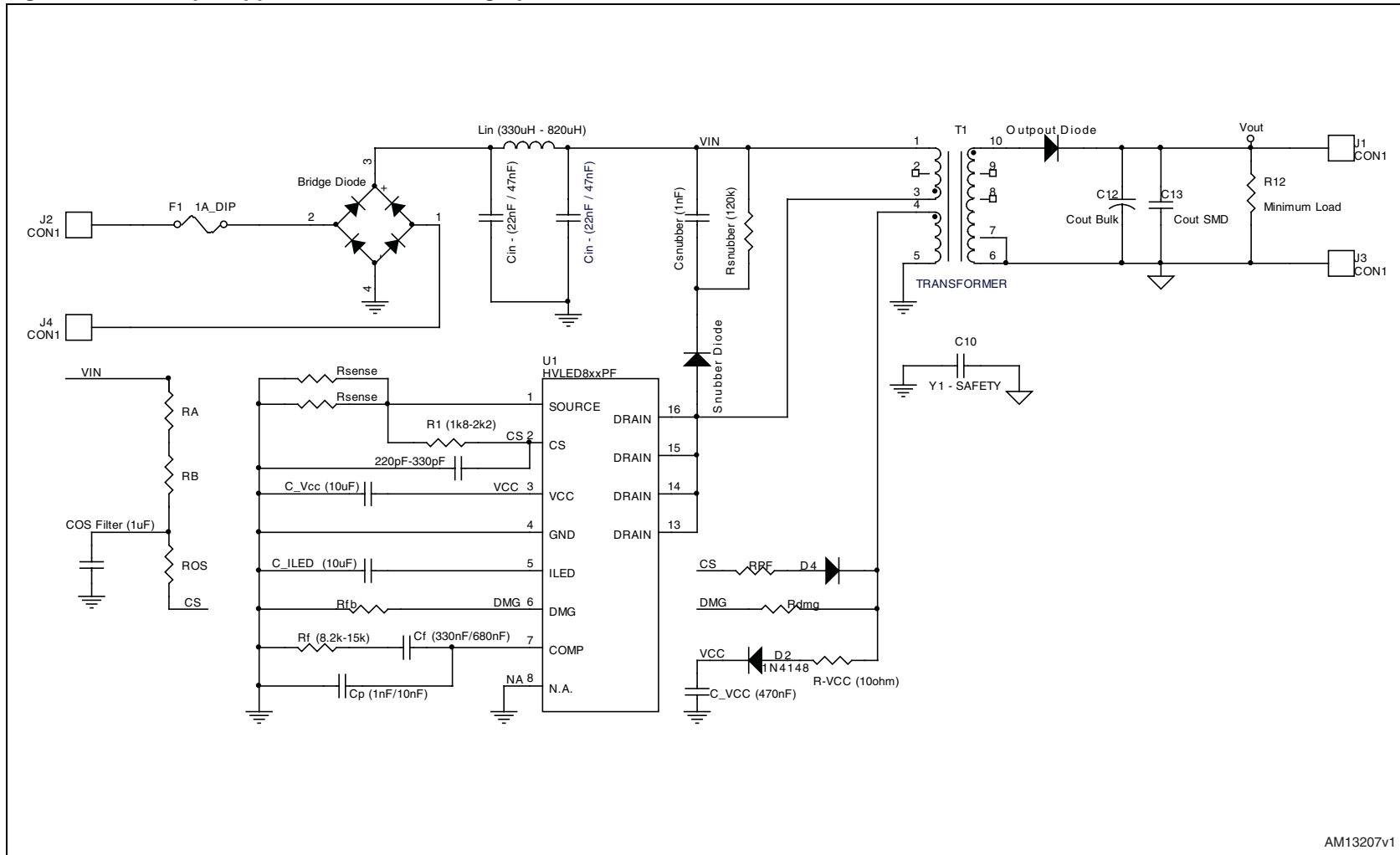
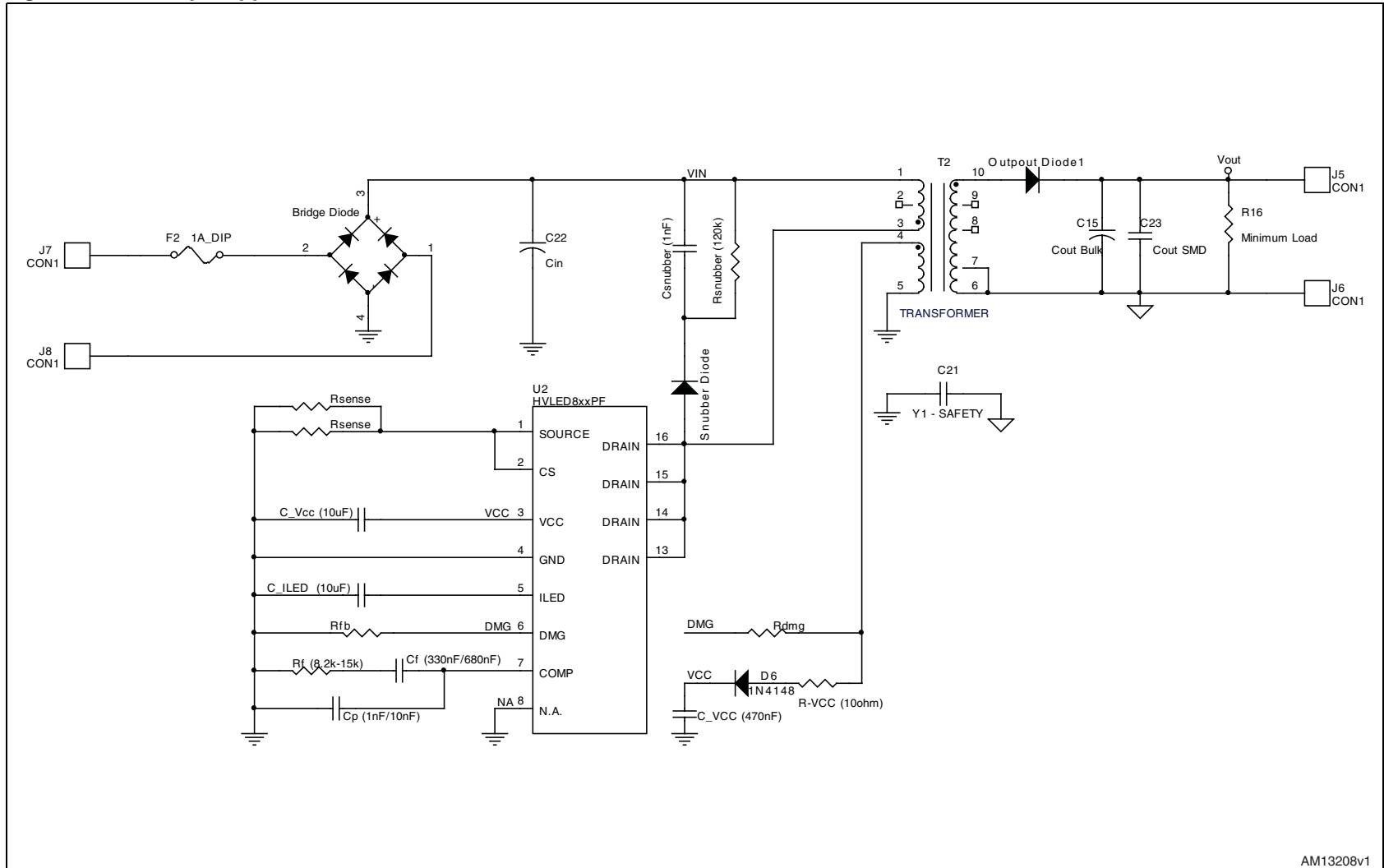
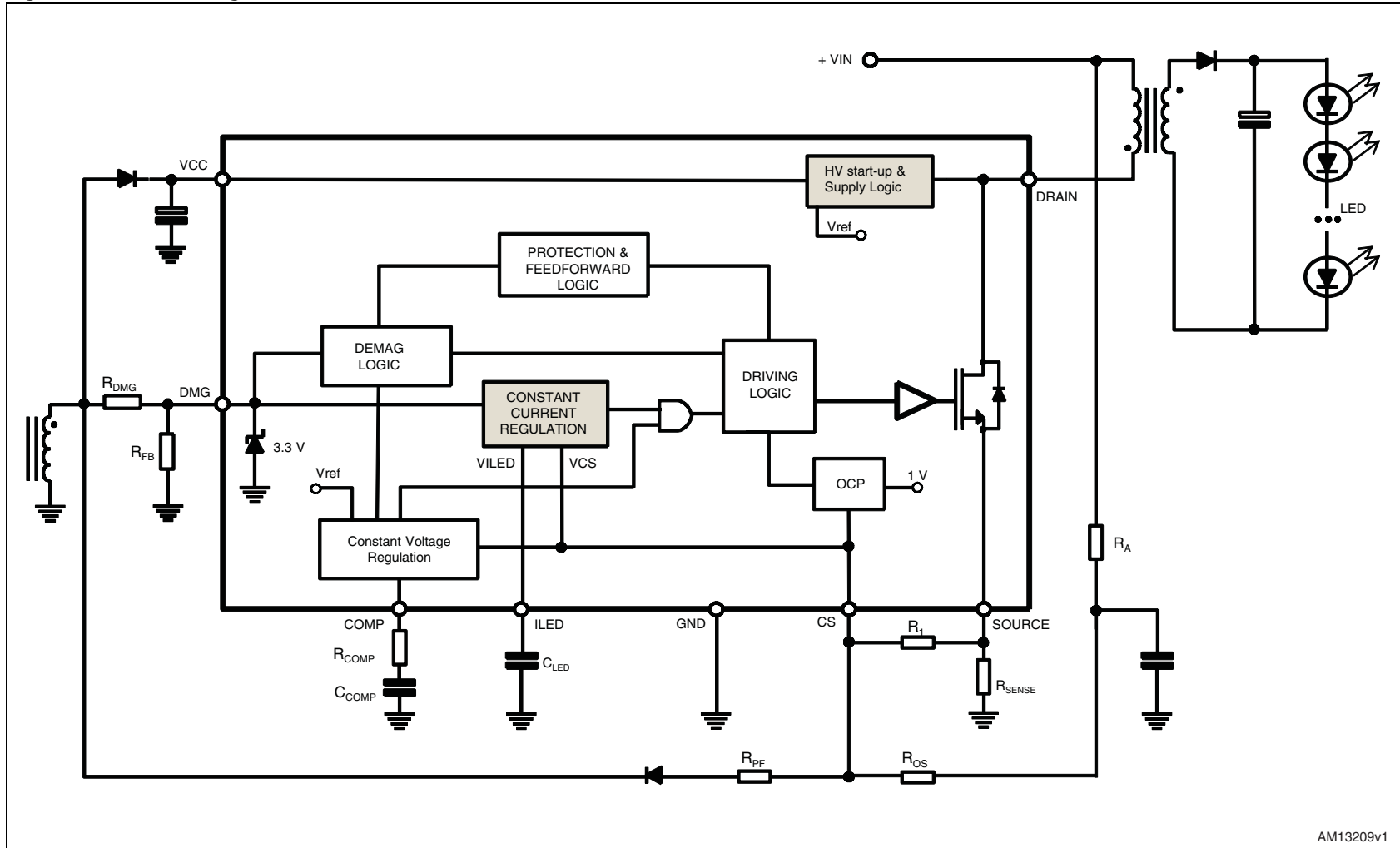


Figure 2. Principle application circuit for standard LED driver


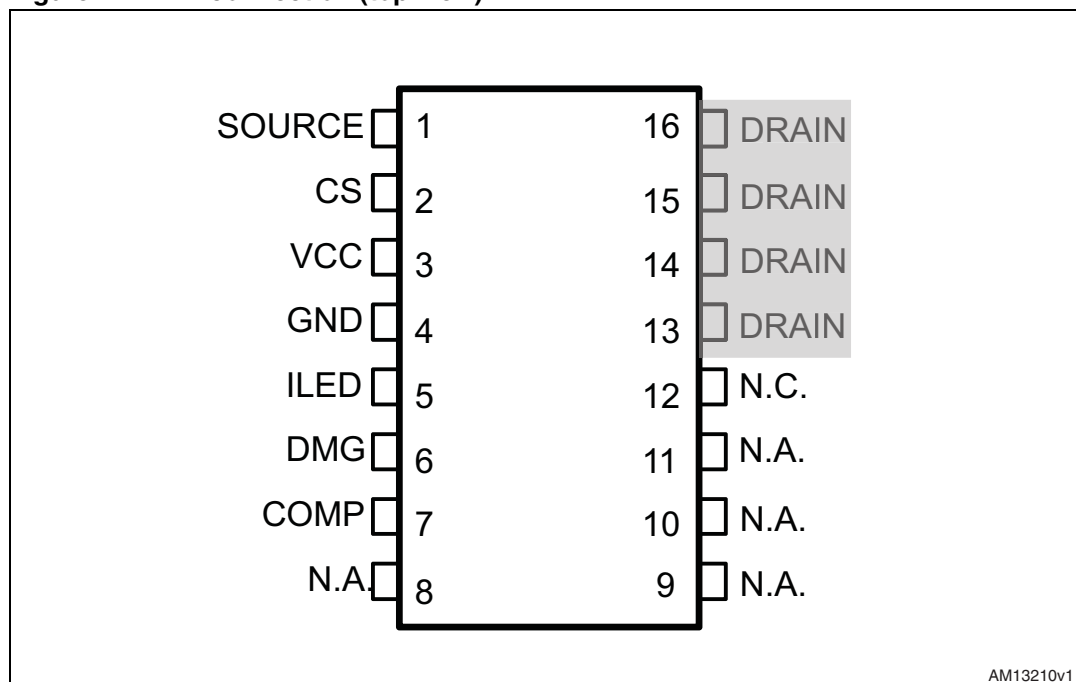
1.2 Block diagram

Figure 3. Block diagram



2 Pin description and connection diagrams

Figure 4. Pin connection (top view)



2.1 Thermal data

Table 2. Thermal data

Symbol	Parameter	Max. value	Unit
R_{thJP}	Thermal resistance, junction-to-pin	10	°C/W
R_{thJA}	Thermal resistance, junction-to-ambient	110	°C/W
P_{TOT}	Maximum power dissipation at $T_A = 50\text{ °C}$	0.9	W
T_{MAX}	Maximum junction temperature	150	°C
T_{STG}	Storage temperature range	-55 to 150	°C
T_J	Junction temperature range	-40 to 125	°C

3 Electrical specifications

3.1 Absolute maximum ratings

Table 3. Absolute maximum ratings

Symbol	Pin	Parameter	Value	Unit
V_{DS}	1, 13-16	Drain-to-source (ground) voltage	-1 to 800	V
I_D	1, 13-16	Drain current ⁽¹⁾	1	A
E_{av}	1, 13-16	Single-pulse avalanche energy ($T_j = 25\text{ °C}$, $I_D = 0.7\text{ A}$)	50	mJ
V_{CC}	3	Supply voltage ($I_{CC} < 25\text{ mA}$)	Self limiting	V
I_{DMG}	6	Zero current detector current	± 2	mA
V_{CS}	2	Current sense analog input	-0.3 to 3.6	V
V_{comp}	7	Analog input	-0.3 to 3.6	V

1. Limited by maximum temperature allowed.

3.2 Electrical characteristics

Table 4. Electrical characteristics^{(1) (2)}

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
Power section						
V _{(BR)DSS}	Drain-source breakdown	I _D < 100 μA; T _j = 25 °C	800			V
I _{DSS}	OFF-state drain current	V _{DS} = 750 V; T _j = 125 °C (3), see Figure 5			80	μA
R _{DS(on)}	Drain-source ON-state resistance	Id=250 mA; T _j = 25 °C		6	7.4	Ω
		Id=250 mA; T _j = 125 °C (3)			14.8	
C _{OSS}	Effective (energy-related) output capacitance	(3) See Figure 6				
High-voltage startup generator						
V _{START}	Min. drain start voltage	I _{charge} < 100 μA	40	50	60	V
I _{CHARGE}	V _{cc} startup charge current	V _{DRAIN} > V _{Start} ; V _{CC} < V _{CCOn} T _j = 25 °C	4	5.5	7	mA
		V _{DRAIN} > V _{Start} ; V _{CC} < V _{CCOn}	+/- 10%			
V _{CC_RESTART}	V _{cc} restart voltage (V _{cc} falling)	(4)	9.5	10.5	11.5	V
		After protection tripping		5		

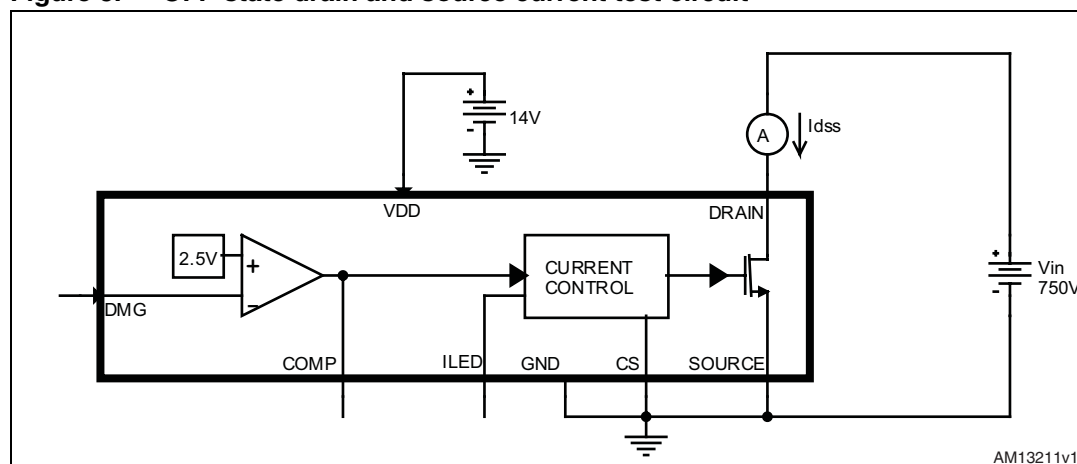
Table 4. Electrical characteristics^{(1) (2)} (continued)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
Supply voltage						
V _{CC}	Operating range	After turn-on	11.5		23	
V _{CC_ON}	Turn-on threshold	(4)	12	13	14	V
V _{CC_OFF}	Turn-off threshold	(4)	9	10	11	V
V _Z	Internal Zener voltage	I _{CC} = 20 mA	23	25	27	V
Supply current						
I _{CC_START-UP}	Startup current	See Figure 7		200	300	μA
I _q	Quiescent current	See Figure 8		1	1.4	mA
I _{CC}	Operating supply current at 50 kHz	See Figure 9		1.4	1.7	mA
I _{q(fault)}	Fault quiescent current	See Figure 10		250	350	μA
Startup timer						
T _{START}	Start timer period		105	140	175	μs
T _{RESTART}	Restart timer period during burst mode		420	500	700	μs
Demagnetization detector						
I _{Dmgb}	Input bias current	V _{DMG} = 0.1 to 3 V		0.1	1	μA
V _{DMGH}	Upper clamp voltage	I _{DMG} = 1 mA	3.0	3.3	3.6	V
V _{DMGL}	Lower clamp voltage	I _{DMG} = - 1 mA	-90	-60	-30	mV
V _{DMGA}	Arming voltage	Positive-going edge	100	110	120	mV
V _{DMGT}	Triggering voltage	Negative-going edge	50	60	70	mV
T _{BLANK}	Trigger blanking time after MOSFET turn-off	V _{COMP} ≥ 1.3 V		6		μs
		V _{COMP} = 0.9 V		30		
Line feedforward						
R _{FF}	Equivalent feedforward resistor	I _{DMG} = 1 mA		45		Ω
Transconductance error amplifier						
V _{REF}	Voltage reference	T _J = 25 °C	2.45	2.51	2.57	V
		(3) T _J = -25 to 125 °C and V _{CC} = 12 V to 23 V	2.4		2.6	
g _m	Transconductance	ΔI _{COMP} = ± 10 μA V _{COMP} = 1.65 V	1.3	2.2	3.2	ms
G _v	Voltage gain	(5) Open loop		73		dB
GB	Gain-bandwidth product	(5)		500		KHz

Table 4. Electrical characteristics^{(1) (2)} (continued)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
I_{COMP}	Source current	$V_{DMG} = 2.3\text{ V}$, $V_{COMP} = 1.65\text{ V}$	70	100		μA
	Sink current	$V_{DMG} = 2.7\text{ V}$, $V_{COMP} = 1.65\text{ V}$	400	750		μA
V_{COMPH}	Upper COMP voltage	$V_{DMG} = 2.3\text{ V}$		2.7		V
V_{COMPL}	Lower COMP voltage	$V_{DMG} = 2.7\text{ V}$		0.7		V
V_{COMPBM}	Burst-mode threshold			1		V
Hys	Burst-mode hysteresis			65		mV
Current reference						
V_{ILEDx}	Maximum value	$V_{COMP} = V_{COMPL}$	1.5	1.6	1.7	V
V_{CLED}	Current reference voltage		0.192	0.2	0.208	V
Current sense						
t_{LEB}	Leading-edge blanking	(5)		330		ns
T_D	Delay-to-output (H-L)			90	200	ns
V_{CSx}	Max. clamp value	(4) $dV_{CS}/dt = 200\text{ mV}/\mu\text{s}$	0.7	0.75	0.8	V
V_{CSdis}	Hiccup-mode OCP level	(4)	0.92	1	1.08	V

1. $V_{CC}=14\text{ V}$ (unless otherwise specified).
2. Limits are production tested at $T_J=T_A=25\text{ }^{\circ}\text{C}$, and are guaranteed by statistical characterization in the range $T_J 25\text{--}125\text{ }^{\circ}\text{C}$.
3. Not production tested, guaranteed statistical characterization only.
4. Parameters tracking each other (in the same section).
5. Guaranteed by design.

Figure 5. OFF-state drain and source current test circuit

Note: The measured $IDSS$ is the sum between the current across the startup resistor and the effective MOSFET's OFF-state drain current.

Figure 6. COSS output capacitance variation

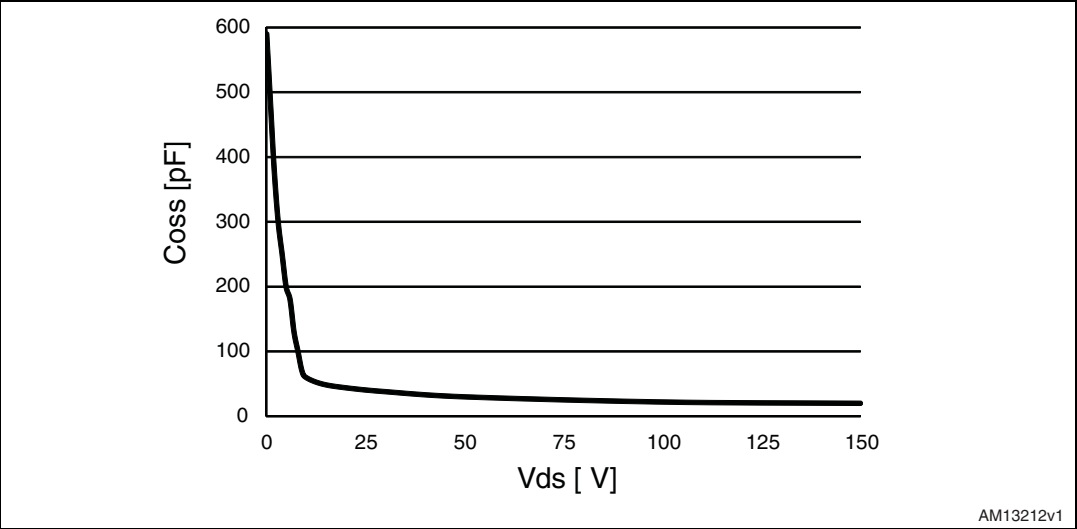


Figure 7. Startup current test circuit

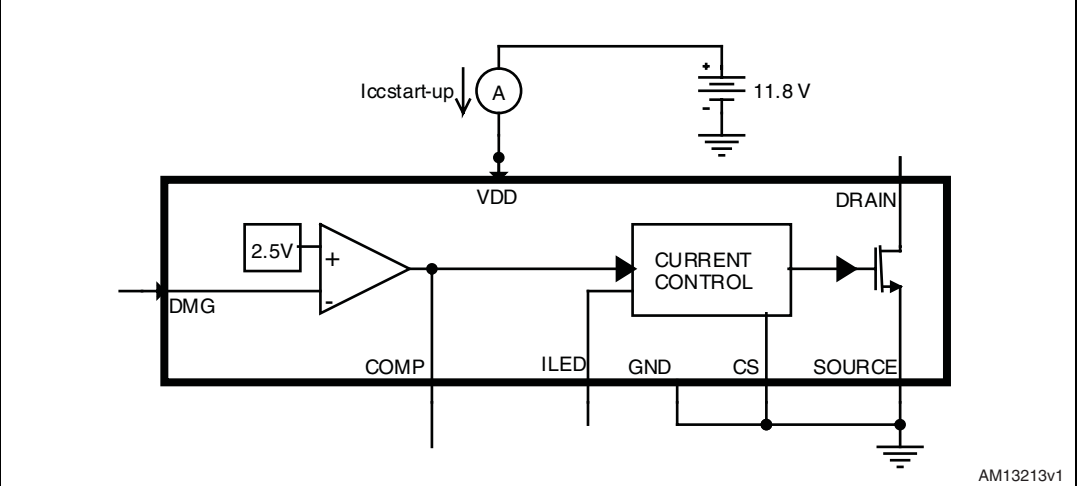
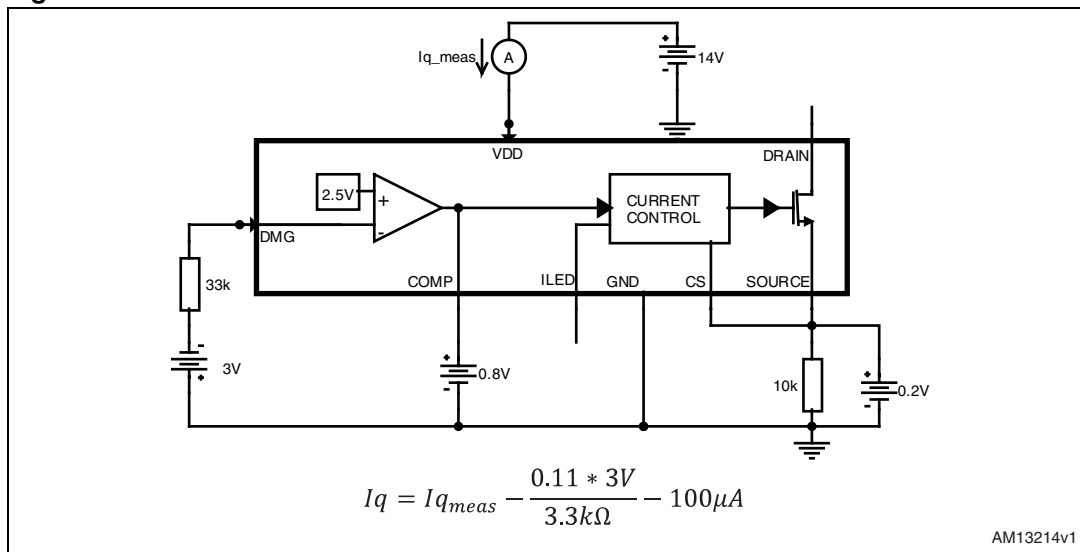
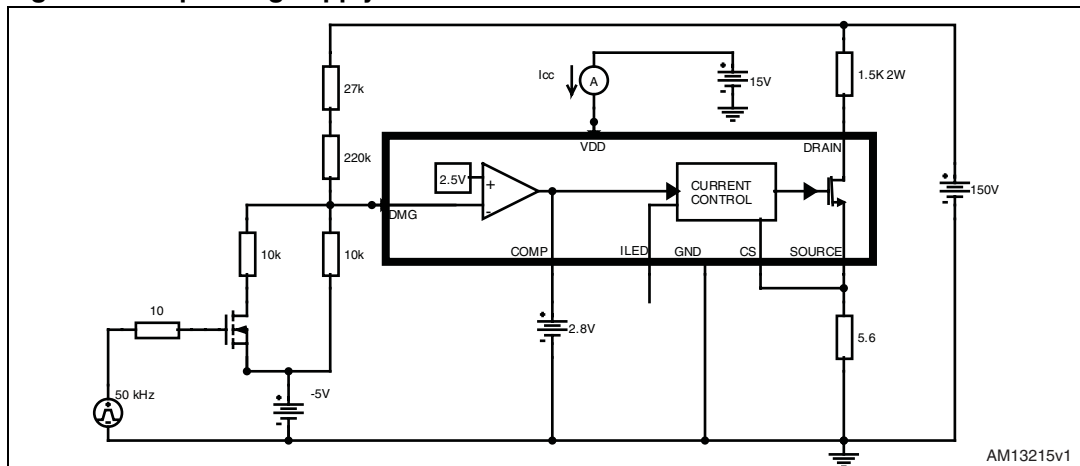
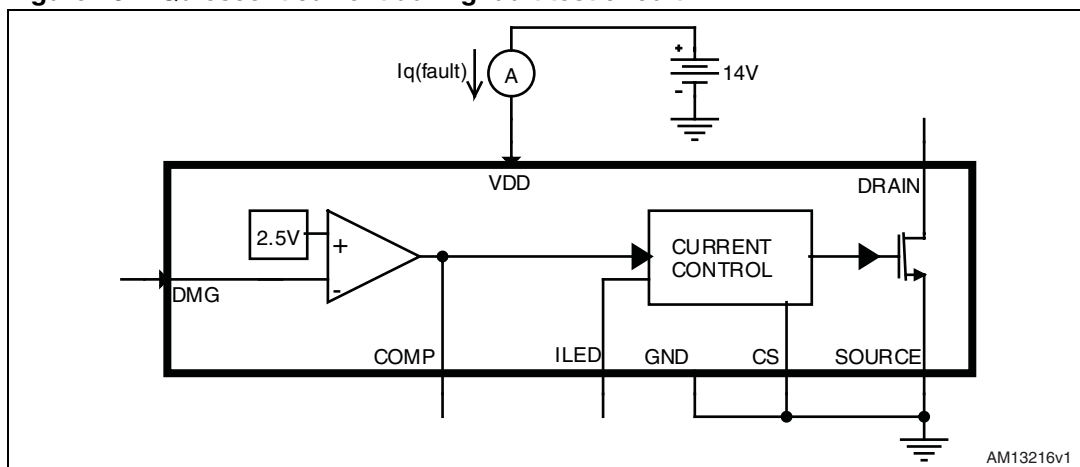


Figure 8. Quiescent current test circuit**Figure 9. Operating supply current test circuit**

Note: The circuit across the DMG pin is used for switch-on synchronization.

Figure 10. Quiescent current during fault test circuit

4 Device description

The HVLED807PF is a high-voltage primary switcher intended to operate directly from the rectified mains with minimum external parts to provide high power factor (> 0.90) and an efficient, compact and cost effective solution for LED driving. It combines a high-performance low-voltage PWM controller chip and an 800 V, avalanche-rugged Power MOSFET, in the same package.

The PWM is a current-mode controller IC specifically designed for ZVS (zero voltage switching) flyback LED drivers, with constant output current (CC) regulation using primary sensing feedback (PSR). This eliminates the need for the optocoupler, the secondary voltage reference, as well as the current sense on the secondary side, while still maintaining a good LED current accuracy. Moreover, it guarantees a safe operation when short-circuit of one or more LEDs occurs.

The device can also provide a constant output voltage regulation (CV): it allows the application to be able to work safely when the LED string opens due to a failure.

In addition, the device offers the shorted secondary rectifier (i.e. LED string shorted due to a failure) or transformer saturation detection.

Quasi-resonant operation is achieved by means of a transformer demagnetization sensing input that triggers MOSFET turn-on. This input serves also as both output voltage monitor, to perform CV regulation, and input voltage monitor, to achieve mains-independent CC regulation (line voltage feedforward).

The maximum switching frequency is top-limited below 166 kHz, so that at medium-light load a special function automatically lowers the operating frequency still maintaining the operation as close to ZVS as possible. At very light load, the device enters a controlled burst-mode operation that, along with the built-in high-voltage startup circuit and the low operating current of the device, helps minimize the residual input consumption.

Although an auxiliary winding is required in the transformer to correctly perform CV/CC regulation, the chip is able to power itself directly from the rectified mains. This is useful especially during CC regulation, where the flyback voltage generated by the winding drops.

5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Figure 11. SO16N mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A			1.75
A1	0.10		0.25
A2	1.25		
b	0.31		0.51
c	0.17		0.25
D	9.80	9.90	10.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e		1.27	
h	0.25		0.50
L	0.40		1.27
k	0		8°
ccc			0.10

Figure 12. SO16N drawing

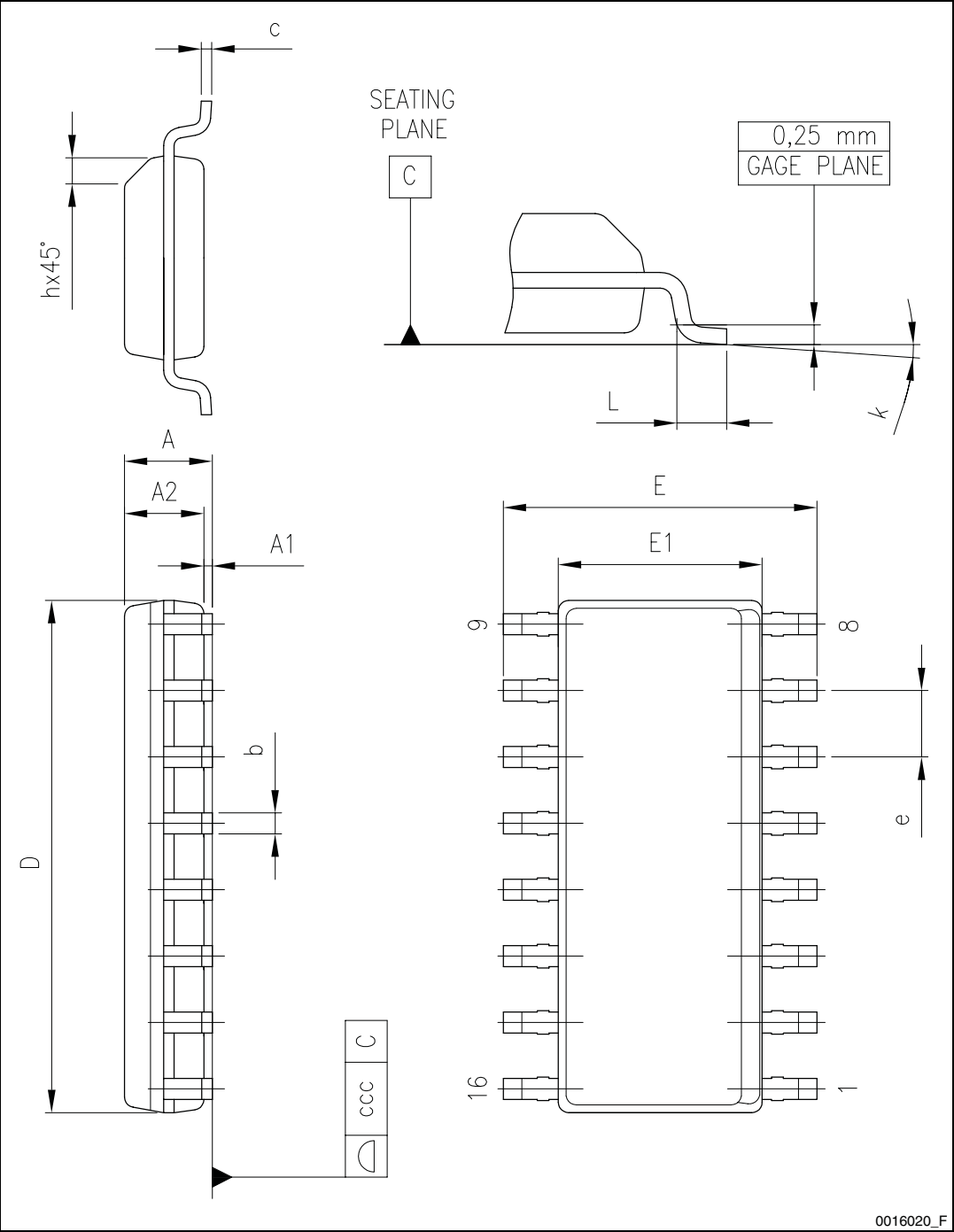
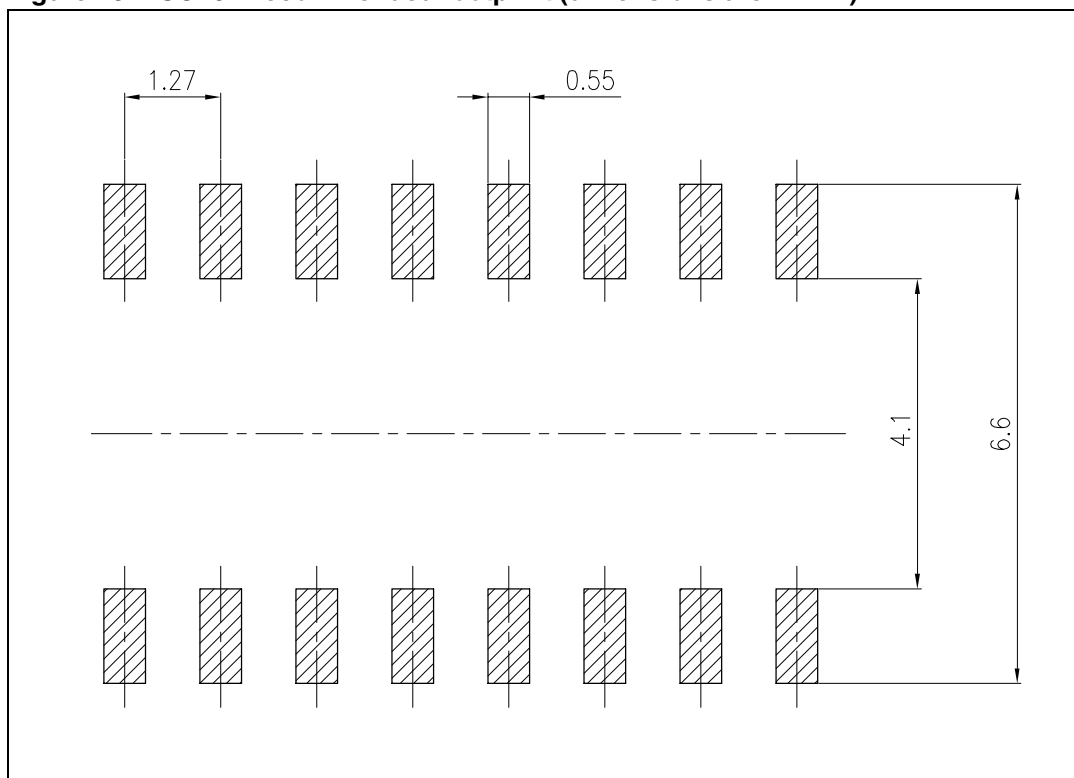


Figure 13. SO16N recommended footprint (dimensions are in mm)

6 Revision history

Table 5. Document revision history

Date	Revision	Changes
26-Jul-2012	1	Initial release.

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