



LD2979 series

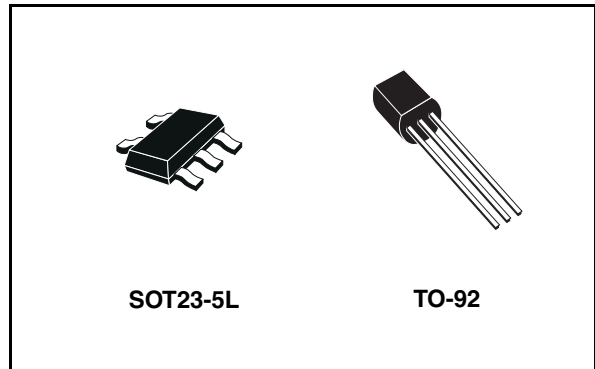
Very low drop voltage regulators with inhibit

Feature summary

- Very low dropout voltage (0.2V typ. at 50mA load)
- Very low quiescent current (typ. 500µA at 50mA load)
- Output current up to 50mA
- Logic-controlled electronic shutdown
- Output voltages of 3.0; 3.2; 3.3; 3.8; 5.0V
- Internal current and thermal limit
- Supply voltage rejection: 63dB (typ)
- Only 1µF for stability
- Selection at 25°C
- Temperature range: -25°C to 125°C
- Package available: SOT23-5L and TO-92

Description

The LD2979 series are very Low Drop regulators available in SOT23-5L and TO-92.



The very low drop-voltage and the very low quiescent current make them particularly suitable for low noise, low power applications and in battery powered systems.

Shutdown Logic Control function is available on five pin version (TTL compatible). This means that when the device is used as local regulator, it is possible to put a part of the board in standby, decreasing the total power consumption.

Order codes

Part number			Output voltage
SOT23-5L	TO92 ⁽¹⁾	TO-92 (Ammopak) ⁽¹⁾	
LD2979M30TR	LD2979Z30TR ⁽²⁾	LD2979Z30AP ⁽²⁾	3.0 V
LD2979M32TR ⁽²⁾			3.2 V
LD2979M33TR	LD2979Z33TR ⁽²⁾	LD2979Z33AP	3.3 V
LD2979M38TR	LD2979Z38TR ⁽²⁾	LD2979Z38AP ⁽²⁾	3.8 V
LD2979M50TR	LD2979Z50TR ⁽²⁾	LD2979Z50AP	5.0 V

1. Please note that in these cases pins are shaped according to Tape & Reel specifications.

2. Available on request.

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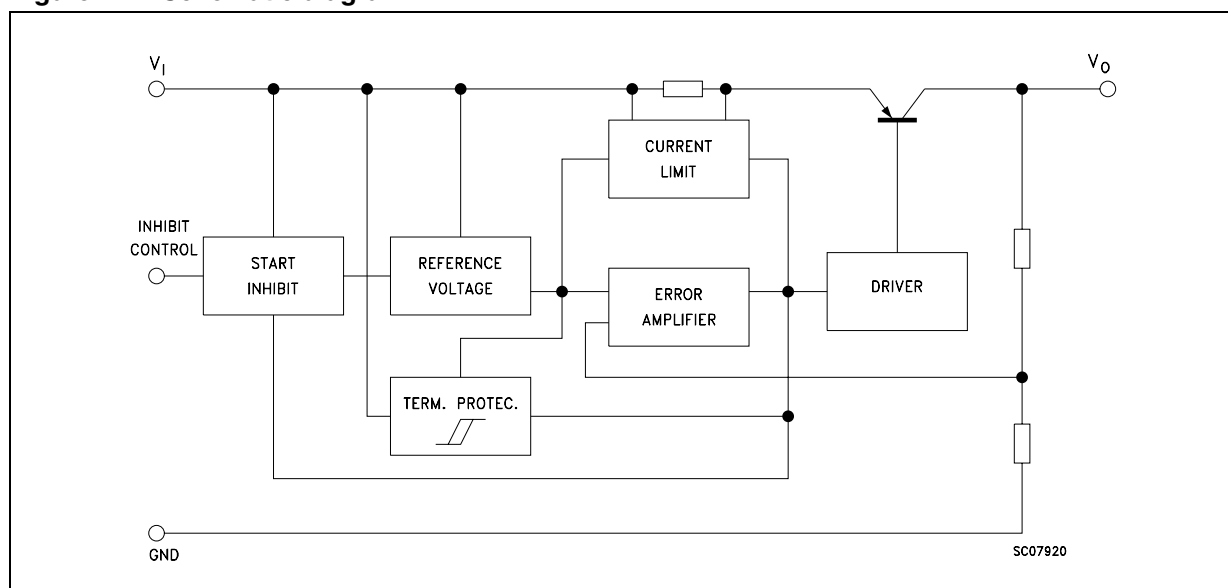
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1 Diagram

Figure 1. Schematic diagram



2 Pin configuration

Figure 2. Pin connections (top view)

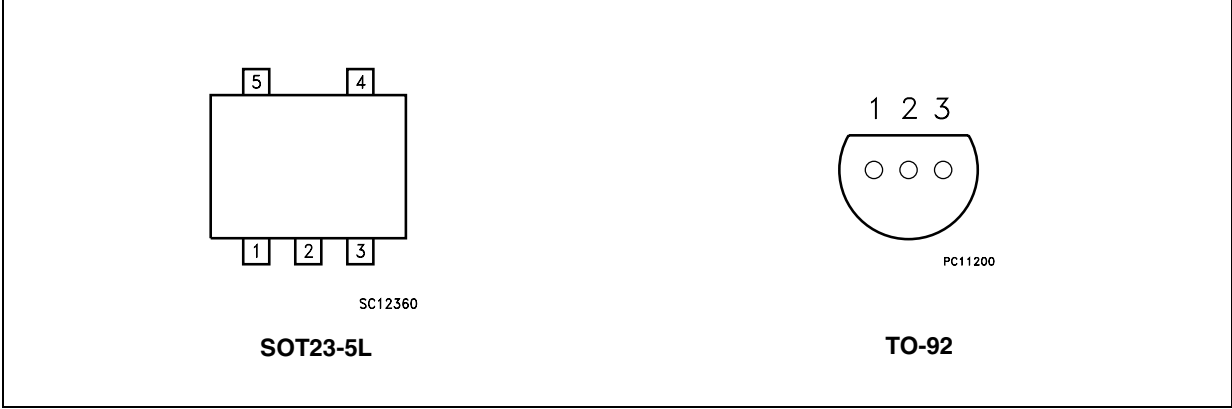


Table 1. Pin description

Symbol	Name and function	Pin number	
		SOT23-5L	TO-92
V_{IN}	Input voltage	1	3
GND	Ground	2	2
INHIBIT	Control switch ON/OFF ⁽¹⁾	3	
NC	Not to be connected	4	
V_{OUT}	Output voltage	5	1

1. Only for the version in SOT23-5L package: Inhibit pin is not internally pulled-up then it must not be left floating. Connect to a positive voltage higher than 2V to able the device.

3 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC Input voltage	16	V
V_{INH}	DC Inhibit input voltage	V_{IN}	V
I_O	Output current	Internally limited	
P_{tot}	Power dissipation	Internally limited	
T_{stg}	Storage temperature range	-40 to 150	°C
T_{op}	Operating junction temperature range	-25 to 125	°C

Note: Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

4 Electrical characteristics

Table 3. Electrical characteristics for LD2979 (refer to the test circuits, $T_a = 25^\circ\text{C}$, $V_{IN} = V_{O(NOM)} + 1\text{V}$, $I_O = 1\text{mA}$, $V_{INH} = 2\text{V}$ ⁽¹⁾, $C_O = 1\mu\text{F}$, unless otherwise specified).

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$V_{IN} = 3.85\text{ V}$	2.793	2.85	2.907	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	2.736		2.964	
V_O	Output voltage	$V_{IN} = 4\text{ V}$	2.940	3	3.060	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	2.880		3.120	
V_O	Output voltage	$V_{IN} = 4.2\text{ V}$	3.136	3.2	3.264	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	3.072		3.328	
V_O	Output voltage	$V_{IN} = 4.3\text{ V}$	3.234	3.3	3.366	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	3.168		3.432	
V_O	Output voltage	$V_{IN} = 4.8\text{ V}$	3.724	3.8	3.876	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	3.648		3.952	
V_O	Output voltage	$V_{IN} = 6\text{ V}$	4.9	5	5.1	V
		$I_O = 1\text{ to }50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$	4.8		5.2	
I_{out}	Output current limit		100			mA
ΔV_O	Line regulation	$V_{IN} = V_{O(NOM)} + 1\text{V to }16\text{V}$, $I_O = 1\text{mA}$			0.028	%/ V_{IN}
		$T_a = -25\text{ to }125^\circ\text{C}$			0.064	
I_d	Quiescent current (On Mode)	$I_O = 0$		80	110	μA
		$I_O = 0$, $T_a = -25\text{ to }125^\circ\text{C}$			170	
		$I_O = 50\text{mA}$		500	700	
		$I_O = 50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$			1300	
	Quiescent current (Off Mode) ⁽¹⁾	$V_{INH} < 0.18\text{ V}$		0		μA
		$V_{INH} < 0.18\text{ V}$, $T_a = -25\text{ to }125^\circ\text{C}$			1	
SVR	Supply voltage rejection	$I_O = 50\text{mA}$, $C_{OUT} = 10\mu\text{F}$, $f = 120\text{Hz}$		63		dB
V_d	Dropout voltage	$I_O = 0$		6	12	mV
		$I_O = 0$, $T_a = -25\text{ to }125^\circ\text{C}$			18	
		$I_O = 1\text{mA}$		30	60	
		$I_O = 1\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$			90	
		$I_O = 10\text{mA}$		100	200	
		$I_O = 10\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$			300	
		$I_O = 50\text{mA}$		200	400	
		$I_O = 50\text{mA}$, $T_a = -25\text{ to }125^\circ\text{C}$			600	
V_{IL}	Inhibit input logic low	Device Off, $T_a = -25\text{ to }125^\circ\text{C}$ (*)			0.18	V
V_{IH}	Inhibit input logic high	Device On, $T_a = -25\text{ to }125^\circ\text{C}$ (*)	2			V

Table 3. Electrical characteristics for LD2979 (refer to the test circuits, $T_a = 25^\circ\text{C}$, $V_{IN} = V_{O(NOM)} + 1\text{V}$, $I_O = 1\text{mA}$, $V_{INH} = 2\text{V}$ ⁽¹⁾, $C_O = 1\mu\text{F}$, unless otherwise specified).

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_I	Inhibit input current	$V_{INH} = 0\text{ V}$ ⁽¹⁾		0	-1	μA
		$V_{INH} = 5\text{V}$, $T_a = -25$ to 125°C		5	15	
eN	Output noise voltage (RMS)	BW= 300Hz to 50KHz, $C_O = 10\mu\text{F}$		160		μV

1. Only for types in SOT23-5L

5 Typical characteristics

(unless otherwise specified $T_A = 25^\circ\text{C}$).

Figure 3. Output voltage vs temperature

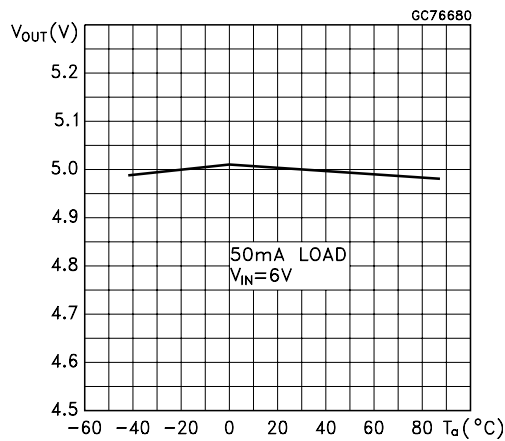


Figure 4. Output voltage vs input voltage

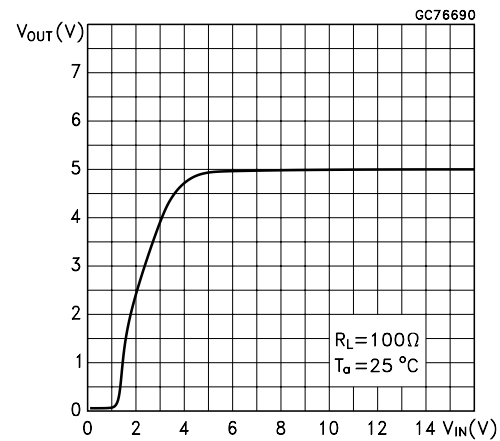


Figure 5. Output voltage vs input voltage

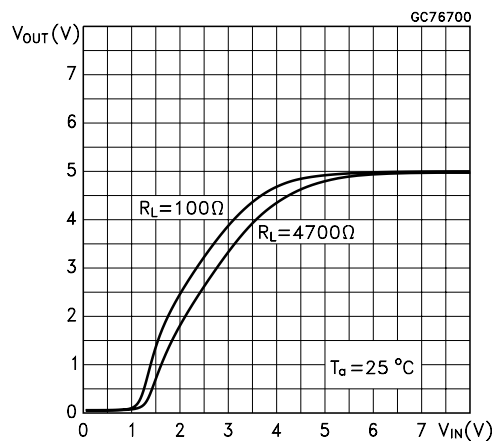


Figure 6. Dropout voltage vs output current

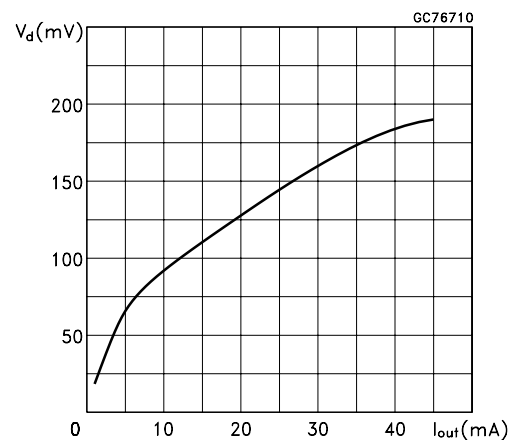


Figure 7. Dropout voltage vs temperature

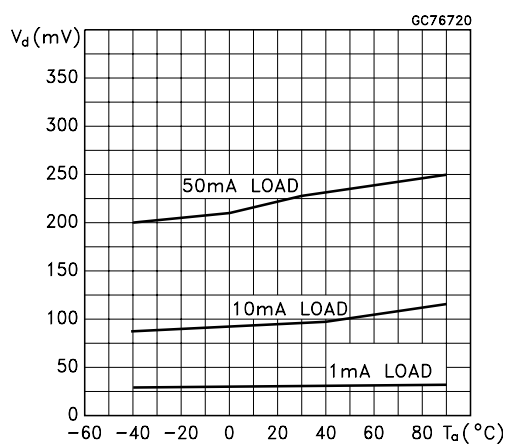


Figure 8. Quiescent current vs temperature

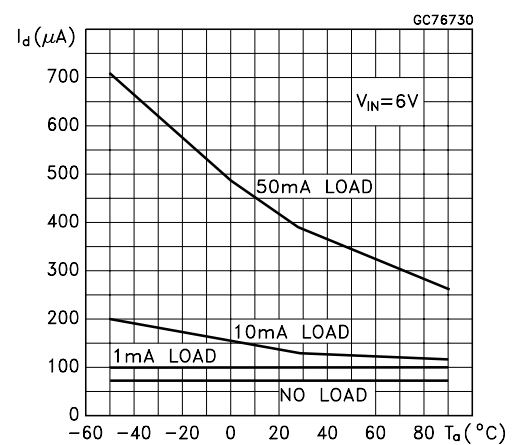
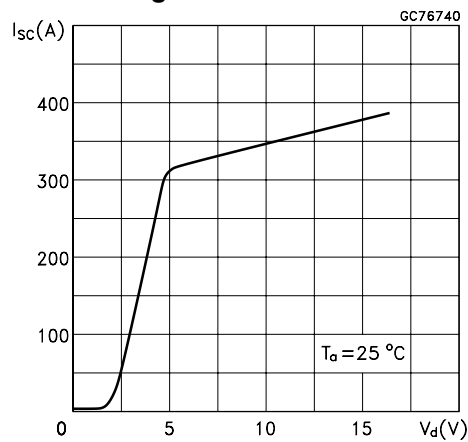
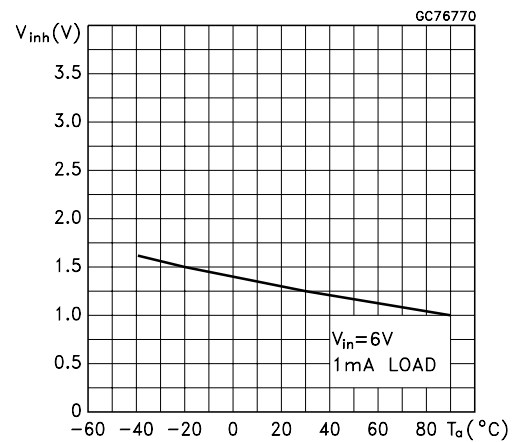
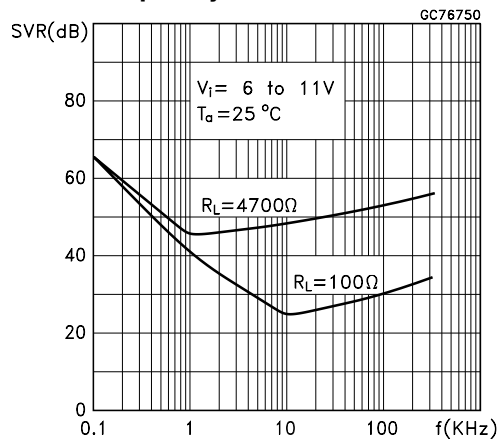
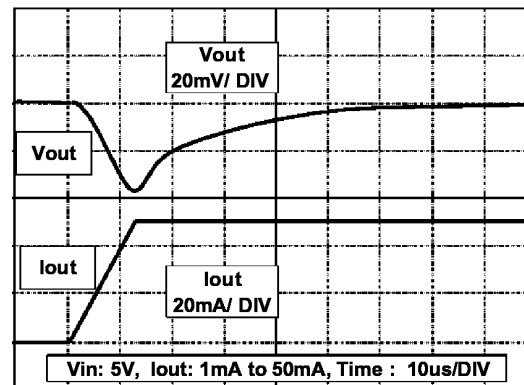
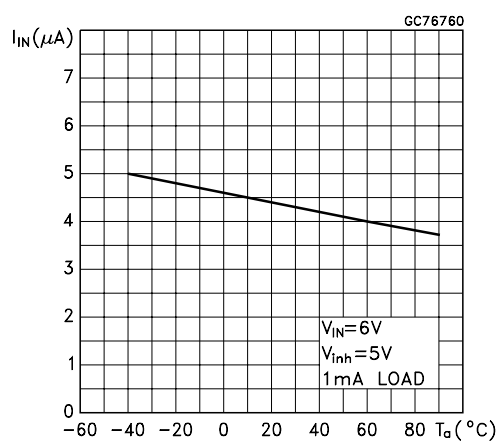
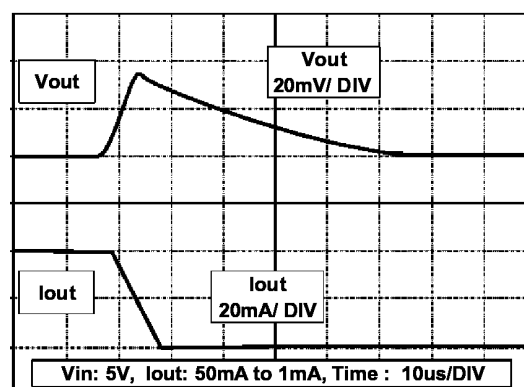


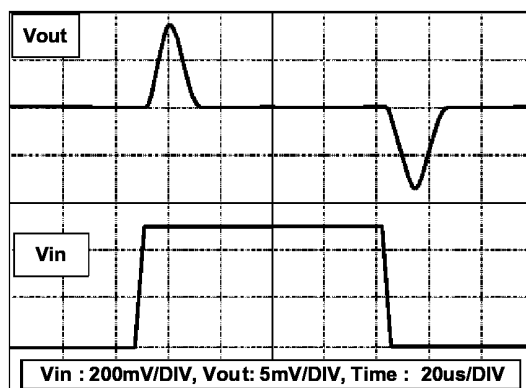
Figure 9. Short circuit current vs dropout voltage**Figure 10. Inhibit voltage vs temperature****Figure 11. Supply voltage rejection vs frequency****Figure 12. Load transient response**

$C_O = 10\mu F$ (ESR = 1 at 1KHz)

Figure 13. Inhibit current vs temperature**Figure 14. Load transient response**

$C_O = 10\mu F$ (ESR = 1 at 1KHz)

Figure 15. Line transient response



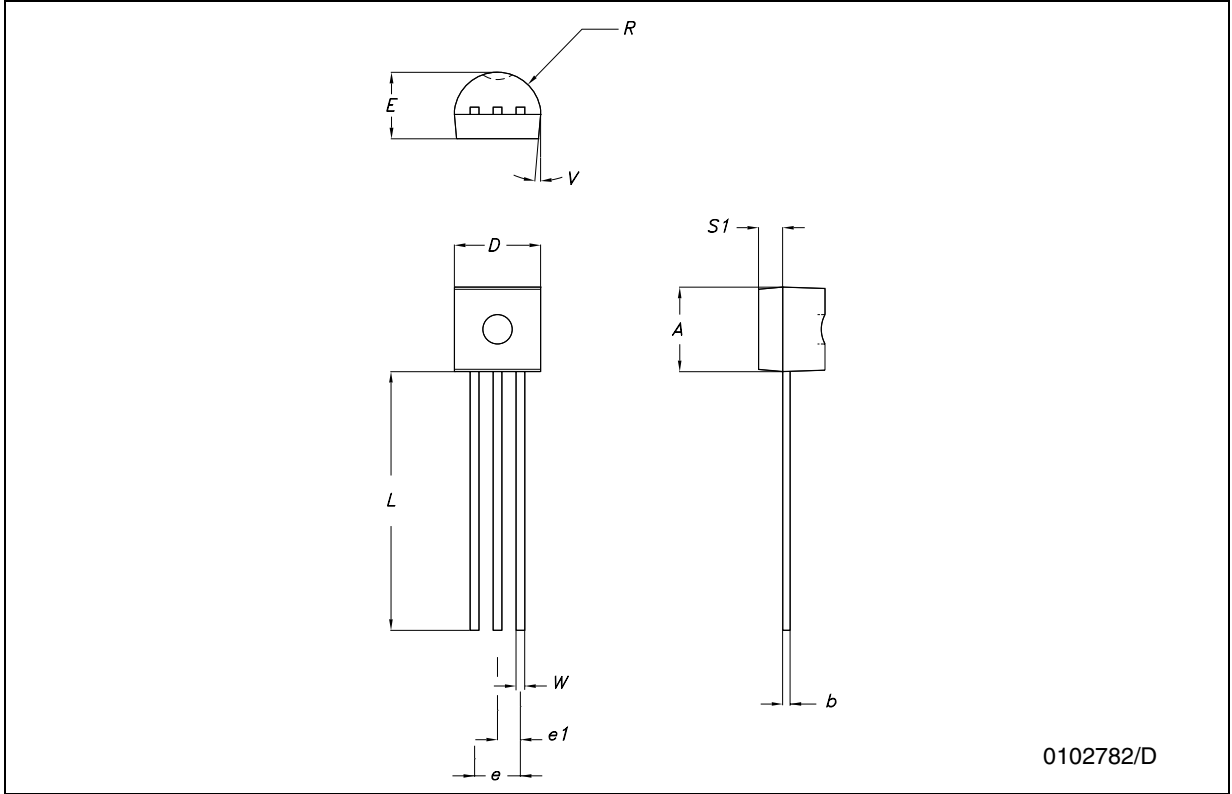
$C_O = 10\mu\text{F}$ (ESR = 1 at 1KHz)

6 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

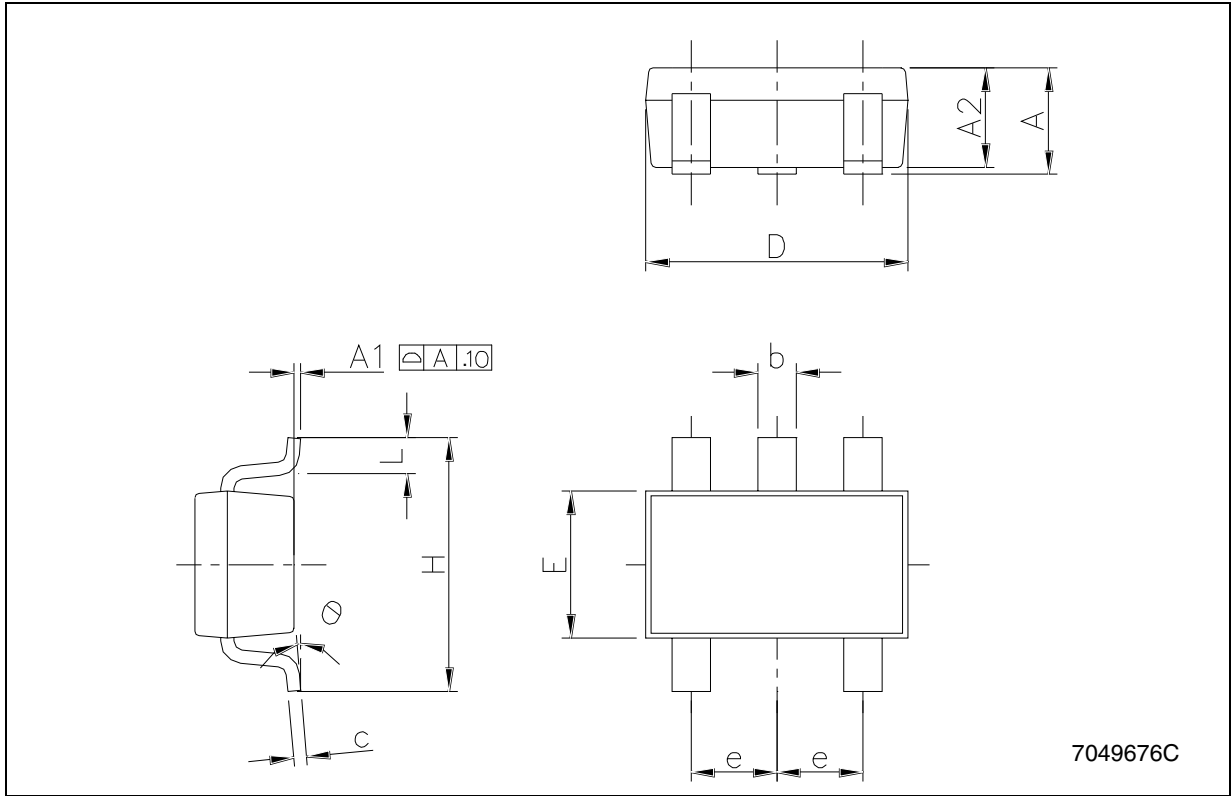
TO-92 MECHANICAL DATA

DIM.	mm.			mils		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.32		4.95	170.1		194.9
b	0.36		0.51	14.2		20.1
D	4.45		4.95	175.2		194.9
E	3.30		3.94	129.9		155.1
e	2.41		2.67	94.9		105.1
e1	1.14		1.40	44.9		55.1
L	12.7		15.49	500.0		609.8
R	2.16		2.41	85.0		94.9
S1	0.92		1.52	36.2		59.8
W	0.41		0.56	16.1		22.0
α		5°			5°	



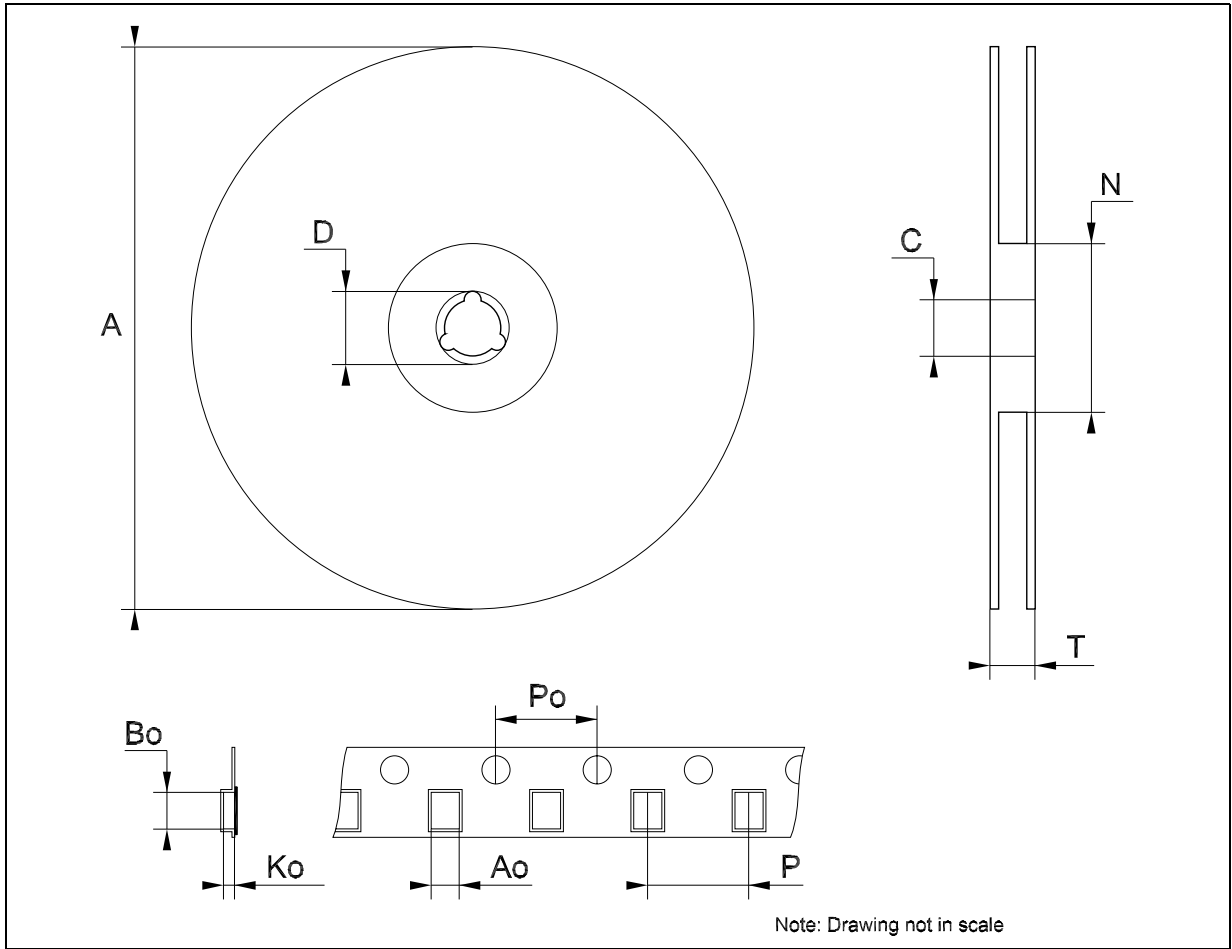
SOT23-5L MECHANICAL DATA

DIM.	mm.			mils		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	0.90		1.45	35.4		57.1
A1	0.00		0.10	0.0		3.9
A2	0.90		1.30	35.4		51.2
b	0.35		0.50	13.7		19.7
C	0.09		0.20	3.5		7.8
D	2.80		3.00	110.2		118.1
E	1.50		1.75	59.0		68.8
e		0.95			37.4	
H	2.60		3.00	102.3		118.1
L	0.10		0.60	3.9		23.6



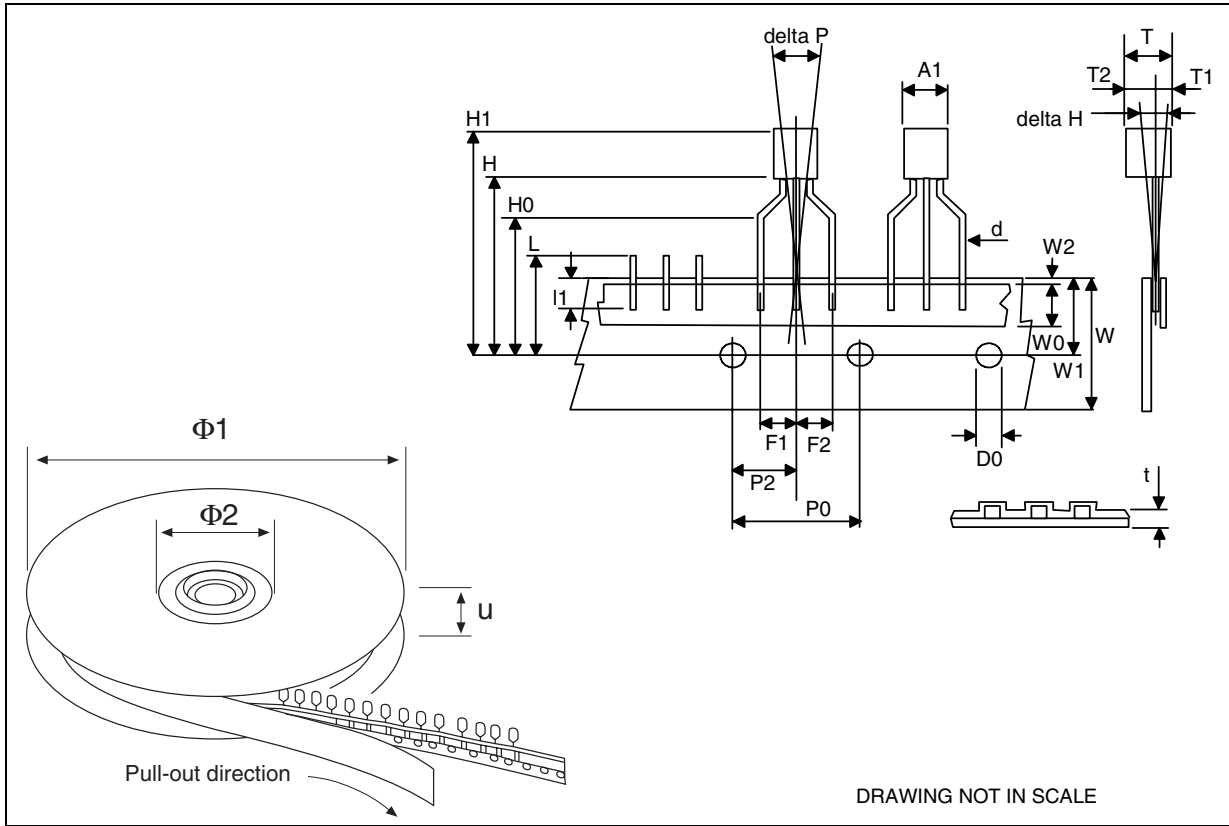
Tape & Reel SOT23-xL MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			180			7.086
C	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	60			2.362		
T			14.4			0.567
Ao	3.13	3.23	3.33	0.123	0.127	0.131
Bo	3.07	3.17	3.27	0.120	0.124	0.128
Ko	1.27	1.37	1.47	0.050	0.054	0.058
Po	3.9	4.0	4.1	0.153	0.157	0.161
P	3.9	4.0	4.1	0.153	0.157	0.161



Tape & Reel for TO-92 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A1		4.80			0.189	
T		3.80			0.150	
T1		1.60			0.063	
T2		2.30			0.091	
d		0.48			0.019	
P0	12.5		12.9	0.492		0.508
P2	5.65		7.05	0.222		0.278
F1, F2	2.44	2.54	2.94	0.096	0.100	0.116
delta H		±2			0.079	
W	17.5	18.00	19.0	0.689	0.709	0.748
W0	5.7		6.3	0.224		0.248
W1	8.5		9.25	0.335		0.364
W2		0.50			0.20	
H		18.50	18.70		0.728	0.726
H0	15.50		16.50	0.610		0.650
H1		25.00			0.984	
D0	3.8		4.2	0.150		0.165
t		0.90			0.035	
L1		3			0.118	
delta P		±1			0.039	
u		50			1.968	
Φ1		360			14.173	
Φ2		30			1.181	



DRAWING NOT IN SCALE

7 Revision history

Table 4. Revision history

Date	Revision	Changes
15-Mar-2005	10	Add Tape & Reel for TO-92.
03-Jul-2006	11	Order Codes has been updated and new template.

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