

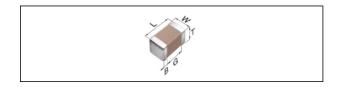
☆TDK Multilayer Ceramic Chip Capacitors

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Creation Date: August 10, 2013

C3225X6S0J107M250AC

Applications Commercial Grade Feature General General (Up to 50V) C3225 [EIA CC1210] Series







Size	
Length(L)	3.20mm +/-0.40mm
Width(W)	2.50mm +/-0.30mm
Thickness(T)	2.50mm +/-0.30mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	

Electrical Characteristics	
Capacitance	100uF +/-20%
Rated Voltage	6.3Vdc
Temperature Characteristic	X6S(+/-22%)
Dissipation Factor	15% Max.
Insulation Resistance	1MΩ Min.

Other	
Soldering Method	Reflow
AEC Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	1000 Pcs Min.

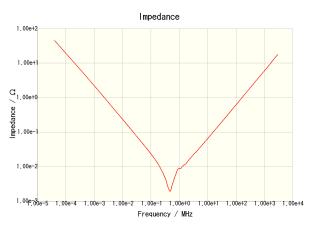
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[•]All specifications are subject to change without notice.

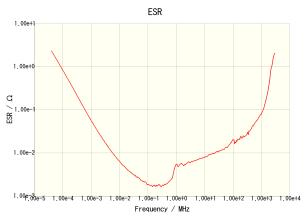
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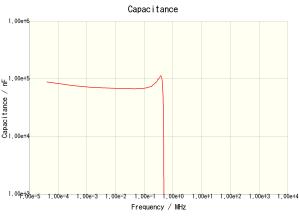
Characteristic Graphs



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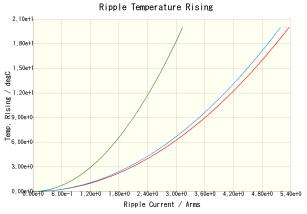
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C3225X6S0J107M250AC



C3225X6S0J107M250AC(DC Bias = 0V)3225X6S0J107M250AC(DC Bias = 3.15V)



C3225X6S0J107M250AC(100kHz)

C3225X6S0J107M250AC(500kHz)

C3225X6S0J107M250AC(1MHz)

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