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68HC711 Microcontrollers

68HC711 Microcontrollers
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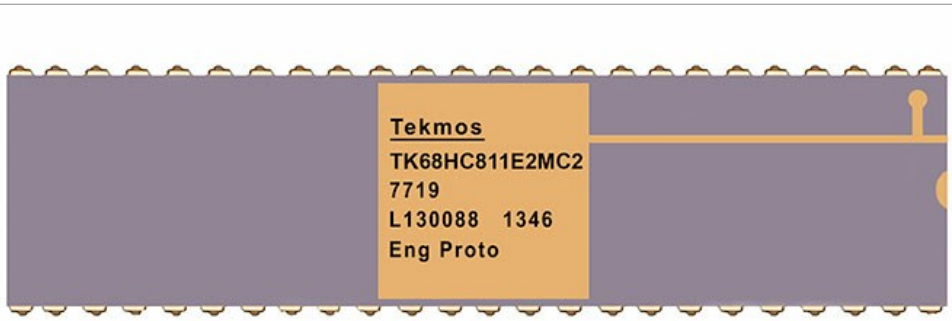

TK68HC11D0 Microcontroller

Request for Product Information

To request information on the TK68HC11D0 click this button: Request Information

Features

- Expanded 16-bit timer system with four-stage programmable prescaler
- Non-return-to-zero (NRZ) serial communications interface (SCI)
- Power-saving stop and wait modes
- 64 Kbytes memory addressability
- Multiplexed address/data bus
- Serial peripheral interface (SPI)
- 4 Kbytes of one-time programmable read-only memory (OTPROM)
- 8-bit pulse accumulator circuit
- 192 bytes of static random-access memory (RAM) (all saved during standby)
- Real-time interrupt (RTI) circuit
- Computer operating properly (COP) watchdog system
- Available in these packages:
  - 40-pin plastic dual in-line package (DIP)
  - 44-pin plastic leaded chip carrier (PLCC)
  - 44-pin plastic quad flat pack (QFP)

General Description

The TK68HC711D3 contains highly sophisticated on-chip peripheral functions. This high-speed, low-power programmable read-only memory (PROM) MCU has a nominal bus speed of 3 MHz. The fully static design allows operations at frequencies down to dc.

The TK68HC11D3 and TK68HC11D0 are read-only memory (ROM) based high-performance microcontrollers (MCU) based on the TK68HC11E9 design. The TK68L11D0 is an extended-voltage version of the TK68HC11D0 that can operate in applications that require supply voltages as low as 3.0 V.

Documentation

The data sheet is available here: TK68HC11D (1.14 MB)

Product Change Notice Form: PCN 17002 (285 KB)

Note: the information in the pdf file pertains to all the devices with the exceptions noted in Appendix A TK68HC11D3 and TK68HC11D0 and Appendix B TK68L11D0.

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