

# DS14C232

#### Low Power +5V Powered TIA/EIA-232 Dual Driver/Receiver

The DS14C232 is a low power dual driver/receiver featuring an onboard DC to DC converter, eliminating the need for  $\pm 12V$  power supplies. The device only requires a  $\pm 5V$  power supply. I<sub>CC</sub> is specified at 3.0 mA maximum, making the device ideal for battery and power conscious applications. The drivers' slew rate is set internally and the receivers feature internal noise filtering, eliminating the need for external slew rate and filter capacitors. The device is designed to interface data terminal equipment (DTE) with data circuit-terminating equipment (DCE). The driver inputs and receiver outputs are TTL and CMOS compatible. DS14C232C driver outputs and receiver inputs meet TIA/EIA-232-E (RS-232) and CCITT V.28 standards.

# Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

### **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
  - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



#### DS14C232

# Low Power +5V Powered TIA/EIA-232 Dual Driver/Receiver

#### **General Description**

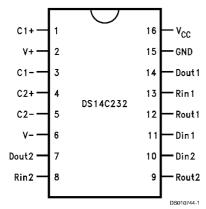
The DS14C232 is a low power dual driver/receiver featuring an onboard DC to DC converter, eliminating the need for ±12V power supplies. The device only requires a +5V power supply. I<sub>CC</sub> is specified at 3.0 mA maximum, making the device ideal for battery and power conscious applications. The drivers' slew rate is set internally and the receivers feature internal noise filtering, eliminating the need for external slew rate and filter capacitors. The device is designed to interface data terminal equipment (DTE) with data circuit-terminating equipment (DCE). The driver inputs and receiver outputs are TTL and CMOS compatible. DS14C232C driver outputs and receiver inputs meet TIA/EIA-232-E (RS-232) and CCITT V.28 standards.

#### **Features**

- Pin compatible with industry standard MAX232, LT1081, ICL232 and TSC232
- Single +5V power supply
- Low power—I<sub>CC</sub> 3.0 mA maximum
- DS14C232C meets TIA/EIA-232-E (RS-232) and CCITT V.28 standards
- CMOS technology
- Receiver Noise Filter
- Package efficiency 2 drivers and 2 receivers
- Available in Plastic DIP, Narrow and Wide SOIC packages
- TIA/EIA-232 compatible extended temperature range option:

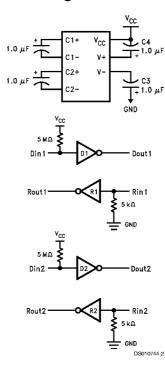
DS14C232T -40°C to +85°C DS14C232E/J: -55°C to +125°C

#### **Connection Diagram**



Order Number DS14C232CN, DS14C232CM, or DS14C232TM
See NS Package Number N16E, or M16A

#### **Functional Diagram**



#### Absolute Maximum Ratings (Note 1)

Specifications for the 883 version of this product are isted separately on the following pages.

Supply Voltage, V<sub>CC</sub> -0.3V to 6V  $(V_{CC} - 0.3)V$  to +14V V<sup>+</sup> Pin V- Pin +0.3V to -14V Driver Input Voltage -0.3V to  $(V_{CC} + 0.3V)$  $(V^+ + 0.3V)$  to  $(V^- - 0.3V)$ Driver Output Voltage Receiver Input Voltage

Receiver Output Voltage -0.3V to (V<sub>CC</sub> + 0.3V) +150°C Junction Temperature

Maximum Package Power Dissipation @ 25°C (Note 6) N Package 1698 mW M Package 1156 mW

Short Circuit Duration,

Continuous  $D_{\text{OUT}}$ 

Storage Temp. Range -65°C to +150°C Lead Temp. (Soldering, 4 +260°C

ESD Rating

sec.)

(HBM, 1.5 kΩ, 100 pF) ≥ 2.5 kV

#### **Recommended Operating Conditions**

	Min	Max	Units
Supply Voltage, V <sub>CC</sub>	4.5	5.5	V
Operating Free Air Temp. (T <sub>A</sub> )			
DS14C232C	0	+70	°C
DS14C232T	-40	+85	°C

#### **Electrical Characteristics** (Note 2)

Over recommended operating conditions, unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Units
DC TO	DC CONVERTER CHARACTER	ISTICS					
V+	Positive Power Supply	$R_L = 3 \text{ k}\Omega, C1-C4 = 1.0 \text{ k}$	μF, D <sub>IN</sub> = 0.8V		9.0		V
V-	Negative Power Supply	$R_L = 3 \text{ k}\Omega, C1-C4 = 1.0 \text{ k}$	ιF, D <sub>IN</sub> = 2.0V		-8.5		V
I <sub>cc</sub>	Supply (V <sub>CC</sub> ) Current	No Load			1.0	3.0	mA
DRIVE	R CHARACTERISTICS						
V <sub>IH</sub>	High Level Input Voltage			2		V <sub>cc</sub>	V
V <sub>IL</sub>	Low Level Input Voltage			GND		0.8	V
I <sub>IH</sub>	High Level Input Current	V <sub>IN</sub> ≥ 2.0V		-10		+10	μΑ
I <sub>IL</sub>	Low Level Input Current	V <sub>IN</sub> ≤ 0.8V		-10		+10	μΑ
V <sub>OH</sub>	High Level Output Voltage	$R_L = 3 k\Omega$	$R_L = 3 \text{ k}\Omega$		8.0		V
V <sub>OL</sub>	Low Level Output Voltage	$R_L = 3 k\Omega$			-7.0	-5.0	V
I <sub>OS+</sub>	Output High Short Circuit Current	$V_{O} = 0V, V_{IN} = 0.8V$	(Note 3)	-30	-15	-5.0	mA
l <sub>os-</sub>	Output Low Short Circuit Current	$V_O = 0V$ , $V_{IN} = 2V$		5.0	11	30	mA
Ro	Output Resistance	$-2V \le V_O \le +2V,$ $V_{CC} = 0V = GND$		300			Ω
RECEIV	VER CHARACTERISTICS						
$V_{TH}$	Input High Threshold Voltage	V <sub>CC</sub> = 5.0V			1.9	2.4	V
		$V_{CC} = 5.0V \pm 10\%$			1.9	2.6	V
V <sub>TL</sub>	Input Low Threshold Voltage			0.8	1.5		٧
V <sub>HY</sub>	Hysteresis			0.2	0.4	1.0	V
R <sub>IN</sub>	Input Resistance		-15V ≤ V <sub>IN</sub> ≤ +15V	3.0	4.7	7.0	kΩ
I <sub>IN</sub>	Input Current	V <sub>IN</sub> = +15V		+2.14	+3.75	+5.0	mA
		V <sub>IN</sub> = +3V	1	+0.43	+0.64	+1.0	mA
		V <sub>IN</sub> = -3V	1	-1.0	-0.64	-0.43	mA
		V <sub>IN</sub> = -15V	1	-5.0	-3.75	-2.14	mA
V <sub>OH</sub>	High Level Output Voltage	$V_{IN} = -3V$ , $I_{O} = -3.2$ mA	•	3.5	4.5		V
		$V_{IN} = -3V$ , $I_{O} = -20 \mu A$		4.0	4.9		l v
V <sub>OL</sub>	Low Level Output Voltage	$V_{IN} = +3V, I_{O} = +3.2 \text{ mA}$			0.15	0.4	T v

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### **Switching Characteristics**

Over recommended operating conditions, unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
DRIVER	CHARACTERISTICS			•	•		•
t <sub>PLH</sub>	Propagation Delay Low to High	$R_{L} = 3 \text{ k}\Omega$ $C_{L} = 50 \text{ pF}$	Figure 1 and		1.0	4.0	μs
t <sub>PHL</sub>	Propagation Delay High to Low		Figure 2		1.0	4.0	μs
t <sub>sk</sub>	Skew  t <sub>PLH</sub> - t <sub>PHL</sub>				0.1	1.0	μs
SR1	Output Slew Rate	$R_L = 3 \text{ k}\Omega \text{ to } 7 \text{ k}\Omega, C_L = 50 \text{ pF}$	(Note 7)	4.0		30	V/µs
SR2	Output Slew Rate	$R_L = 3 \text{ k}\Omega, C_L = 2500 \text{ pF}$	1		4.5		V/µs
RECEIVE	ER CHARACTERISTICS			•			
t <sub>PLH</sub>	Propagation Delay Low to High	Input Pulse Width > 10 μs			2.9	6.5	μs
t <sub>PHL</sub>	Propagation Delay High to Low	$C_L = 50 \text{ pF}$			2.5	6.5	μs
t <sub>sk</sub>	Skew  t <sub>PLH</sub> - t <sub>PHL</sub>	(Figures 3, 4)			0.4	2.0	μs
t <sub>nw</sub>	Noise Pulse Width Rejected	(Figures 3, 4)			0.7	0.5	μs

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" specify conditions for device operation.

Note 2: Current into device pins is defined as positive. Current out of device pins is defined as negative. All voltages are referenced to ground unless otherwise specified.

Note 3: IOS, and IOS, values are for one output at a time. If more than one output is shorted simultaneously, the device power dissipation may be exceeded.

Note 4: Receiver AC input waveform for test purposes:  $t_f = t_f = 200$  ns,  $V_{IH} = 3V$ ,  $V_{IL} = -3V$ , f = 30 kHz.

Note 5: All typicals are given for V<sub>CC</sub> = 5.0V.

Note 6: Ratings apply to ambient temperature at +25°C. Above this temperature derate: N Package 15.6 mW/°C, and M Package 10.6 mW/°C.

Note 7: Slew rate is defined as ΔV/Δt, measured between ±3V level.

#### Absolute Maximum Ratings (Note 1)

For complete Military Product Specifications, refer to the appropriate SMD or MDS.  $\label{eq:model} % \begin{center} \end{center} \begin{center} \end{center} % \begin{center} \end{cen$ 

Receiver Output Voltage -0.3V to (V<sub>CC</sub> + 0.3V)

Maximum Package Power Dissipation @ 25°C (Note 8)

J Package 1520 mW E Package 2000 mW Short Circuit Duration, D<sub>OUT</sub> Continuous Storage Temp. Range  $-65^{\circ}$ C to +150 $^{\circ}$ C Lead Temp. (Soldering, 4 sec.) +260 $^{\circ}$ C ESD Rating (HMB, 1.5 k $\Omega$ , 100 pF) ≥ 2.5 kV

# Recommended Operating Conditions

	Min	Max	Units
Supply Voltage, V <sub>CC</sub>	4.5	5.5	V
Operating Free Air Temp. (T <sub>A</sub> )			
DS14C232E/J	-55	+125	°C

#### **Electrical Characteristics** (Note 2)

Over recommended operating conditions, unless otherwise specified

Symbol	Parameter	Conditions		Min	Max	Units
DEVICE	CHARACTERISTICS (C1-C4 = 1.0 μF)			•		
Icc	Supply (V <sub>CC</sub> ) Current	No Load			8.0	mA
DRIVER	CHARACTERISTICS			•	•	
V <sub>IH</sub>	High Level Input Voltage			2		٧
V <sub>IL</sub>	Low Level Input Voltage				0.8	٧
I <sub>IH</sub>	High Level Input Current	V <sub>IN</sub> ≥ 2.0V			100	μΑ
IIL	Low Level Input Current	V <sub>IN</sub> = 0V			100	μΑ
V <sub>OH</sub>	High Level Output Voltage	$R_L = 3 \text{ k}\Omega$		5.0		٧
V <sub>OL</sub>	Low Level Output Voltage	$R_L = 3 \text{ k}\Omega$			-5.0	٧
I <sub>os+</sub>	Output High Short Circuit Current	V <sub>O</sub> = 0V	(Note 3)	-25		mA
I <sub>os</sub> _	Output Low Short Circuit Current	V <sub>O</sub> = 0V	1		25	mA
Ro	Output Resistance	$-2V \le V_O \le +2V, T_A = 25^{\circ}C,$		300		Ω
		V <sub>CC</sub> = 0V = GND				
RECEIV	ER CHARACTERISTICS (C1-C4 = 1.0	μF)		•	•	
V <sub>TH</sub>	Input High Threshold Voltage				3.0	٧
V <sub>TL</sub>	Input Low Threshold Voltage			0.2		٧
V <sub>HY</sub>	Hysteresis	T <sub>A</sub> = 25°C, +125°C		0.1	1.0	٧
		$T_A = -55^{\circ}C$		0.05	1.0	٧
R <sub>IN</sub>	Input Resistance	V <sub>IN</sub> = ±3V and ±15V, T <sub>A</sub> = 25°C		3.0	7.0	kΩ
V <sub>OH</sub>	High Level Output Voltage	I <sub>O</sub> = -3.2 mA		3.5		٧
		I <sub>O</sub> = -20 μA		4.0		٧
V <sub>OL</sub>	Low Level Output Voltage	I <sub>O</sub> = +3.2 mA			0.4	V

#### **Switching Characteristics**

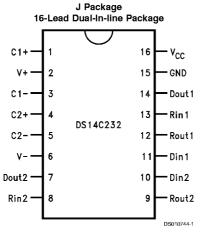
Over recommended operating conditions, unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Units
DRIVER C	DRIVER CHARACTERISTICS (C1–C4 = 1.0 μF)					
t <sub>PLH</sub>	Propagation Delay Low to High	$R_L = 3 \text{ k}\Omega, C_L = 50 \text{ pF}$	Figures 1, 2		4.0	μs
t <sub>PHL</sub>	Propagation Delay High to Low				4.0	μs
t <sub>sk</sub>	Skew  t <sub>PLH</sub> - t <sub>PHL</sub>				1.0	μs
SR1	Output Slew Rate	$R_L = 3 \text{ k}\Omega \text{ to } 7 \text{ k}\Omega, C_L = 2500 \text{ pF}$	(Note 7)	1.5	30	V/µs
RECEIVER CHARACTERISTICS (C1–C4 = 1.0 μF)						
t <sub>PLH</sub>	Propagation Delay Low to High	Input Pulse Width > 10 μs			8.0	μs
t <sub>PHL</sub>	Propagation Delay High to Low	$C_L = 50 pF$			8.0	μs
t <sub>sk</sub>	Skew  t <sub>PLH</sub> - t <sub>PHL</sub>	(Figures 3, 4)			2.0	μs

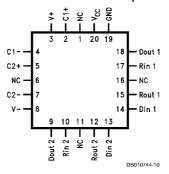
#### **Switching Characteristics** (Continued)

Note 8: Ratings apply to ambient temperature at +25°C. Above this temperature derate: J Package 12.2 mW/°C and E Package 13.3 mW/°C.

#### **Connection Diagrams**



#### E Package 20-Lead Ceramic Leadless Chip Carrier



For Complete Military Product Specifications See MDS or SMD. Order Number DS14C232J/883 or DS14C232E/883 See NS Package Number E20A or J16A

#### **Parameter Measurement Information**

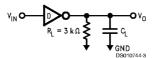


FIGURE 1. Driver Load Circuit

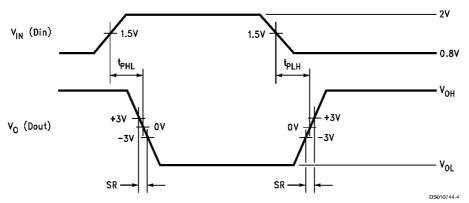


FIGURE 2. Driver Switching Waveform

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#### Parameter Measurement Information (Continued)

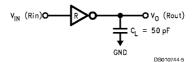


FIGURE 3. Receiver Load Circuit

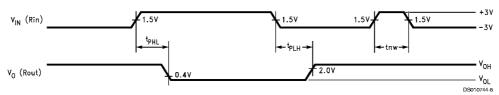


FIGURE 4. Receiver Propagation Delays and Noise Rejection (Note 4)

#### **Pin Descriptions**

#### V<sub>CC</sub> (Pin 16)

Power supply pin for the device, +5V (±10%).

#### V+ (Pin 2)

Positive supply for TIA/EIA-232-E drivers. Recommended external capacitor: C4-1.0  $\mu\text{F}$  (6.3V). Capacitor value should be larger than 1  $\mu\text{F}$ . This supply is not intended to be loaded externally.

#### V- (Pin 6)

Negative supply for TIA/EIA-232-E drivers. Recommended external capacitor: C3-1.0  $\mu$ F (16V). Capacitor value should be larger than 1  $\mu$ F. This supply is not intended to be loaded externally.

#### C1+, C1- (Pins 1, 3)

External capacitor connection pins. Recommended capacitor: 1.0  $\mu F$  (6.3V). Capacitor value should be larger than 1  $\mu F$ 

#### C2+, C2- (Pins 4, 5)

External capacitor connection pins. Recommended capacitor: 1.0  $\mu F$  (16V). Capacitor value should be greater than 1  $\mu F$ .

#### D<sub>IN</sub>1, D<sub>IN</sub>2 (Pins 11, 10)

Driver input pins are TTL/CMOS compatible. Inputs of unused drivers may be left open, an internal active pull-up resistor (500 k $\Omega$  minimum, typically 5 M $\Omega$ ) pulls input HIGH. Output will be LOW for open inputs.

#### D<sub>OUT</sub>1, D<sub>OUT</sub>2 (Pins 14, 7)

Driver output pins conform to TIA/EIA-232-E levels.

#### R<sub>IN</sub>1, R<sub>IN</sub>2 (Pins 13, 8)

Receiver input pins accept TIA/EIA-232-E input voltages ( $\pm 25$ V). Receivers feature a noise filter and guaranteed hysteresis of 100 mV. Unused receiver input pins may be left open. Internal input resistor 4.7 k $\Omega$  pulls input low, providing a failsafe high output.

#### R<sub>OUT</sub>1, R<sub>OUT</sub>2 (Pins 12, 9)

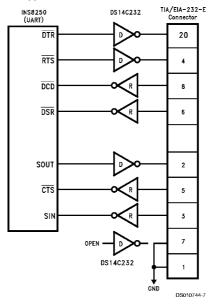
Receiver output pins are TTL/CMOS compatible. Receiver output HIGH voltage is specified for both CMOS and TTL load conditions.

#### GND (Pin 15)

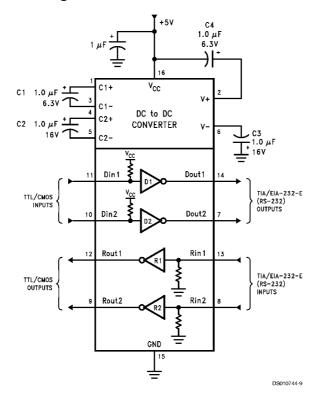
Ground Pin.

# **Typical Application Information**

#### Application of DS14C232 and INS8250

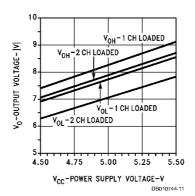


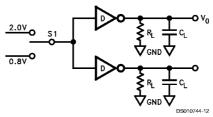
# **Typical Connection Diagram**



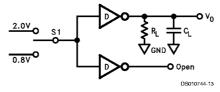
# **Typical Performance Characteristics**

DRIVER V<sub>OH</sub> & V<sub>OL</sub> vs POWER SUPPLY VOLTAGE





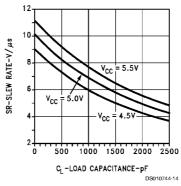
 $V_{CC}$  = 5.0V,  $R_L$  = 3  $k\Omega,\,C_L$  = 15 pF (includes jig and probe capacitance),  $C_P$  = 1  $\mu F$ 

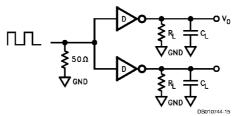


S1	٧o
2.0V	V <sub>OL</sub>
0.8V	٧٥٦

# **Typical Performance Characteristics** (Continued)

DRIVER SLEW RATE vs POWER SUPPLY VOLTAGE & LOAD CAPACITANCE

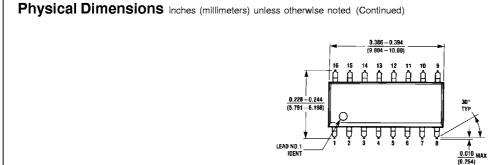


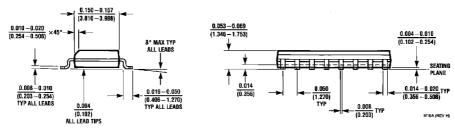


 $T_a$  = 25°C,  $R_L$  = 5 k $\Omega$ ,  $C_P$  = 1  $\mu F$ , f = 30 KHz

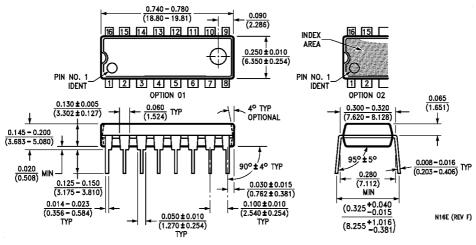


SR = 6V/t1 or 6V/t2, whichever is greater.





Order Number DS14C232CM or DS14C232TM NS Package Number M16A



Order Number DS14C232CN NS Package Number N16E