SLCS008A - OCTOBER 1979 - REVISED OCTOBER 1991

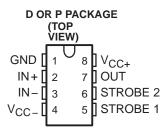
- Fast Response Times
- Improved Gain and Accuracy
- Fanout to 10 Series 54/74 TTL Loads
- Strobe Capability
- Short-Circuit and Surge Protection
- Designed to Be Interchangeable With National Semiconductor LM306

description

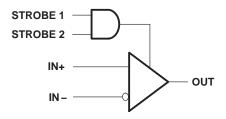
The LM306 is a high-speed voltage comparator with differential inputs, a low-impedance high-sink-current (100 mA) output, and two strobe inputs. This device detects low-level analog or digital signals and can drive digital logic or lamps and relays directly. Short-circuit protection and surge-current limiting is provided.

A low-level input at either strobe causes the output to remain high regardless of the differential input. When both strobe inputs are either open or at a high logic level, the output voltage is controlled by the differential input voltage. The circuit will operate with any negative supply voltage between -3 V and -12 V with little difference in performance.

The LM306 is characterized for operation from 0°C to 70°C.



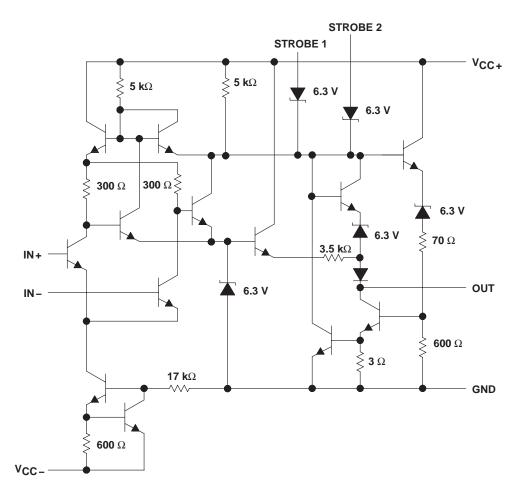
functional block diagram



AVAILABLE OPTIONS

	V	PACKAGE				
TA	V _{IO} max at 25°C	SMALL OUTLINE (D)	PLASTIC DIP (P)			
0°C to 70°C	5 mV	LM306D	LM306P			

schematic



Resistor values are nominal.

LM306 DIFFERENTIAL COMPARATOR WITH STROBES

SLCS008A - OCTOBER 1979 - REVISED OCTOBER 1991

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V _{CC+} (see Note 1)	15 V
Supply voltage, V _{CC} (see Note 1)	
Differential input voltage, V _{ID} (see Note 2)	±5 V
Input voltage, V _I (either input, see Notes 1 and 3)	±7 V
Strobe voltage range (see Note 1)	0 V to V _{CC+}
Output voltage, V _O (see Note 1)	24 V
Voltage from output to V _{CC} - · · · · · · · · · · · · · · · · · · ·	30 V
Duration of output short circuit to ground (see Note 4)	10 s
Continuous total dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A	
Storage temperature range	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages and the voltage from the output to V_{CC} -, are with respect to the network ground.

- 2. Differential voltages are at IN+ with respect to IN-.
- 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 7 V, whichever is less.
- 4. The output may be shorted to ground or either power supply.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{\scriptsize A}} \le 25^{\circ}\mbox{\scriptsize C}$ POWER RATING			T _A = 70°C POWER RATING
D	600 mW	5.8 mW/°C	46°C	464 mW
P	600 mW	8.0 mW/°C	75°C	600 mW

SLCS008A - OCTOBER 1979 - REVISED OCTOBER 1991

electrical characteristics at specified free-air temperature, V_{CC+} = 12 V, V_{CC-} = -3 V to -12 V (unless otherwise noted)

	PARAMETER	TEST CON	TEST CONDITIONS†			TYP	MAX	UNIT
				25°C		1.6§	5	.,
VIO	Input offset voltage	R _S ≤ 200 Ω		Full range			6.5	mV
ανιο	Average temperature coefficient of input offset voltage	$R_S = 50 \Omega$,	See Note 5	Full range		5	20	μV/°C
				25°C		1.8	5	
IIO	Input offset current	See Note 5		MIN		1	7.5	μΑ
				MAX		0.5	5	
	Average temperature coefficient of	Con Note 5		MIN to 25°C		24	100	- A /0C
αΙΙΟ	input offset current	See Note 5		25°C to MAX		15	50	nA/°C
	Leave biogramment	V 05V4-5V		MIN to 25°C			40	
lΒ	Input bias current	$V_0 = 0.5 \text{ V to 5 V}$	25°C to MAX		16	25	μΑ	
I _{IL} (S)	Low-level strobe current	V _(strobe) = 0.4 V		Full range		-1.7	-3.2	mA
V _{IH} (S)	High-level strobe voltage			Full range	2.2			V
V _{IL} (S)	Low-level strobe voltage			Full range			0.9	V
VICR	Common-mode input voltage range	$V_{CC} = -7 \text{ V to } -1$	12 V	Full range	±5			V
VID	Differential input voltage range			Full range	±5			V
A _{VD}	Large-signal differential voltage amplification	$V_{O} = 0.5 \text{ V to 5 V},$	No load	25°C		40		V/mV
Vон	High-level output voltage	$I_{OH} = -400 \mu A$	$V_{ID} = 8 \text{ mV}$	Full range	2.5		5.5	V
		I _{OL} = 100 mA	$V_{ID} = -7 \text{ mV}$	25°C		0.8	2	
VOL	Low-level output voltage	I _{OL} = 50 mA	$V_{ID} = -7 \text{ mV}$	Full range			1	V
		I _{OL} = 16 mA	$V_{ID} = -8 \text{ mV}$	Full range		0.4		
			$V_D = 7 \text{ mV}$	MIN to 25°C		0.02	2	
ЮН	High-level output voltage	$V_{OH} = 8 \text{ V to } 24 \text{ V}$	$V_{ID} = 8 \text{ mV}$	25°C to MAX			100	μΑ
ICC+	Supply current from V _{CC+}	$V_{ID} = -5 \text{ mV},$	No load	Full range		6.6	10	mA
ICC-	Supply current from V _{CC} -	No load		Full range		-1.9	-3.6	mA

[†] Unless otherwise noted, all characteristics are measured with both strobes open.

NOTE 5: The offset voltages and offset currents given are the maximum values required to drive the output down to the low range (V_{OL}) or up to the high range (V_{OH}). These parameters actually define an error band and take into account the worst-case effects of voltage gain and input impedance.

switching characteristics, V_{CC+} = 12 V, V_{CC-} = -6 V, T_A = 25°C

PARAMETER	TEST CONDITIONS [†]	MIN	TYP	MAX	UNIT	
Response time, low-to-high-level output	$R_L = 390 \Omega$ to 5 V, $C_L = 15 pF$, See N	ote 6		28	40	ns

[†] All characteristics are measured with both strobes open.

NOTE 6: The response time specified is for a 100-mV input step with 5-mV overdrive and is the interval between the input step function and the instant when the output crosses 1.4 V.



[‡]Full range is 0°C to 70°C. MIN is 0°C. MAX is 70°C.

[§] This typical value is at $V_{CC+} = 12 \text{ V}$, $V_{CC-} = -6 \text{ V}$.

Table of Graphs

		_	FIGURE
I _{IB}	Input bias current	vs Free-air temperature	1
IIO	Input offset current	vs Free-air temperature	2
Vон	High-level output voltage	vs Free-air temperature	3
VOL	Low-level output voltage	vs Free-air temperature	4
VO	Output voltage	vs Differential input voltage	5
IO	Output current	vs Differential input voltage	6
AVD	Large-signal differential voltage amplification	vs Free-air temperature	7
los	Short-circuit output current	vs Free-air temperature	8
	Output response	vs Time	9, 10
ICC+	Positive supply current	vs Positive supply voltage	11
I _{CC} _	Negative supply current	vs Negative supply voltage	12
P_{D}	Total power dissipation	vs Free-air temperature	13

INPUT OFFSET CURRENT vs FREE-AIR TEMPERATURE

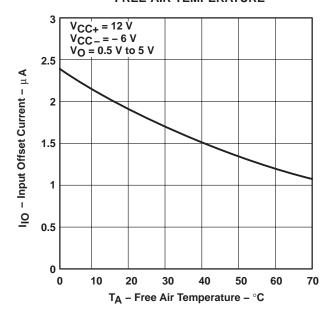


Figure 1

INPUT BIAS CURRENT vs FREE-AIR TEMPERATURE

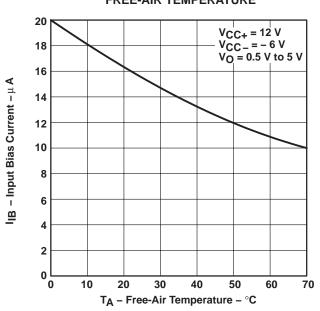


Figure 2

HIGH-LEVEL OUTPUT VOLTAGE vs FREE-AIR TEMPERATURE

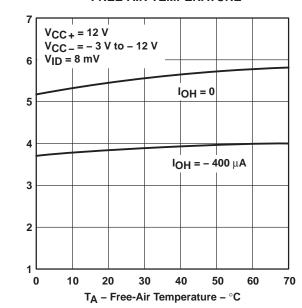


Figure 3

OUTPUT VOLTAGE vs DIFFERENTIAL INPUT VOLTAGE

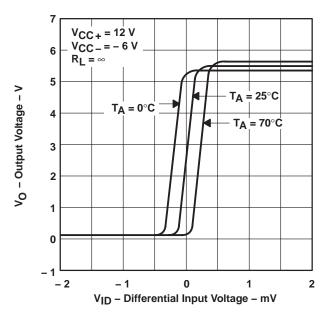


Figure 5

LOW-LEVEL OUTPUT VOLTAGE vs FREE-AIR TEMPERATURE

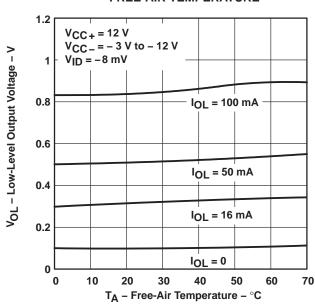


Figure 4

OUTPUT CURRENT vs DIFFERENTIAL INPUT VOLTAGE

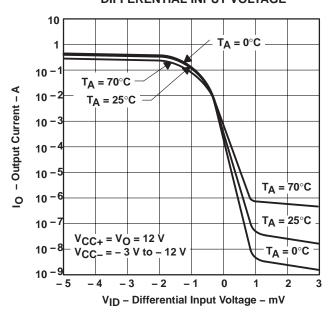
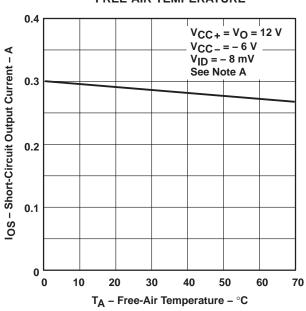


Figure 6

VOH - High-Level Output Voltage - V

LARGE-SIGNAL DIFFERENTIAL **VOLTAGE AMPLIFICATION** vs FREE-AIR TEMPERATURE 80,000 $V_{CC} = -3 \text{ V to } -12 \text{ V}$ V_O = 1 to 2 V $R_L = \infty$ A_{VD} - Large-Signal Differential 60,000 Voltage Amplification V_{CC+} = 15 V 40,000 $V_{CC+} = 10 V$ $V_{CC+} = 15 \text{ V}$ 20,000 0 0 10 20 30 40 50 60 70

SHORT-CIRCUIT OUTPUT CURRENT
vs
FREE-AIR TEMPERATURE



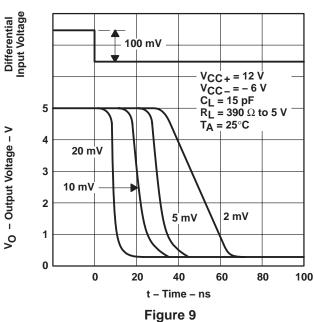
NOTE A: This parameter was measured using a single 5-ms pulse.

Figure 8

Figure 7



 T_A – Free-Air Temperature – $^{\circ}C$



OUTPUT RESPONSE FOR VARIOUS INPUT OVERDRIVES

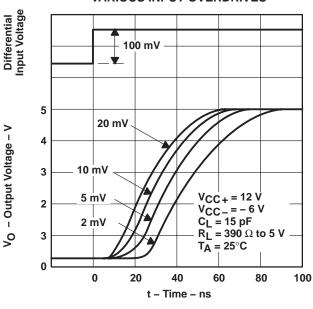


Figure 10

POSITIVE SUPPLY CURRENT POSITIVE SUPPLY VOLTAGE 10 VCC-= 3 V to - 12 V 9 **R**L = ∞ ICC+ - Positive Supply Current - mA $T_A = 25^{\circ}C$ 8 $V_{ID} = -5 \text{ mV}$ 7 6 5 $V_{ID} = 5 \text{ mV}$ 3 2 1 0 9 10 11 12 13 14 15 16 17

NEGATIVE SUPPLY CURRENT VS NEGATIVE SUPPLY VOLTAGE

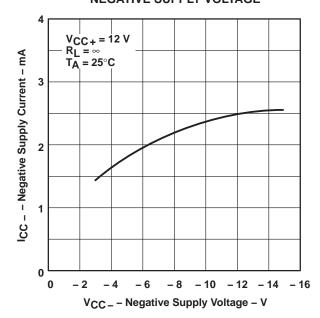


Figure 11

V_{CC+} - Positive Supply Voltage - V

Figure 12

TOTAL POWER DISSIPATION vs FREE-AIR TEMPERATURE

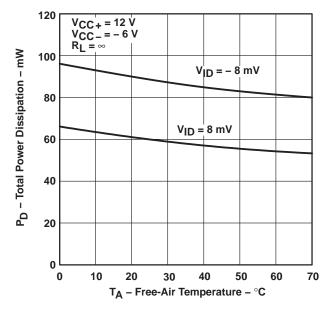


Figure 13





i.com 23-Apr-2007

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
LM306D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306DE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LM306P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
LM306PE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

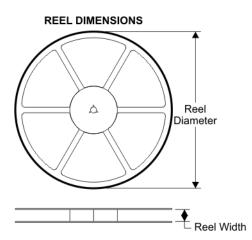
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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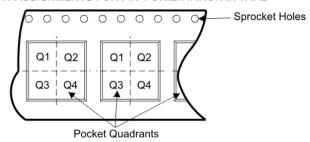
TAPE AND REEL BOX INFORMATION





	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



Device	Package	Pins		Reel Diameter (mm)	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM306DR	D	8	SITE 27	330	12	6.4	5.2	2.1	8	12	Q1





Ī	Device	Package	Package Pins		Length (mm)	Width (mm)	Height (mm)	
I	LM306DR	D	8	SITE 27	342.9	336.6	20.64	

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

For the latest package information, go to $http://www.ti.com/sc/docs/package/pkg_info.htm$

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.



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