RETOKO

THREE-TERMINAL VOLTAGE REGULATOR

FEATURES

- **■** Low Dropout Voltage
- Very Low Standby Current (No Load)
- **■** Good Load Regulation
- Internal Thermal Shutdown
- Short Circuit Protection
- 3% Output Voltage Accuracy
- **Customized Versions Are Available**

DESCRIPTION

The TK116xxU series devices are low power, linear 3-terminal regulators.

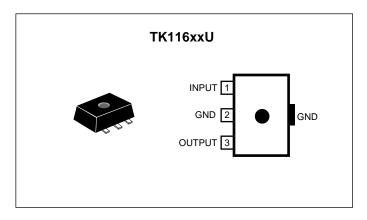
An internal PNP pass-transistor is used in order to achieve low dropout voltage (typically 160 mV at 80 mA load current).

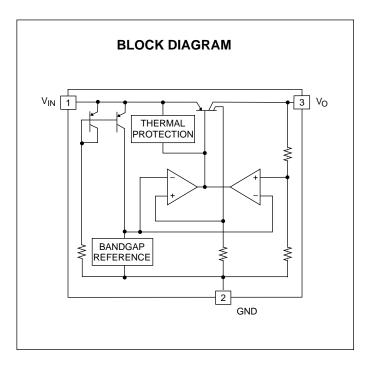
The regulated output voltages of 3, 3.3, 5 and 9 V are available. The device has very low $(400 \ \mu A)$ quiescent current with no load and 2 mA with 60 mA load.

An internal thermal shutdown circuit limits the junction temperature to below 150 $^{\circ}$ C. The load current is internally monitored and the device will shut down in the presence of a short circuit at the output.

APPLICATIONS

- **■** Battery Powered Systems
- **■** Portable Consumer Equipment
- Cordless Telephones
- **■** Personal Communications Equipment
- Portable Instrumentation
- Radio Control Systems
- Toys
- Low Voltage Systems





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ABSOLUTE MAXIMUM RATINGS

Supply Voltage 18 V	Storage Temperature Range55 to +150 °C
Operating Voltage Range 2.5 to 16 V	Operating Temperature Range (standard) -30 to +80 °C
Load Current	Lead Soldering Temp. (10 sec.) 240 °C
Power Dissipation (Note1)1,000 mW	Junction Temperature 150 °C

TK11630U ELECTRICAL CHARACTERISTICS

Test Conditions: $T_A = 25$ °C, $V_O = 4.0$ V, unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{IN}	Supply Current	$V_{IN} = 4.0 \text{ V}, I_{O} = 0 \text{ mA}$		400	800	μА
		$V_{IN} = 2.5 \text{ V}, I_{O} = 0 \text{ mA}$		0.8	2.0	mA
V _O	Regulated Output Voltage	V _{IN} = 4.0 V, I _O = 10 mA	2.9	3.0	3.1	V
V _{DROP}	Dropout Voltage	I _O = 30 mA		80	150	mV
		I _O = 100 mA		170	330	mV
Io	Output Current	V _{IN} = 4.0 V (Note 2)		190		mA
I _{OR}	Recommended Output Curren	t V _{IN} = 4.0 V			150	mA
I _Q	Quiescent Current	$V_{IN} = 4.0V, I_{O} = 60 \text{ mA}$		2.0	4.5	mA
Line Reg	Line Regulation	$V_{IN} = 4.0 \text{ V} \rightarrow 9.0 \text{ V}$		2.0	30	mV
Load Reg	Load Regulation	$I_O = 0 \text{ mA} \rightarrow 30 \text{ mA}, \ V_{IN} = 4.0 \text{ V}$		15	60	mV
		$I_O = 0 \text{ mA} \rightarrow 100 \text{ mA}, V_{IN} = 4.0 \text{ V}$		40	140	mV
		$I_O = 0 \text{ mA} \rightarrow 150 \text{ mA}, V_{IN} = 4.0 \text{ V}$		120	220	mV
RR	Ripple Rejection	100 mV(rms), f = 400 Hz,		55		dB
		$V_{IN} = 4.5 \text{ V}, I_{O} = 10 \text{ mA}$				
$\Delta V_{O}/\Delta T_{A}$	Temperature Coefficient	$V_{IN} = 4.5 \text{ V}, I_{O} = 10 \text{ mA},$		± .35		mV/°C
		-30 °C ≤ T _A ≤ 80 °C				

Note 1: Power dissipation must be derated at the rate of 4.8 mW/ $^{\circ}$ C for operation at T_A = 25 $^{\circ}$ C and above. Power dissipation is 1 W when mounted as recommended and must be derated at the rate of 8 mW/ $^{\circ}$ C for operation at T_A = 25 $^{\circ}$ C and above.

Note 2: I_o (Load Current) is current when V_o drops down 0.4 V from V_o at I_o = 10 mA.

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TK11633U ELECTRICAL CHARACTERISTICS

Test Conditions: $T_A = 25$ °C, $V_O = 4.3$ V, unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{IN}	Supply Current	$V_{IN} = 4.3 \text{ V}, I_{O} = 0 \text{ mA}$		400	800	μΑ
		$V_{IN} = 3.0 \text{ V}, I_{O} = 0 \text{ mA}$		0.8	2.0	mA
Vo	Regulated Output Voltage	V _{IN} = 4.3 V, I _O = 10 mA	3.2	3.3	3.4	V
V _{DROP}	Dropout Voltage	I _O = 30 mA		80	150	mV
		I _O = 100 mA		170	330	mV
Io	Output Current	V _{IN} = 4.3 V (Note 1)		190		mA
I _{OR}	Recommended Output Current	V _{IN} = 4.3 V			150	mA
IQ	Quiescent Current	$V_{IN} = 4.3V, I_{O} = 60 \text{ mA}$		2	4.5	mA
Line Reg	Line Regulation	V_{IN} =4.3 V \rightarrow 9.3 V		2	30	mV
Load Reg	Load Regulation	$I_O = 0 \text{ mA} \rightarrow 30 \text{ mA}, \ V_{IN} = 4.3 \text{V}$		15	60	mV
		$I_O = 0 \text{ mA} \rightarrow 100 \text{ mA}, \ V_{IN} = 4.3 \text{ V}$		40	140	mV
		$I_O = 0 \text{ mA} \rightarrow 150 \text{ mA}, \ V_{IN} = 4.3 \text{ V}$		120	220	mV
RR	Ripple Rejection	100 mV(rms), f = 400 Hz,		55		dB
		$V_{IN} = 4.8 \text{ V}, I_{O} = 10 \text{ mA}$				
$\Delta V_{O}/\Delta T_{A}$	Temperature Coefficient	$V_{IN} = 4.8 \text{ V}, I_{O} = 10 \text{ mA},$		± .35		mV/°C
		$-30 ^{\circ}\text{C} \le \text{T}_{\text{A}} \le 80 ^{\circ}\text{C}$				

Note 1: I_O (Load current) is current when V_O drops down 0.4 V from V_O at I_O = 10 mA

TK11650U ELECTRICAL CHARACTERISTICS

Test Conditions: $T_A = 25$ °C, $V_O = 6.0$ V, unless otherwise specified

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{IN}	Supply Current	$V_{IN} = 6.0 \text{ V}, I_{O} = 0 \text{ mA}$		400	800	μΑ
		V _{IN} = 4.0V, I _O = 0 mA		0.8	2.0	mA
Vo	Regulated Output Voltage	V _{IN} = 6.0 V, I _O = 10 mA	4.85	5.0	5.15	V
V _{DROP}	Dropout Voltage	I _O = 30 mA		80	150	mV
		I _O = 100 mA		170	330	mV
Io	Output Current	V _{IN} = 6.0 V (Note 1)		190		mA
I _{OR}	Recommended Output Current	V _{IN} = 6.0 V			150	mA
I _Q	Quiescent Current	$V_{IN} = 6.0 \text{ V}, I_{O} = 60 \text{ mA}$		2.0	4.5	mA
Line Reg	Line Regulation	$V_{IN} = 6.0 \text{ V} \rightarrow 11 \text{ V}$		2.0	30	mV
Load Reg	Load Regulation	$I_O = 0 \text{ mA} \rightarrow 30 \text{ mA}, \ V_{IN} = 6.0 \text{V}$		15	60	mV
		$I_O = 0 \text{ mA} \rightarrow 100 \text{ mA}, \ V_{IN} = 6.0 \text{ V}$		40	140	mV
		$I_O = 0 \text{ mA} \rightarrow 150 \text{ mA}, \ V_{IN} = 6.0 \text{ V}$		120	220	mV
RR	Ripple Rejection	100 mV(rms), f = 400 Hz,		55		dB
		$V_{IN} = 6.5 \text{ V}, I_{O} = 10 \text{ mA}$				
$\Delta V_{O}/\Delta T_{A}$	Temperature Coefficient	$V_{IN} = 6.5 \text{ V}, I_{O} = 10 \text{ mA},$ -30 °C \le T_{A} \le 80 °C		± .35		mV/°C

Note 1: I_O (Load current) is current when V_O drops down 0.4 V from V_O at I_O = 10 mA

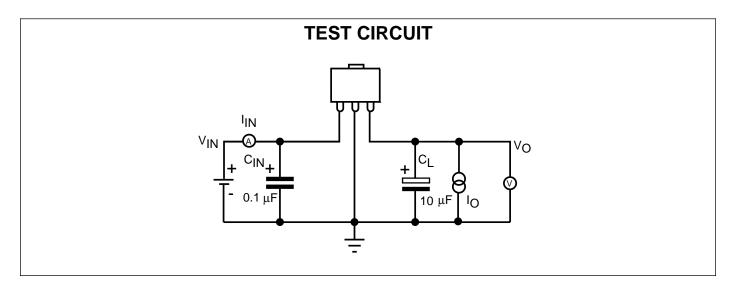
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TK11690U ELECTRICAL CHARACTERISTICS

Test Conditions: T_A = 25 °C, V_O = 10.0 V, unless otherwise specified

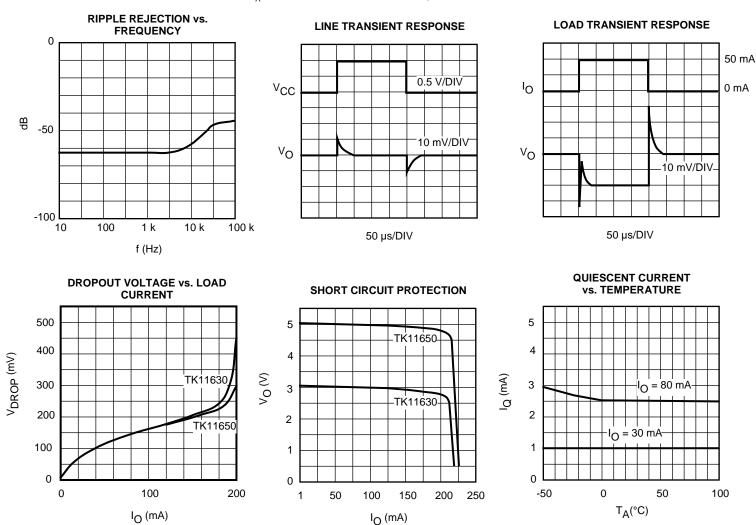
SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{IN}	Supply Current	V _{IN} =10 V, I _O = 0 mA		400	800	μΑ
		V _{IN} = 8.0 V, I _O = 0 mA		0.8	2.0	mA
Vo	Regulated Output Voltage	V _{IN} = 10 V, I _O = 10 mA	8.73	9.0	9.27	V
V _{DROP}	Dropout Voltage	I _O = 30 mA		80	150	mV
		I _O = 100 mA		170	330	mV
Io	Output Current	V _{IN} = 10.0 V (Note 1)		190		mA
I _{OR}	Recommended Output Current	V _{IN} = 10.0 V			150	mA
I _Q	Quiescent Current	$V_{IN} = 6.0V, I_{O} = 60 \text{ mA}$		2.0	4.5	mA
Line Reg	Line Regulation	V_{IN} =10 V \rightarrow 16V		2.0	30	mV
Load Reg	Load Regulation	$I_O = 0 \text{ mA} \rightarrow 30 \text{ mA}, V_{IN} = 10 \text{ V}$		15	60	mV
		$I_O = 0 \text{ mA} \rightarrow 100 \text{ mA}, \ V_{IN} = 10 \text{ V}$		40	140	mV
		$I_O = 0 \text{ mA} \rightarrow 150 \text{ mA}, V_{IN} = 10 \text{ V}$		120	220	mV
RR	Ripple Rejection	100 mV(rms), f = 400 Hz,		55		dB
		$V_{IN} = 10.5 \text{ V}, I_{O} = 10 \text{ mA}$				
$\Delta V_{O}/\Delta T_{A}$	Temperature Coefficient	$V_{IN} = 10.5 \text{ V}, I_{O} = 10 \text{ mA},$ -30 °C ≤ T_{A} ≤ 80 °C		± 0.7		mV/°C

Note 1: I_O (Load current) is current when V_O drops down 0.4 V from V_O at I_O = 10 mA



TYPICAL PERFORMANCE CHARACTERISTICS

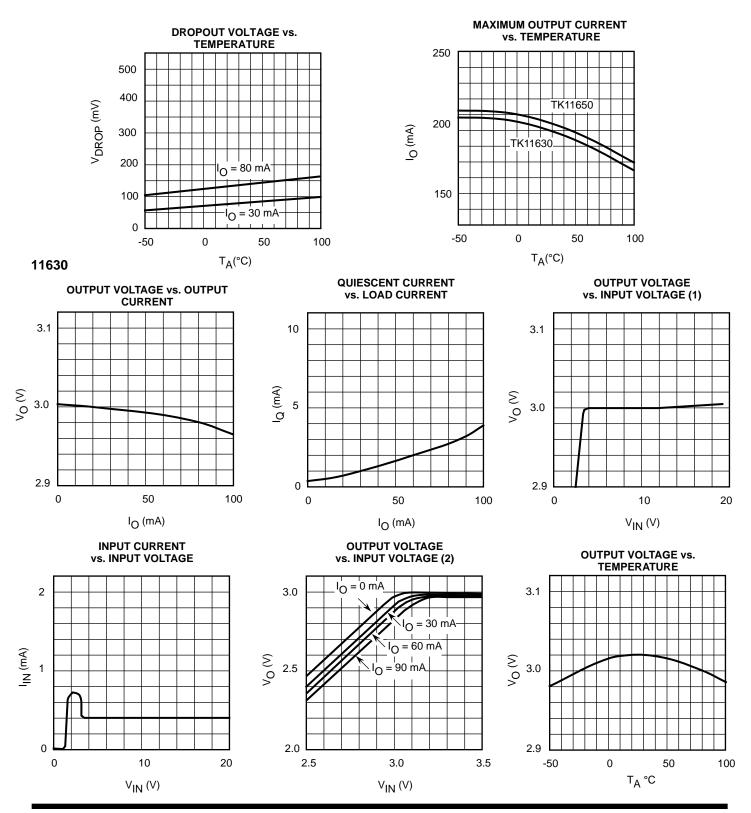
 $T_A = 25$ °C, unless otherwise specified



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TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)

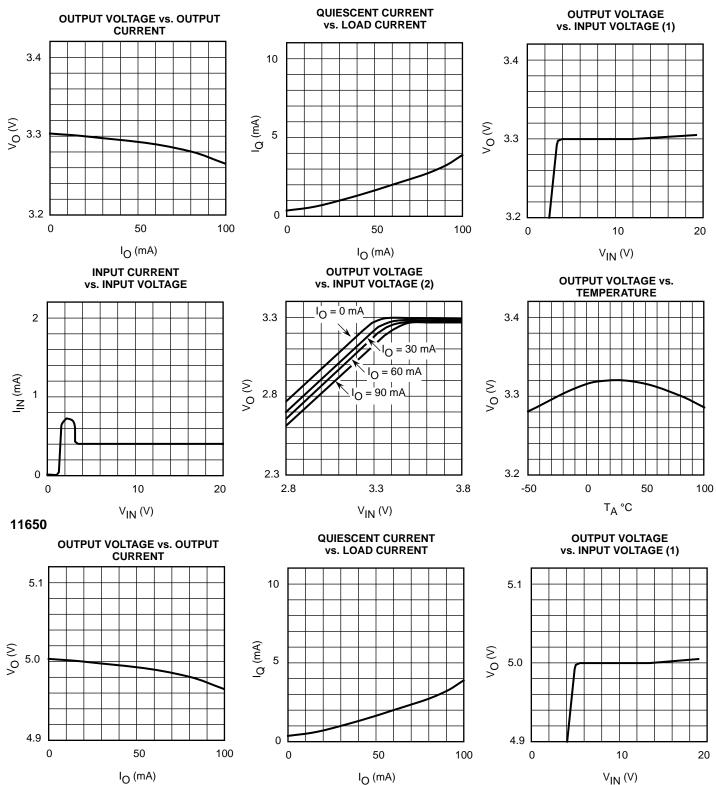
 $T_A = 25$ °C, unless otherwise specified



TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)

 $T_A = 25$ °C unless otherwise specified

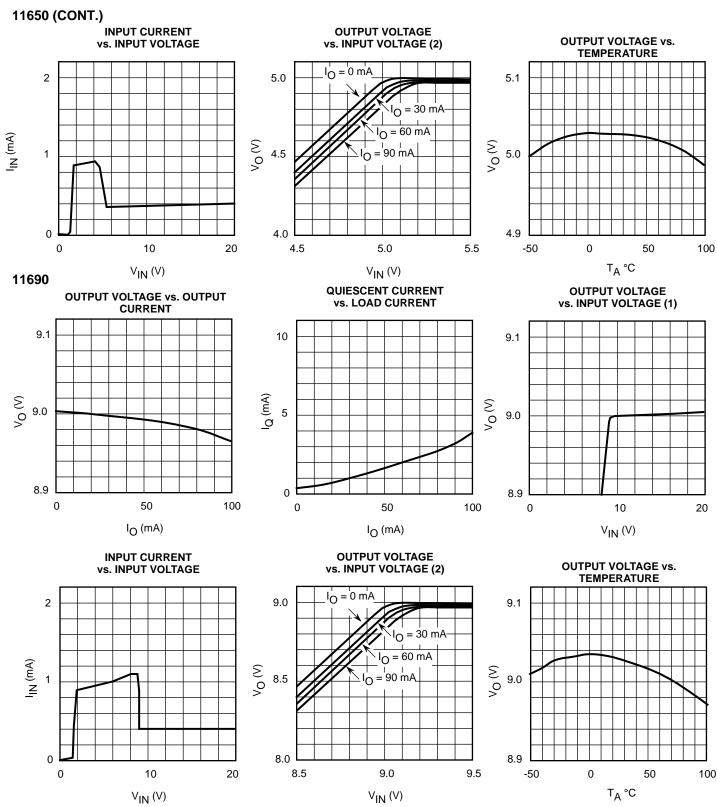




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TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)

 $T_A = 25$ °C, unless otherwise specified



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DEFINITION AND EXPLANATION OF TECHNICAL TERMS

LINE REGULATION (LINE REG)

Line regulation is the relationship between change in output voltage due to a change in input voltage.

LOAD REGULATION (LOAD REG)

Load regulation is the relationship between change in output voltage due to a change in load current.

DROP OUT VOLTAGE (VDROP)

This is a measure of how well the regulator performs as the input voltage decreases. The smaller the number, the further the input voltage can decrease before regulation problems occur. Nominal output voltage is first measured when $V_{IN} = V_O + 1$ at a chosen load current. When the output voltage has dropped 100 mV from the nominal, V_{IN} - V_O is the dropout voltage. This voltage is affected by load current and junction temperature.

OUTPUT NOISE VOLTAGE

This is the effective AC voltage that occurs on the output voltage under the condition where the input noise is low and with a given load, filter capacitor, and frequency range.

THERMAL PROTECTION

This is an internal feature which turns the regulator off when the junction temperature rises above 150 °C. After the regulator turns off, the temperature drops and the regulator output turns back on. Under certain conditions, the output waveform may appear to be an oscillation as the output turns off and on and back again in succession.

PACKAGE POWER DISSIPATION (PD)

This is the power dissipation level at which the thermal sensor is activated. The IC contains an internal thermal sensor which monitors the junction temperature. When the junction temperature exceeds the monitor threshold of 150 °C, the IC is shutdown. The junction temperature rises as the difference between the input power (V_{IN} X I_{IN}) and the output power $(V_O \times I_O)$ increases. The rate of temperature rise is greatly affected by the mounting pad configuration on the PCB, the board material, and the ambient temperature. When the IC mounting has good thermal conductivity, the junction temperature will be low even if the power dissipation is great. ambient temperature. When the radiation of heat is good, the device temperature will be low, even if the power loss is great. When mounted on the recommended mounting pad, the power dissipation of the SOT-89-3 package is 600 mW. Derate the power dissipation at 4.8 mW/°C for operation above 25 °C. To determine the power dissipation for shutdown when mounted, attach the device on the actual PCB and deliberately increase the output current (or raise the inout voltage) until the thermal protection circuit is activated. Calculate the power dissipation of the device by subtracting the output power from the inut power. The measurements should allow for the ambient temperature of the PCB. The value obtained from PD/ (150 °C - T_A) is the deating factor. The PCB mounting pad should provide maximum thermal conductivity in order to maintain low device temperatures. As a general rule, the lower the temperature, the better the reliability of the device. The thermal resistance when mounted is expressed as follows:

$$T_J = \Theta_{JA} X P_D + T_A$$

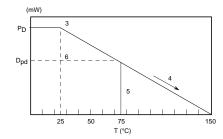
For Toko IC's, the internal limit for junction temperature is 150 mounted is 150 $^{\circ}$ C. If the ambient temperature, T_{A} is 25 °C, then:

$$150 \, ^{\circ}\text{C} = \Theta_{\text{JA}} \, \text{x} \, \text{P}_{\text{D}} + 25 \, ^{\circ}\text{C}$$

 $\Theta_{\text{JA}} = 125 \, ^{\circ}\text{C/P}_{\text{D}}$

 P_{D} is the value when the thermal sensor is activated. A simple way to determine P_D is to calculate VIN X IIN when the output side is shorted. Input current gradually falls as temperature rises. You should use the value when the thermal equilibrium is reached.

The range of currents usable can also be found from the graph below:



Procedure:

- 1) Find P_D
- 2) P_{D1} is taken to be PD X (0.8-0.9)
 3) Plot P_{D1} against 25 °C.

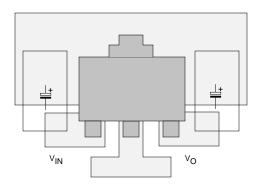
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DEFINITION AND EXPLANATION OF TECHNICAL TERMS (CONT.)

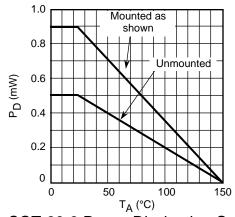
- 4) Connect P_{D1} to the point corresponding to the 150 °C. with a straight line.
- 5) In design, take a vertical line from the maximum operating temperature (e.g. 75 °C.) to the derating curve.
- 6) Read off the value of P_D against the point at ehich the vertical line intersects the derating curve. This is taken as the maximum power dissipation, D_{PD}.

The maximum operating current is:

$$I_{OUT} = (D_{PD}/(V_{IN(MAX}) - V_O)$$



SOT-89-3 Board Layout



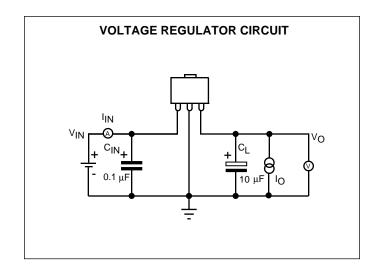
SOT-89-3 Power Dissipation Curve

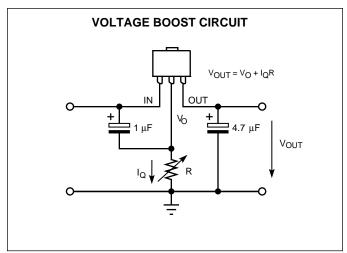
Copper pattern should be as large as possible. Power dissipation is 600 mW for Sot-89-3. A low ESR capacitor is recommended. FOr low temperature operatin, select a capacitor with a low ESR at the lowest operating temperature to prevent oscillation, degradation of ripple rejection and increase in noise. The minimum recommended capacitance is 2.2 μ F.

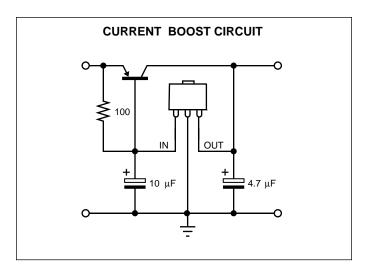
INPUT/OUTPUT DECOUPLING CAPACITOR CONSIDER-ATIONS

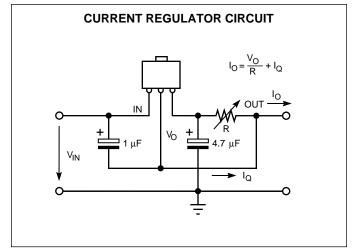
Voltage regulators require input and output decoupling capacitors. The required value of these capacitors vary with application. Capacitors made by different manufacturers can have different characteristics, particularly with regard to high frequencies and equivalent series resistance (ESR) over temperature. The type of capacitor is also important. For example, a 4.7 µF aluminum electrolytic may be required for a certain application. If a tantalum capacitor is used, a lower value of 2.2 µF would be adequate. It is important to consider the temperature characteristics of the decoupling capacitors. While Toko regulators are designed to operate as low as -30 °C, many capacitors will not operate properly at this temperature. The capacitance of aluminum electrolytic capacitors may decrease to 0 at low temperatures. This may cause oscillation on the output of the regulator since some capacitance is required to guarantee stability. Thus, it is important to consider the characteristics of the capacitor over temperature when selection decoupling capacitors. The ESR is another important parameter. The ESR will increase with temperature but low ESR capacitors are often larger and more costly. In general, Tantalum capacitors offer lower ESR than aluminum electrolytic, but new low ESR aluminum electrolytic capacitors are now available from several manufacturers. Usually a bench test is sufficient to determine the minimum capacitance required for a particular application. After taking thermal characteristics and tolerance into account, the minimum capacitance value should be approximately two times this value. Please note that linear regulators with a low dropout voltage have high internal loop gains which requires care in guarding against oscillation caused by insufficient decoupling capacitance. The use of high quality decoupling capacitors suited for your application will guarantee proper operation of the circuit.

TYPICAL APPLICATIONS









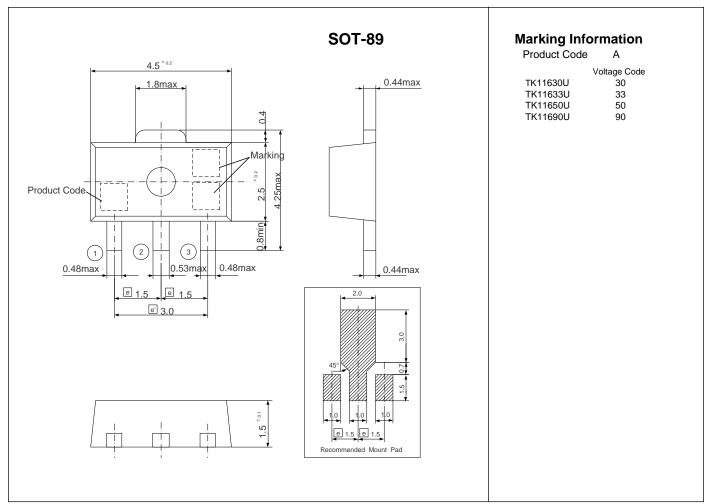
APPLICATION HINTS

Maximize copper foil area connecting to all IC pins for optimum heat conduction. Place input and output bypass capacitors close to the GND pin.

For best transient behavior and lowest output impedance, use as large a capacitor value as possible. The temperature coefficient of the capacitance and Equivalent Series Resistance (ESR) should be taken into account. These parameters can influence power supply noise and ripple rejection. In extreme cases, oscillation may occur. In order to maintain stability, the output bypass capacitor value should be minimum 1 μF for Tantalum electrolytic or 4.7 μF for Aluminum electrolytic at T_A = 25 °C.

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PACKAGE OUTLINE



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