

TOSHIBA Diode Silicon Epitaxial Planar Type

JDV2S36E

TCXO/VCO

- High capacitance ratio: $C_{1V} / C_{6V} = 7.5$ (typ.)
- Low series resistance: $r_s = 0.4 \Omega$ (typ.)
- Useful for small size tuner.

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Reverse voltage	V_R	10	V
Junction temperature	T_j	125	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to 125	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit: mm

ESC	
JEDEC	—
JEITA	—
TOSHIBA	1-1G1A

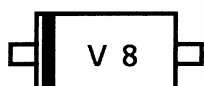
Weight: 1.4 mg (typ.)

Electrical Characteristics ($T_a = 25^\circ\text{C}$)

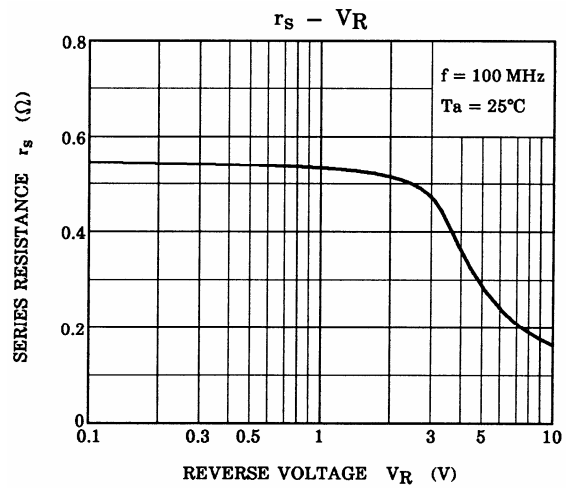
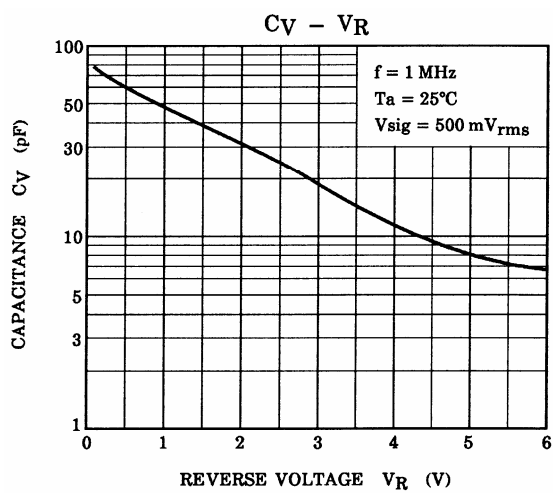
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reverse voltage	V_R	$I_R = 1 \mu\text{A}$	10	—	—	V
Reverse current	I_R	$V_R = 10 \text{ V}$	—	—	3	nA
Capacitance	C_{1V}	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$	44	—	49.5	pF
Capacitance	C_{6V}	$V_R = 6 \text{ V}, f = 1 \text{ MHz}$	5.4	—	7.3	pF
Capacitance ratio	C_{1V} / C_{6V}	—	6.3	7.5	—	—
Series resistance	r_s	$V_R = 4 \text{ V}, f = 100 \text{ MHz}$	—	0.4	0.8	Ω

Note: Signal level when capacitance is measured: $V_{sig} = 500 \text{ mV}_{rms}$

Marking



Start of commercial production
2006-08



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