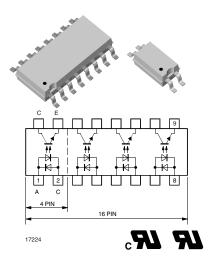


Vishay Semiconductors

Optocoupler, Phototransistor Output, AC Input, Single/Quad Channel, Half Pitch Mini-Flat



FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}
- · Low coupling capacitance of typical 0.3 pF
- · Low temperature coefficient of CTR
- Wide ambient temperature range
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



· Programmable logic controllers

AGENCY APPROVALS

- UL1577, file no. E76222 system code M, double protection
- C-UL CSA 22.2 bulletin 5A

DESCRIPTION

The low profile miniflat package includes an optocoupler with AC Input and transistor output. It is available in single channel (4 pin) TCMT1600 or quad channel (16 pin) TCMT4600.

ORDER INFORMATION					
PART	REMARKS				
TCMT1600	CTR 80 %to 300 %, single channel, SOP-4				
TCMT1600T3 (1)	CTR 80 % to 300 %, single channel, SOP-4				
TCMT4600	CTR 80 % to 300 %, quad channel, SOP-16				
TCMT4600T0 (1)	CTR 80 % to 300 %, quad channel, SOP-16				
TCMT4606	CTR 100 % to 300 %, quad channel, SOP-16				

Notes

Available only on tape and reel.

(1) Product is rotated 180° in tape and reel cavity.

ABSOLUTE MAXIMUM RATINGS (1)							
PARAMETER	TEST CONDITION	TEST CONDITION SYMBOL		UNIT			
INPUT							
Reverse voltage		V_{R}	6	V			
Forward current		I _F	± 60	mA			
Forward surge current	t _P ≤ 10 μs	I _{FSM}	± 1.5	Α			
Power dissipation		P _{diss}	100	mW			
Junction temperature		Tj	125	°C			
OUTPUT							
Collector emitter voltage		V_{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		Ic	50	mA			
Collector peak current	$t_P/T = 0.5, t_P \le 10 \text{ ms}$	I _{CM}	100	mA			
Power dissipation		P _{diss}	150	mW			
Junction temperature		T _i	125	°C			

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ABSOLUTE MAXIMUM RATINGS (1)								
PARAMETER	TEST CONDITION	SYMBOL VALUE UNI						
COUPLER								
AC isolation test voltage (RMS)		V _{ISO}	3750	V_{RMS}				
Total power dissipation		P _{tot}	250	mW				
Operating ambient temperature range		T _{amb}	- 40 to + 100	°C				
Storage temperature range		T _{stg}	- 40 to + 100	°C				
Soldering temperature (2)		T _{sld}	260	°C				

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(2) Refer to reflow profile for soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTICS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	Cj		50		pF		
OUTPUT								
Collector emitter voltage	I _C = 100 μA	V_{CEO}	70			V		
Emitter collector voltage	$I_E = 100 \mu A$	V _{ECO}	7			V		
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}			0.3	V		
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c		100		kHz		
Capacitance (input to output)	f = 1 MHz	C _{IO}		0.3		pF		

Note

 T_{amb} = 25 °C, unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I_{C}/I_{F} $V_{CE} = 5 \text{ V}, I_{F} = 5 \text{ mA}$	TCMT1600	CTR	80		300	%	
	TCMT4600	CTR	80		300	%	
	TCMT4606	CTR	100		300	%	

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _d		3		μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _r		3		μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _f		4.7		μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _s		0.3		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{on}		6		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{off}		5		μs	

 $^{^{(1)}}$ T_{amb} = 25 °C, unless otherwise specified.



Optocoupler, Phototransistor Output, AC Input, Single/Quad Channel, Half Pitch Mini-Flat

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SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t _{on}		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t _{off}		18		μs

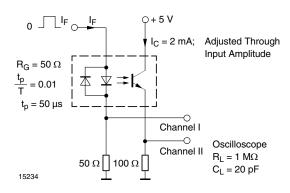


Fig. 1 - Test Circuit, Non-Saturated Operation

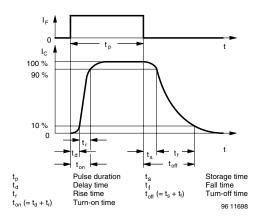


Fig. 3 - Switching Times

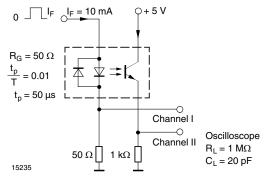


Fig. 2 - Test Circuit, Saturated Operation

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

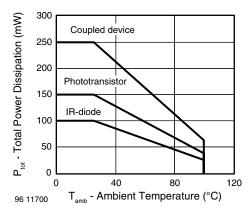


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

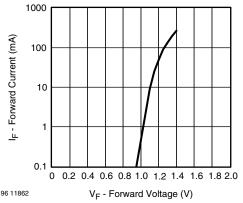


Fig. 5 - Forward Current vs. Forward Voltage

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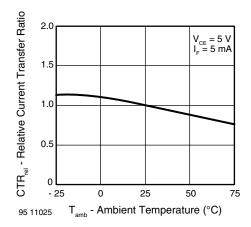


Fig. 6 - Relative Current Transfer Ratio vs.
Ambient Temperature

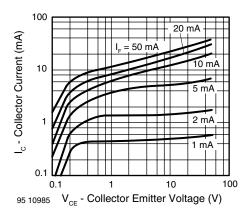


Fig. 9 - Collector Current vs. Collector Emitter Voltage

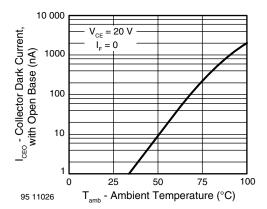


Fig. 7 - Collector Dark Current vs. Ambient Temperature

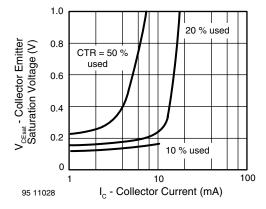


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

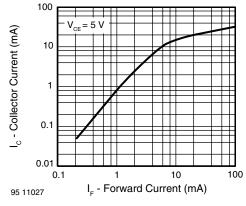


Fig. 8 - Collector Current vs. Forward Current

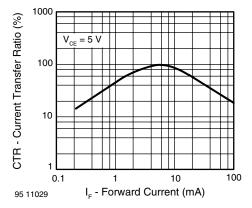


Fig. 11 - Current Transfer Ratio vs. Forward Current





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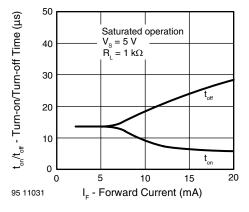


Fig. 12 - Turn-on/Turn-off Time vs. Forward Current

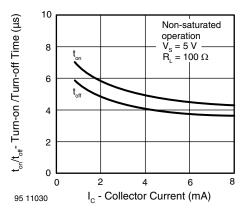
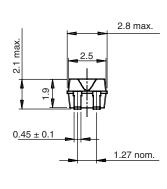
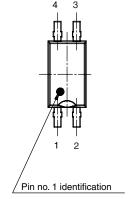
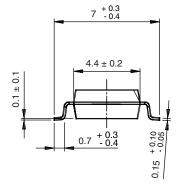


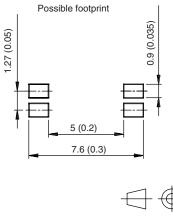
Fig. 13 - Turn-on/Turn-off Time vs. Collector Current

PACKAGE DIMENSIONS in millimeters







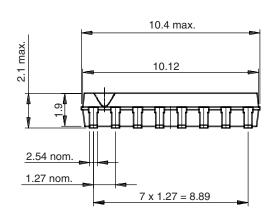


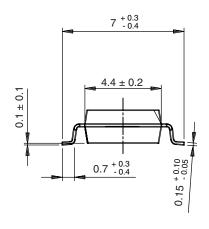
technical drawings according to DIN specifications

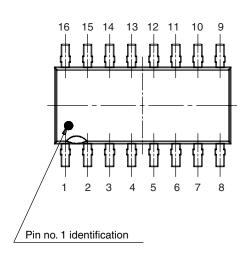
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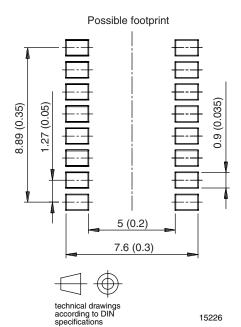
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Drawing-No.: 6.544-5330.03-4

Issue: 1; 04.04.00



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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

Document Number: 83512 Rev. 1.9, 17-Dec-08



Vishay

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