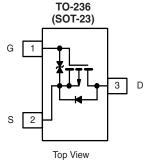




P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}(\Omega)$	V _{GS(th)} (V)	I _D (mA)				
- 60	6 at V _{GS} = - 10 V	- 1 to - 3	- 185				



Marking Code: 6Kwll

6K = Part Number Code for TP0610K w = Week Code

II = Lot Traceability

Ordering Information: TP0610K-T1-E3 (Lead (Pb)-free)

TP0610K-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- High-Side Switching
- Low On-Resistance: 6 Ω
- Low Threshold: 2 V (typ.)
- Fast Swtiching Speed: 20 ns (typ.)
- Low Input Capacitance: 20 pF (typ.)
- 2000 V ESD Protection
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- · Battery Operated Systems
- Power Supply Converter Circuits
- · Solid-State Relays

BENEFITS

- · Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- · Easily Driven without Buffer

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted							
Parameter		Symbol	Limit	Unit			
Drain-Source Voltage		V _{DS}	- 60				
Gate-Source Voltage		V _{GS}	± 20	V			
0 11	T _A = 25 °C	- I _D	- 185	mA			
Continuous Drain Current ^a	T _A = 100 °C		- 115				
Pulsed Drain Current ^b		I _{DM}	- 800				
Developed to the stand	T _A = 25 °C	P _D	350	mW			
Power Dissipation ^a	T _A = 100 °C	- FD	140				
Maximum Junction-to-Ambient ^a		R _{thJA}	350	°C/W			
Operating Junction and Storage Temperature Range		T _{J,} T _{stg}	- 55 to 150	°C			

Notes

- a. Surface mounted on FR4 board.
- b. Pulse width limited by maximum junction temperature.

FREE

TP0610K

Vishay Siliconix



SPECIFICATIONS T _A = 25	°C, unless oth	erwise noted				
	Symbol	Test Conditions	Limits			
Parameter			Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -10 \mu\text{A}$	- 60			v
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	
Gate-Body Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 10	μΑ
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 200	nA
	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			± 500	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	
Zero Gate Voltage Drain Current		V _{DS} = - 60 V, V _{GS} = 0 V			- 25	
	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 85 °C			- 250	
0.01.0.19	I _{D(on)}	V _{GS} = - 10 V, V _{DS} = - 4.5 V	- 50			mA
On-State Drain Current ^a		V _{GS} = - 10 V, V _{DS} = - 10 V	- 600			
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 25 mA			10	Ω
		V _{GS} = - 10 V, I _D = - 500 mA			6	
		V _{GS} = - 10 V, I _D = - 500 mA, T _J =125 °C			9	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 100 mA	80			mS
Diode Forward Voltage	V _{SD}	I _S = - 200 mA, V _{GS} = 0 V			- 1.4	V
Dynamic	<u>.</u>					
Total Gate Charge	Qg	$V_{DS} = -30 \text{ V}, V_{GS} = -15 \text{ V}$ $I_{D} \cong -500 \text{ mA}$		1.7		nC
Gate-Source Charge	Q _{gs}			0.26		
Gate-Drain Charge	Q _{gd}			0.46		
Input Capacitance	C _{iss}	V _{DS} = - 25 V, V _{GS} = 0 V f = 1 MHz		23		pF
Output Capacitance	C _{oss}			10		
Reverse Transfer Capacitance	C _{rss}	1 - 1 1411.12		5		
Switching ^b						
Turn-On Time	t _{d(on)}	$V_{DD} = -25 \text{ V}, R_{L} = 150 \Omega$		20		ns
Turn-Off Time	t _{d(off)}	$I_D \cong$ - 200 mA, $V_{GEN} =$ - 10 V, $R_g =$ 10 Ω		35		

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.

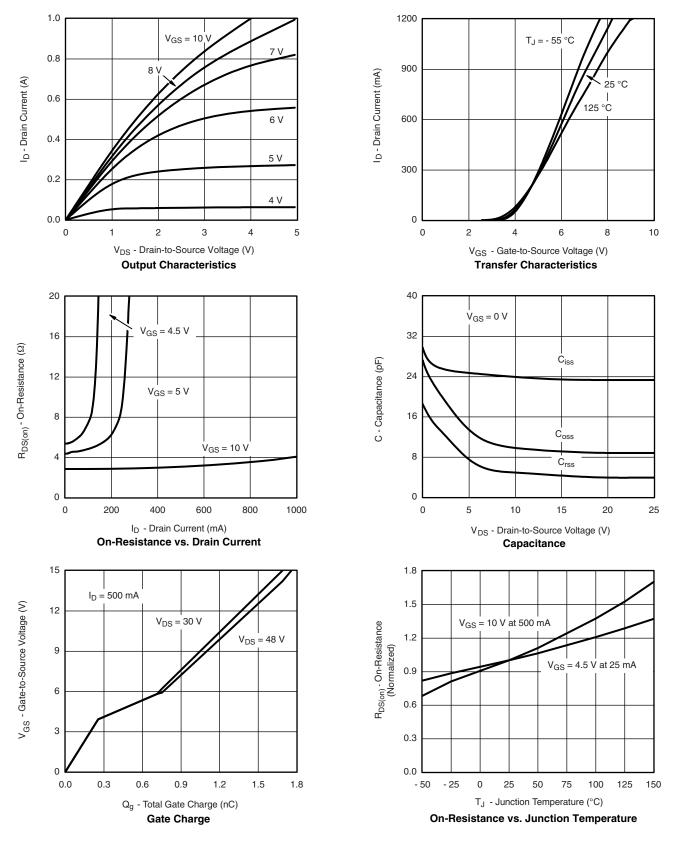
b. Switching time is essentially independent of operating temperature.







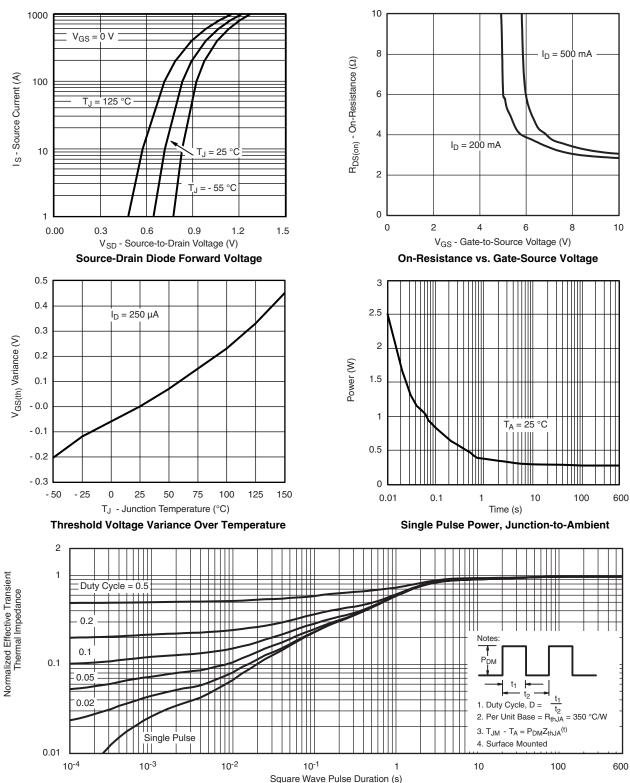
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71411.

Normalized Thermal Transient Impedance, Junction-to-Ambient





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