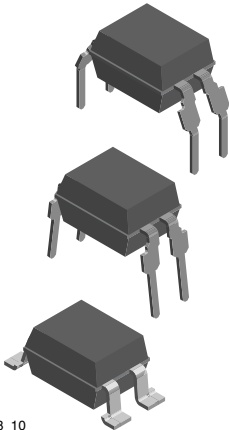
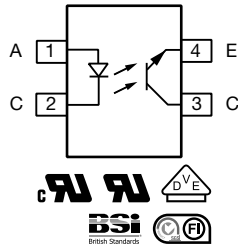


Optocoupler, Phototransistor Output, High Temperature, 110 °C Rated



17918_10



DESCRIPTION

The VO610A consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic dual in line package.

AGENCY APPROVALS

- BSI: EN 60065:2002, EN 60950:2000
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1
- FIMKO
- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A

FEATURES

- CTR offered in 4 groups
- Isolation materials according to UL 94 V-0
- Pollution degree 2 (DIN/VDE 0110/resp. IEC 60664)
- Climatic classification 55/110/21 (IEC 60068 part 1)
- Temperature range - 55 °C to + 110 °C
- Rated impulse voltage (transient overvoltage) $V_{IOTM} = 6 kV_{peak}$
- Isolation test voltage (partial discharge test voltage) $V_{pd} = 1.6 kV$
- Rated isolation voltage (RMS includes DC) $V_{IOWM} = 600 V_{RMS}$
- Rated recurring peak voltage (repetitive) $V_{IORM} = 850 V_{peak}$
- Creepage current resistance according to VDE 0303/IEC 60112 comparative tracking index: $CTI \geq 175$
- Thickness through insulation $\geq 0.4 mm$
- Compliant to RoHS Directive 2002/95/EC



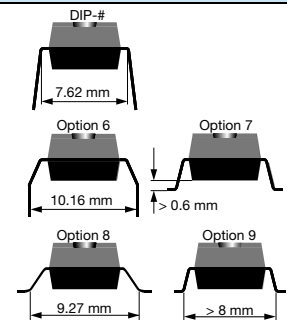
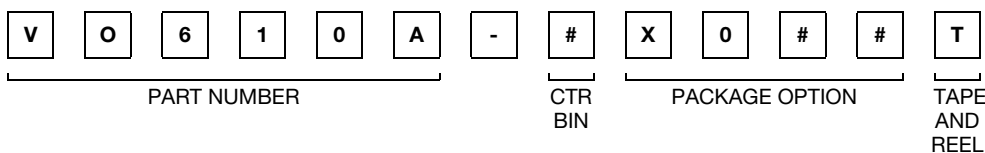
RoHS COMPLIANT

APPLICATIONS

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- for appl. class I - IV at mains voltage $\leq 300 V$
- for appl. class I - IV at mains voltage $\leq 600 V$ according to table 1 of IEC 60664-1, suitable for:
 - Switch-mode power supplies
 - Line receiver
 - Computer peripheral interface
 - Microprocessor system interface

ORDERING INFORMATION



AGENCY CERTIFIED/PACKAGE	CTR (%)			
	40 to 80	63 to 125	100 to 200	160 to 320
BSI, FIMKO, UL, cUL				
DIP-4	VO610A-1	VO610A-2	VO610A-3	-
SMD-4, option 7	-	-	VO610A-3X007T	-
SMD-4, option 8	-	-	VO610A-3X008T	VO610A-4X008T
SMD-4, option 9	-	-	VO610A-3X009T	-
VDE, BSI, FIMKO, UL, cUL				
DIP-4	-	-	VO610A-3X001	-
DIP-4, 400 mil, option 6	-	-	VO610A-3X016	-
SMD-4, option 7	-	-	-	VO610A-4X017T
SMD-4, option 8	-	-	VO610A-3X018T	-
SMD-4, option 9	VO610A-1X019T	-	VO610A-3X019T	VO610A-4X019T

Note

- Additional options may be possible, please contact sales office



ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V_R	6	V
Forward current		I_F	60	mA
Forward surge current	$t_p \leq 10\text{ }\mu\text{s}$	I_{FSM}	1.5	A
LED power dissipation	at $25\text{ }^{\circ}\text{C}$	P_{diss}	100	mW
OUTPUT				
Collector emitter voltage		V_{CEO}	70	V
Emitter collector voltage		V_{ECO}	7	V
Collector current		I_C	50	mA
Collector peak current	$t_p/T = 0.5, t_p \leq 10\text{ ms}$	I_{CM}	100	mA
Output power dissipation	at $25\text{ }^{\circ}\text{C}$	P_{diss}	150	mW
COUPLER				
Isolation test voltage (RMS)	$t = 1\text{ min}$	V_{ISO}	5000	V_{RMS}
Operating ambient temperature range		T_{amb}	- 55 to + 110	$^{\circ}\text{C}$
Storage temperature range		T_{stg}	- 55 to + 125	$^{\circ}\text{C}$
Soldering temperature ⁽¹⁾	2 mm from case, $\leq 10\text{ s}$	T_{sld}	260	$^{\circ}\text{C}$

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- ⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted parts (SMD). Refer to wave profile for soldering conditions for through hole parts (DIP).

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = 50\text{ mA}$	V_F		1.25	1.6	V
Junction capacitance	$V_R = 0, f = 1\text{ MHz}$	C_j		50		pF
OUTPUT						
Collector emitter voltage	$I_C = 1\text{ mA}$	V_{CEO}	70			V
Emitter collector voltage	$I_E = 100\text{ }\mu\text{A}$	V_{ECO}	7			V
Collector emitter cut-off current	$V_{CE} = 20\text{ V}, I_F = 0\text{ A}$	I_{CEO}		10	100	nA
COUPLER						
Collector emitter saturation voltage	$I_F = 10\text{ mA}, I_C = 1\text{ mA}$	V_{CEsat}			0.3	V
Cut-off frequency	$V_{CE} = 5\text{ V}, I_F = 10\text{ mA}, R_L = 100\text{ }\Omega$	f_c		110		kHz
Coupling capacitance	$f = 1\text{ MHz}$	C_k		0.6		pF

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I_C/I_F	$V_{CE} = 5\text{ V}, I_F = 1\text{ mA}$	VO610A-1	CTR	13	30		%
		VO610A-2	CTR	22	45		%
		VO610A-3	CTR	34	70		%
		VO610A-4	CTR	56	90		%
	$V_{CE} = 5\text{ V}, I_F = 10\text{ mA}$	VO610A-1	CTR	40		80	%
		VO610A-2	CTR	63		125	%
		VO610A-3	CTR	100		200	%
		VO610A-4	CTR	160		320	%

SAFETY AND INSULATION RATED PARAMETERS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Partial discharge test voltage - routine test	100 %, $t_{test} = 1\text{ s}$	V_{pd}	1.6			kV
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60\text{ s}, t_{test} = 10\text{ s}$, (see figure 2)	V_{pd}	1.3			kV
Insulation resistance	$V_{IO} = 500\text{ V}$	R_{IO}	10^{12}			Ω
	$V_{IO} = 500\text{ V}, T_{amb} = 100\text{ }^{\circ}\text{C}$	R_{IO}	10^{11}			Ω
	$V_{IO} = 500\text{ V}, T_{amb} = 150\text{ }^{\circ}\text{C}$ (construction test only)	R_{IO}	10^9			Ω
Rated impulse voltage		V_{IOTM}			6	kV
Max. working voltages	Recurring peak voltage	V_{IORM}	850			V_{peak}
Forward current		I_{SI}			130	mA
Power dissipation		P_{SO}			265	mW
Safety temperature		T_{si}			150	$^{\circ}\text{C}$
Creepage distance			7.6			mm

Note

- According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

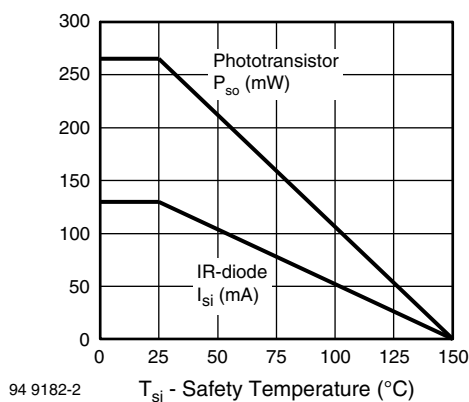


Fig. 1 - Derating Diagram

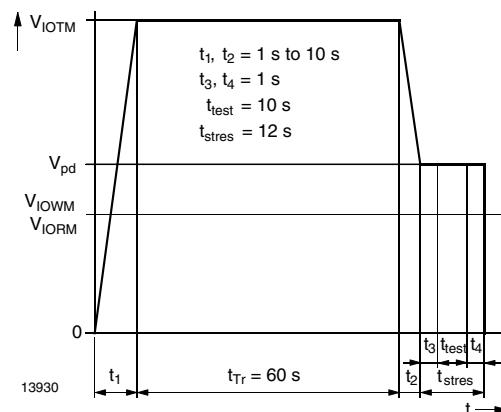
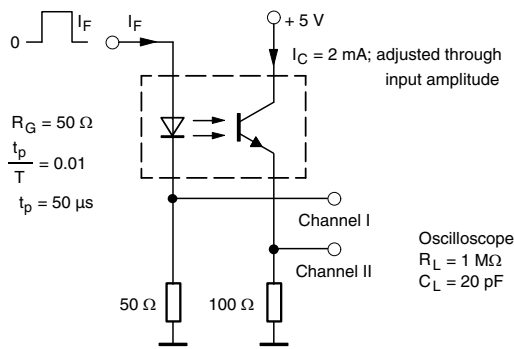


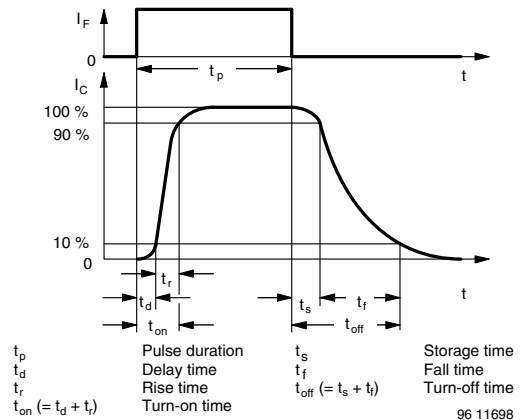
Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2 (VDE0884)/DIN EN 60747-; IEC60747

SWITCHING CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_d		3		μs
Rise time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_r		3		μs
Fall time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_f		4.7		μs
Storage time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_s		0.3		μs
Turn-on time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_{on}		6		μs
Turn-off time	$V_S = 5\text{ V}$, $I_C = 2\text{ mA}$, $R_L = 100\text{ }\Omega$, (see figure 3)	t_{off}		5		μs
Turn-on time	$V_S = 5\text{ V}$, $I_F = 10\text{ mA}$, $R_L = 1\text{ k}\Omega$, (see figure 4)	t_{on}		9		μs
Turn-off time	$V_S = 5\text{ V}$, $I_F = 10\text{ mA}$, $R_L = 1\text{ k}\Omega$, (see figure 4)	t_{off}		10		μs



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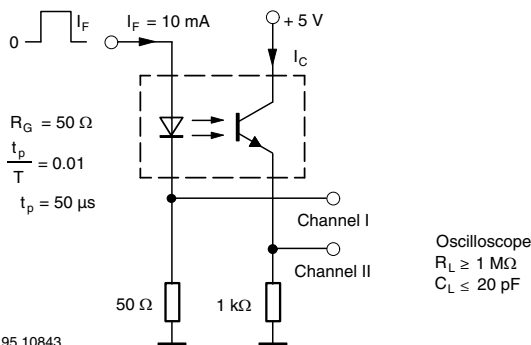
Fig. 3 - Test Circuit, Non-Saturated Operation



t_p Pulse duration
 t_d Delay time
 t_r Rise time
 $t_{on} (= t_d + t_r)$ Turn-on time
 t_s Storage time
 t_f Fall time
 $t_{off} (= t_s + t_f)$ Turn-off time

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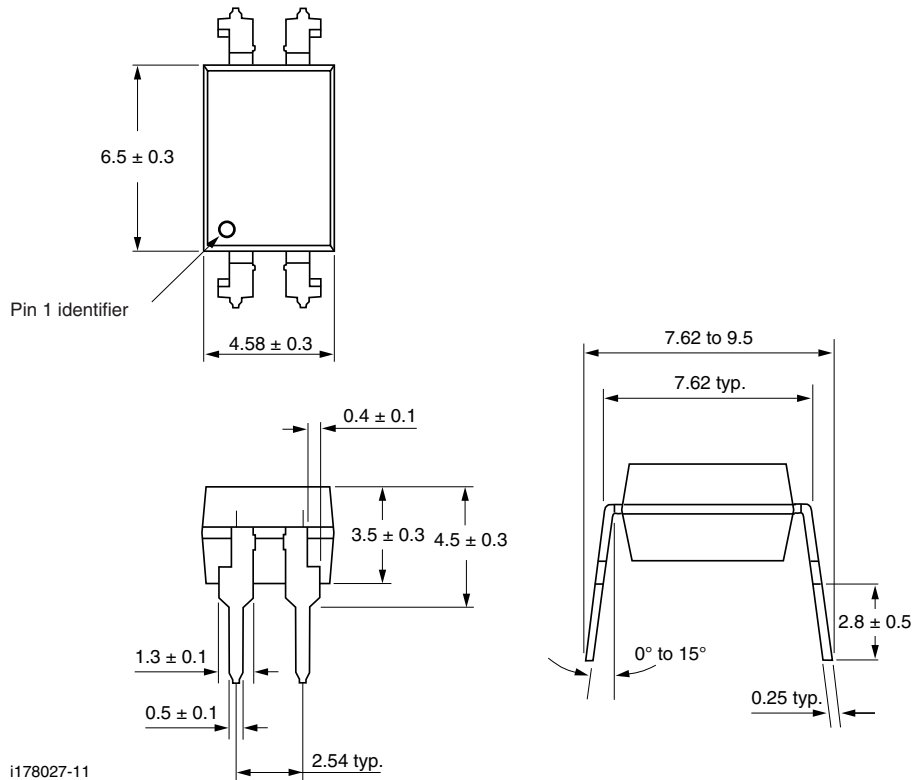
Fig. 5 - Switching Times



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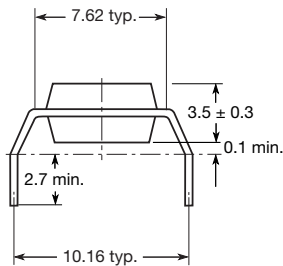
Fig. 4 - Test Circuit, Saturated Operation

PACKAGE DIMENSIONS in millimeters

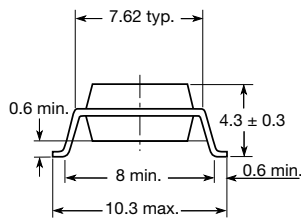


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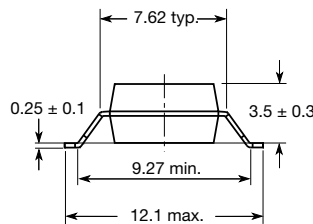
Option 6



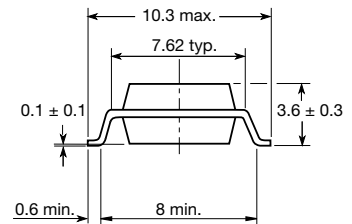
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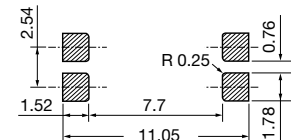
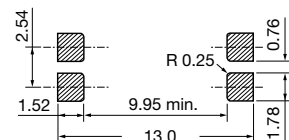
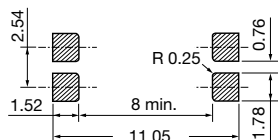
Option 8



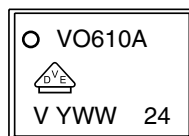
Option 9



20802-38



PACKAGE MARKING



Note

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE logo is only printed on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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