

1. General description

AC Thyristor power switch in a SOT223 surface-mountable plastic package with self-protective capabilities against low and high energy transients.

2. Features and benefits

- Common terminal on mounting base allows multiple ACTs on shared cooling pad
- Exclusive negative gate triggering
- Full cycle AC conduction
- High voltage capability
- Remote gate separates the gate driver from the effects of the load current
- Safe clamping of low energy over-voltage transients
- Self-protective turn-on during high energy voltage transients
- Surface-mountable package
- Very high noise immunity

3. Applications

- Fan motor circuits
- Pump motor circuits
- Lower-power highly inductive, resistive and safety loads
- Contactors, circuit breakers, valves, dispensers and door locks

4. Quick reference data

Table 1. Quick reference data

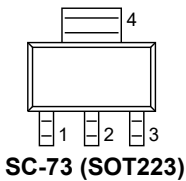
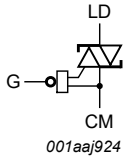
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{sp}} \leq 112\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	0.8	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{\text{j(init)}} = 25\text{ °C}$; $t_{\text{p}} = 20\text{ ms}$; Fig. 4 ; Fig. 5	-	-	13	A
		full sine wave; $T_{\text{j(init)}} = 25\text{ °C}$; $t_{\text{p}} = 16.7\text{ ms}$	-	-	14.3	A
T_{j}	junction temperature		-	-	125	°C
V_{PP}	peak pulse voltage	$T_{\text{j}} = 25\text{ °C}$; non-repetitive, off-state; ten pulses on each voltage polarity; 20s or more between successive pulses; Fig. 6	-	-	2.5	kV

Static characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 100\text{ mA}$; LD+ G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10	1	-	10	mA
		$V_D = 12\text{ V}$; $I_T = 100\text{ mA}$; LD- G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10	1	-	10	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 12	-	-	20	mA
V_T	on-state voltage	$I_T = 1.1\text{ A}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 13	-	-	1.3	V
V_{CL}	clamping voltage	$I_{CL} = 0.1\text{ mA}$; $t_p = 1\text{ ms}$; $T_j = 25\text{ }^\circ\text{C}$	850	-	-	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit; Fig. 15	500	-	-	V/ μs
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; $I_{T(RMS)} = 0.8\text{ A}$; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$; (snubberless condition); gate open circuit; Fig. 16; Fig. 17	0.5	-	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	LD	load	 SC-73 (SOT223)	 001aa 924
2	CM	common		
3	G	gate		
4	CM	common		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
ACT108W-800E	SOT223	ACT108W-800EF	Reel	4000	SOT223	16-Mar-2006

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{sp}} \leq 112\text{ }^{\circ}\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	0.8	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{\text{j(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 20\text{ ms}$; Fig. 4 ; Fig. 5	-	13	A
		full sine wave; $T_{\text{j(init)}} = 25\text{ }^{\circ}\text{C}$; $t_{\text{p}} = 16.7\text{ ms}$	-	14.3	A
I^2t	I^2t for fusing	$t_{\text{p}} = 10\text{ ms}$; sine-wave pulse	-	0.84	A^2s
di_{T}/dt	rate of rise of on-state current	$I_{\text{G}} = 20\text{ mA}$	-	100	$\text{A}/\mu\text{s}$
I_{GM}	peak gate current	$t = 20\text{ }\mu\text{s}$	-	1	A
V_{GM}	peak gate voltage	positive applied gate voltage	-	15	V
$P_{\text{G(AV)}}$	average gate power	over any 20 ms period	-	0.1	W
T_{stg}	storage temperature		-40	150	$^{\circ}\text{C}$
T_{j}	junction temperature		-	125	$^{\circ}\text{C}$
V_{PP}	peak pulse voltage	$T_{\text{j}} = 25\text{ }^{\circ}\text{C}$; non-repetitive, off-state; ten pulses on each voltage polarity; 20s or more between successive pulses; Fig. 6	-	2.5	kV

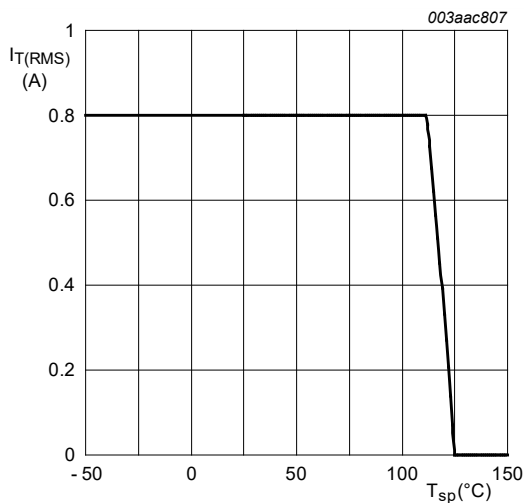
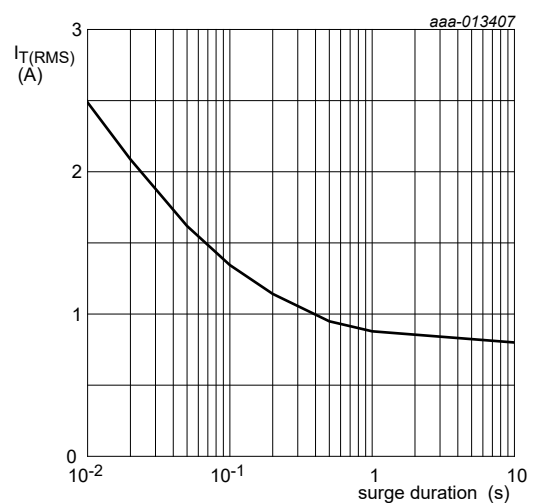
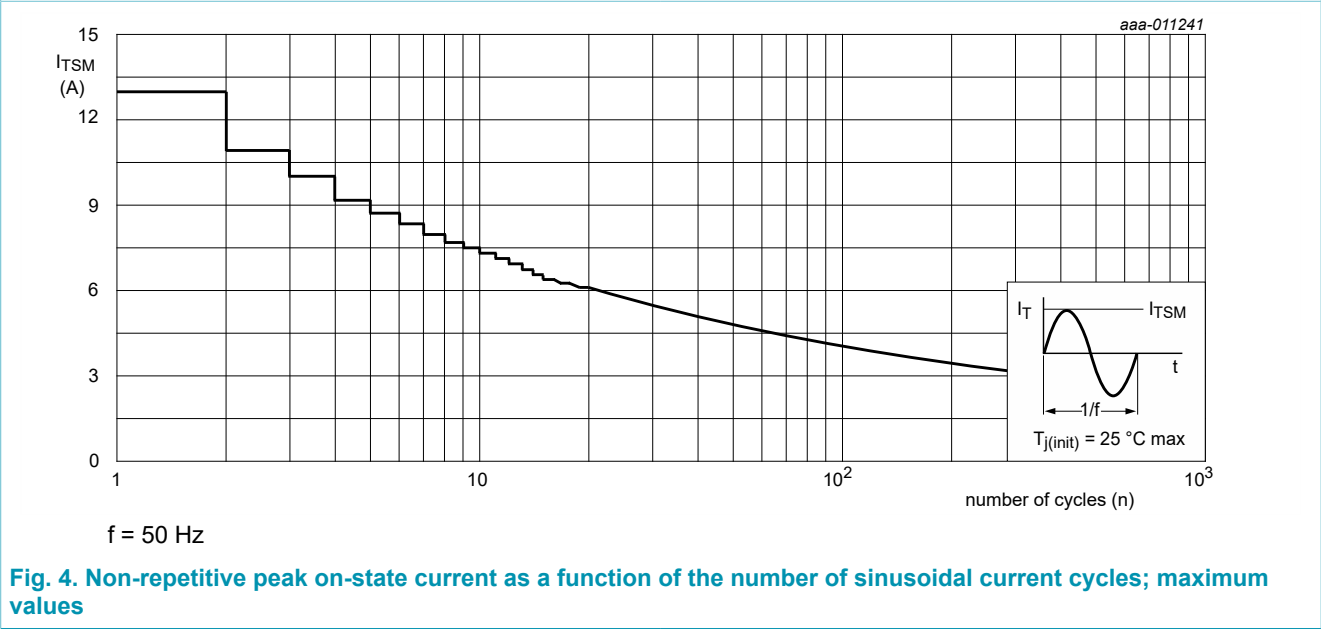
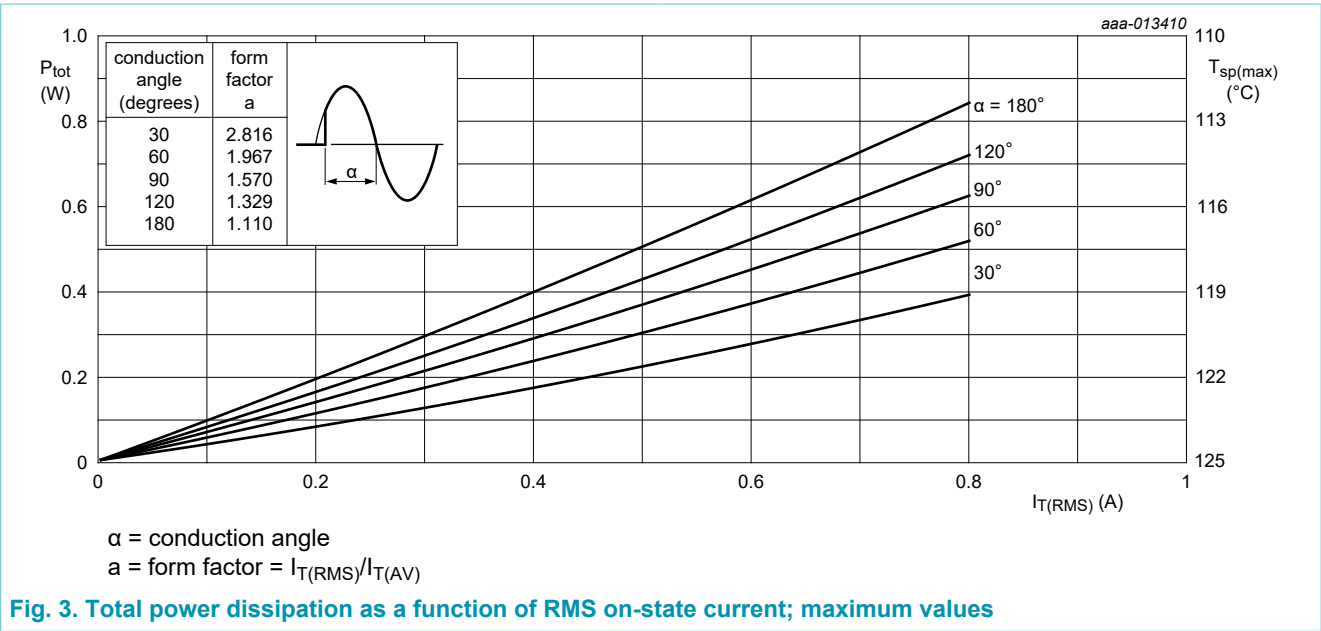


Fig. 1. RMS on-state current as a function of solder point temperature; maximum values



$f = 50\text{ Hz}$
 $T_{\text{sp}} = 112\text{ }^{\circ}\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values



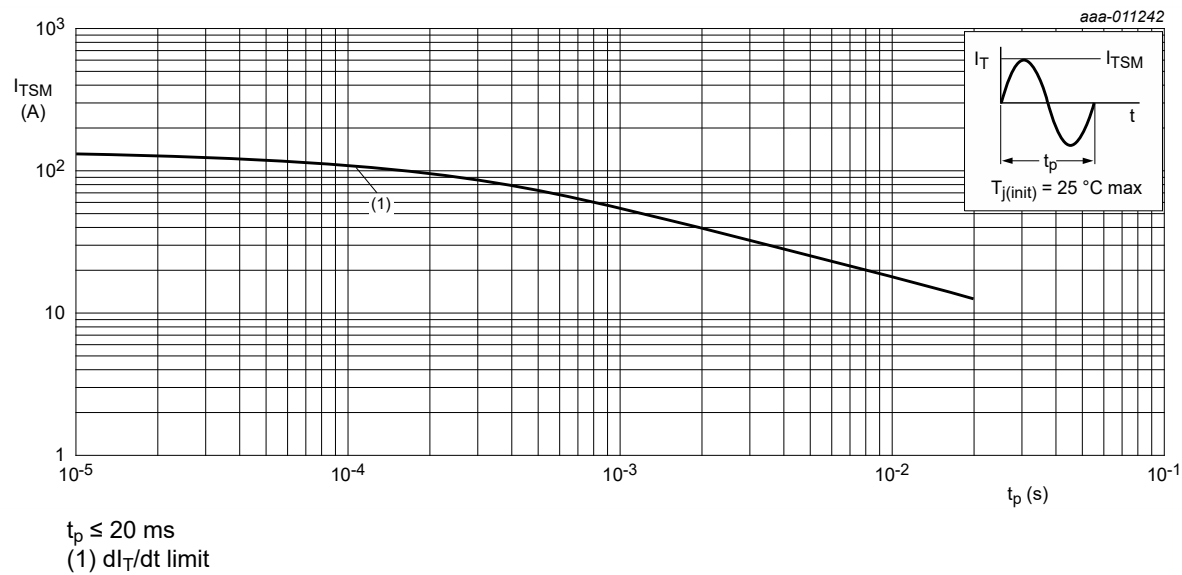


Fig. 5. Non-repetitive peak on-state current as a function of pulse width; maximum values

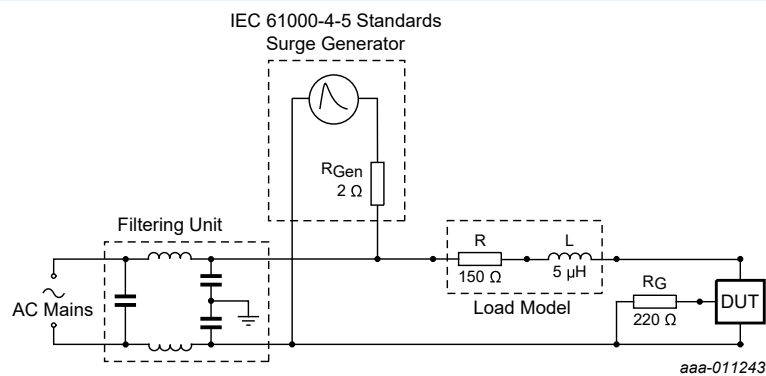


Fig. 6. Test circuit for inductive and resistive loads with conditions equivalent to IEC 61000-4-5

8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	full cycle with heatsink compound;; Fig. 7	-	-	15	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air; printed circuit board mounted; minimum footprint; Fig. 8	-	156	-	K/W
		in free air; printed circuit board mounted; pad area; Fig. 9	-	70	-	K/W

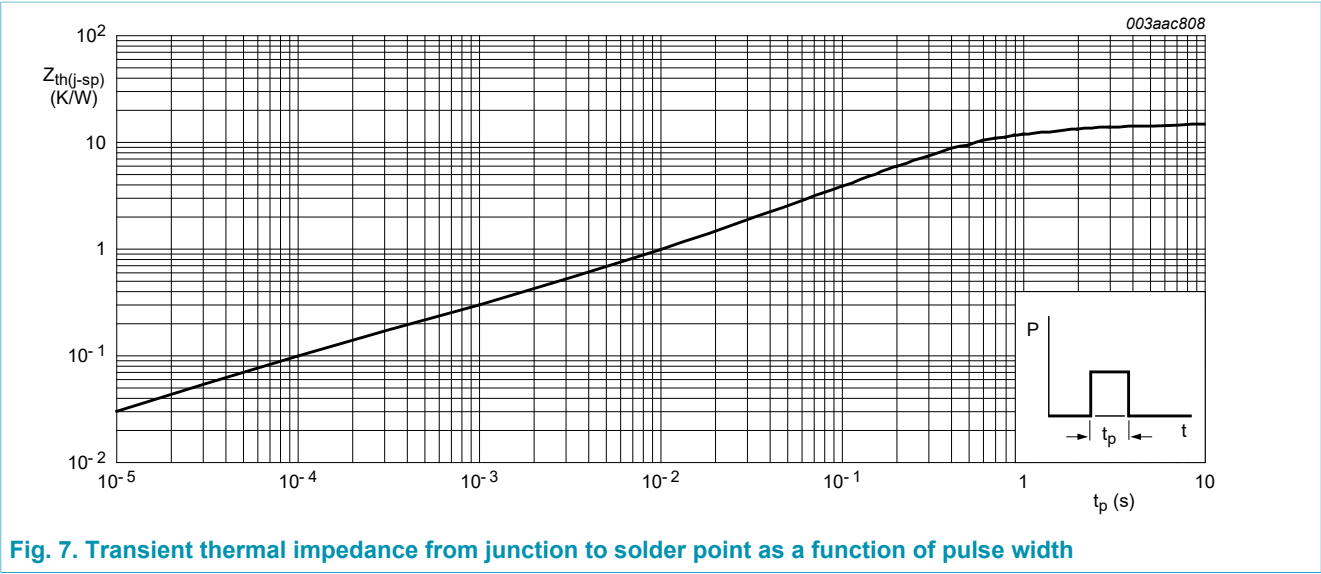
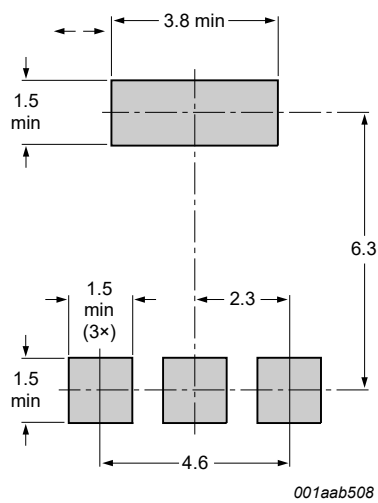
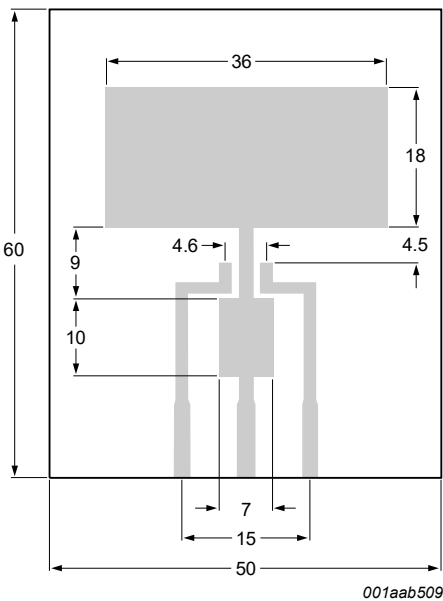


Fig. 7. Transient thermal impedance from junction to solder point as a function of pulse width



All dimensions are in mm

Fig. 8. Minimum footprint SOT223



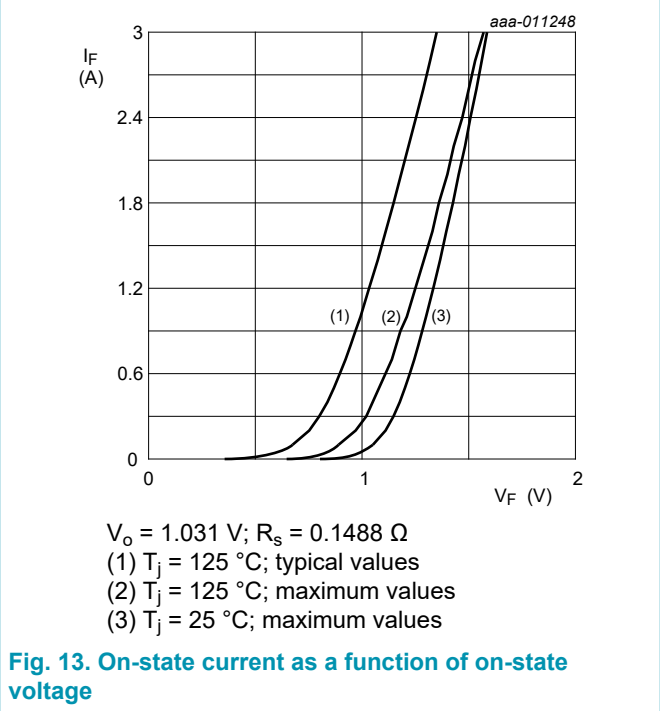
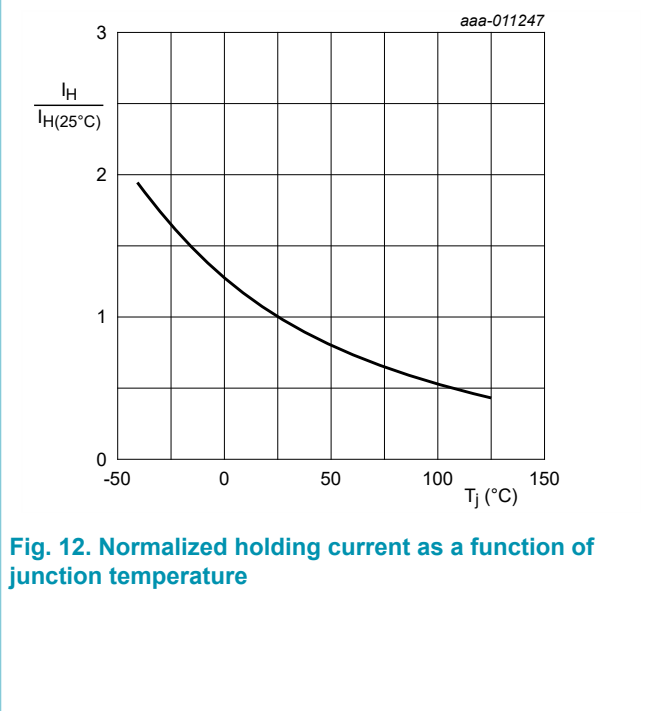
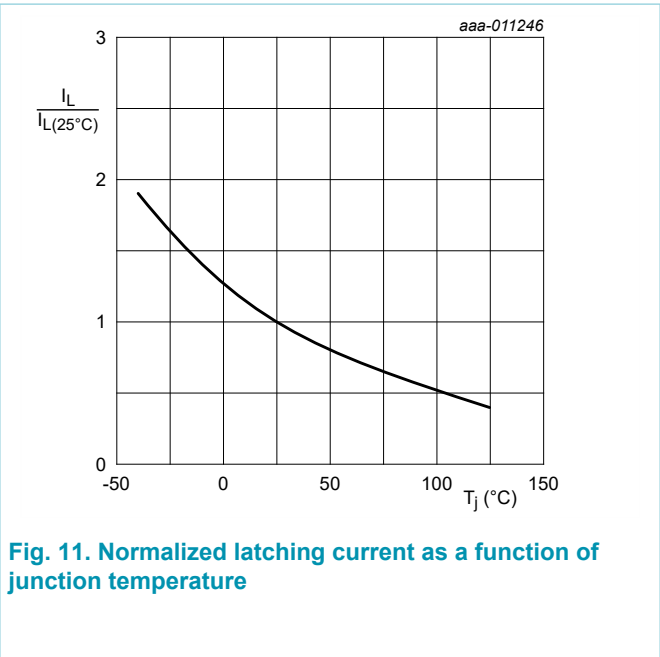
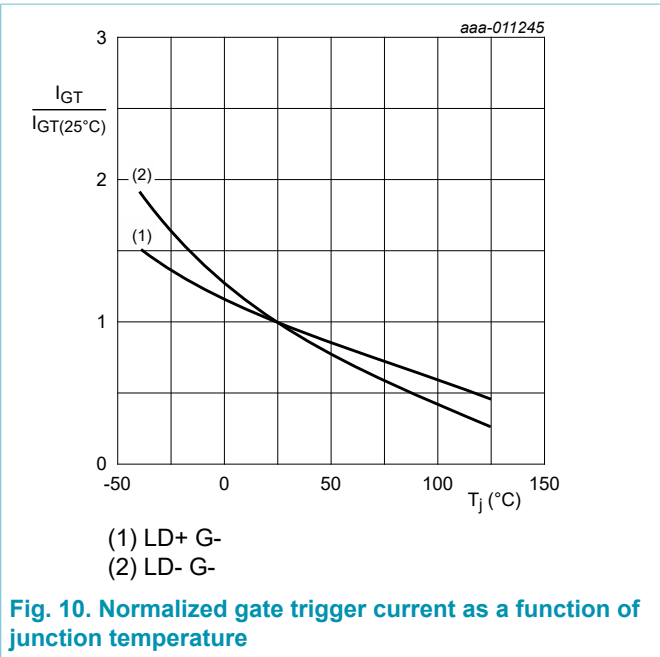
All dimensions are in mm
Printed circuit board:
FR4 epoxy glass (1.6 mm thick), copper laminate
(35 um thick)

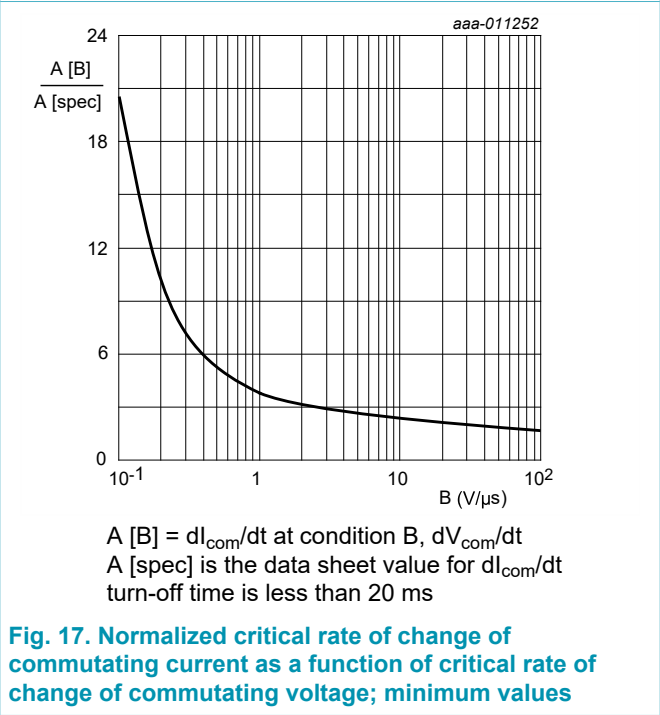
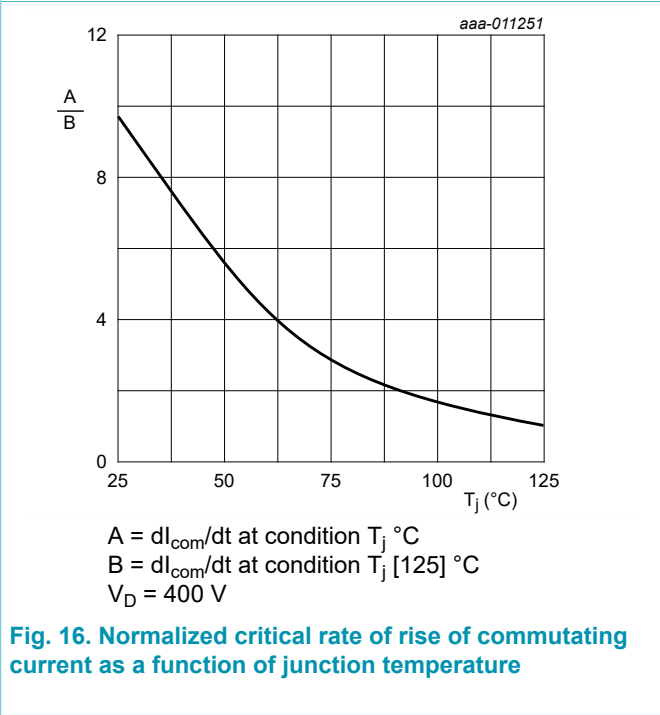
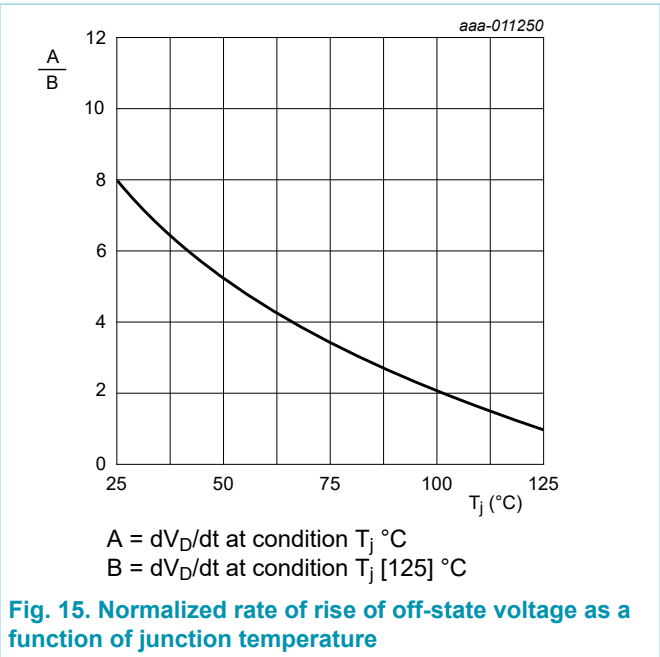
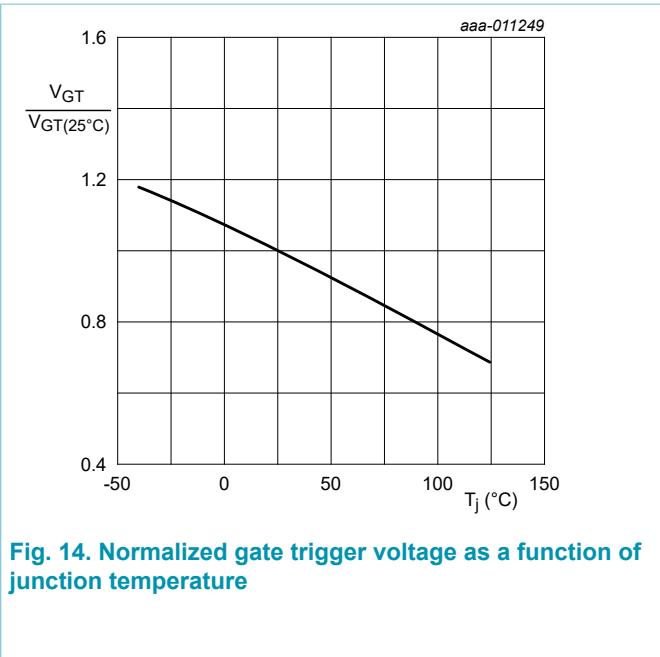
Fig. 9. Printed circuit board pad area: SOT223

9. Characteristics

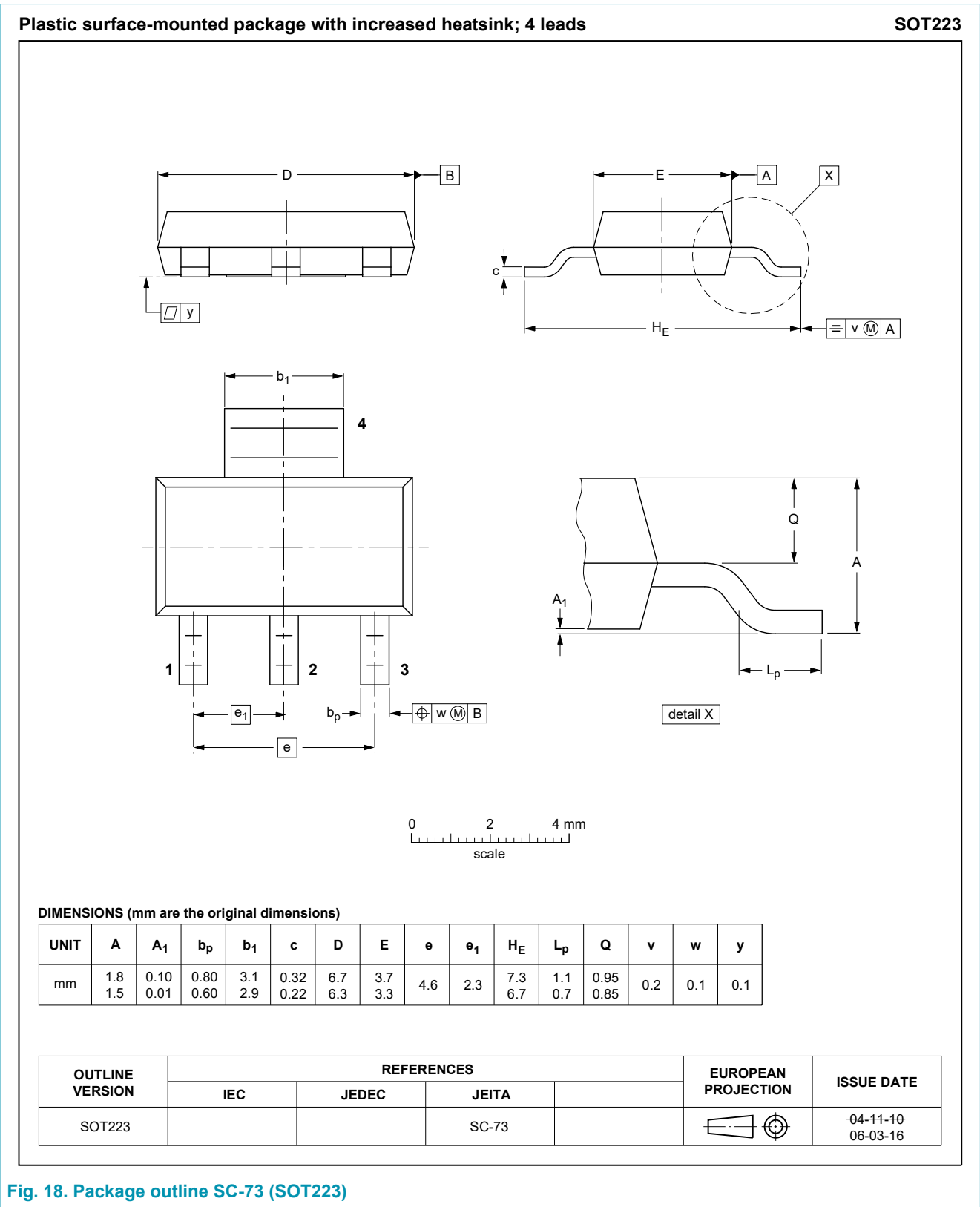
Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 100\text{ mA}$; LD+ G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10	1	-	10	mA
		$V_D = 12\text{ V}$; $I_T = 100\text{ mA}$; LD- G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 10	1	-	10	mA
I_L	latching current	$V_D = 12\text{ V}$; $I_G = 100\text{ mA}$; LD+ G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 11	-	-	25	mA
		$V_D = 12\text{ V}$; $I_G = 100\text{ mA}$; LD- G-; $T_j = 25\text{ }^\circ\text{C}$; Fig. 11	-	-	20	mA
I_H	holding current	$V_D = 12\text{ V}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 12	-	-	20	mA
V_T	on-state voltage	$I_T = 1.1\text{ A}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 13	-	-	1.3	V
V_{GT}	gate trigger voltage	$V_D = 400\text{ V}$; $I_T = 100\text{ mA}$; $T_j = 125\text{ }^\circ\text{C}$; Fig. 14	0.15	-	-	V
		$V_D = 12\text{ V}$; $I_T = 100\text{ mA}$; $T_j = 25\text{ }^\circ\text{C}$; Fig. 14	-	-	1	V
I_D	off-state current	$V_D = 800\text{ V}$; $T_j = 25\text{ }^\circ\text{C}$	-	-	2	μA
		$V_D = 800\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$	-	-	0.2	mA
V_{CL}	clamping voltage	$I_{CL} = 0.1\text{ mA}$; $t_p = 1\text{ ms}$; $T_j = 25\text{ }^\circ\text{C}$	850	-	-	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit; Fig. 15	500	-	-	V/ μs
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}$; $T_j = 125\text{ }^\circ\text{C}$; $I_{T(RMS)} = 0.8\text{ A}$; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$; (snubberless condition); gate open circuit; Fig. 16 ; Fig. 17	0.5	-	-	A/ms





10. Package outline



11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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