



Title of Change:	Former Fairchild TinyLogic® in SC70 and Fab Assembly Material and Assembly Location changing to SC88 and Datasheet Update		
Proposed first ship date:	21 April 2019		
Contact information:	Contact your local ON Semiconductor Sales Office or <logic.fpcn@onsemi.com >		
Samples:	Contact your local ON Semiconductor Sales Office or <PCN.samples@onsemi.com> Sample requests are to be submitted no later than 30 days from the date of first notification, Initial PCN or Final PCN, for this change.		
Additional Reliability Data:	Contact your local ON Semiconductor Sales Office or <ChangKit.Mok@onsemi.com>		
Type of notification:	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. ON Semiconductor will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact <PCN.Support@onsemi.com>		
Change Part Identification:	Marking Style will be change. CS code on the label will be changed from US to JP.		
Change Category:	☒ Wafer Fab Change ☒ Assembly Change ☒ Test Change ☐ Other _____		
Change Sub-Category(s):	☐ Manufacturing Site Addition ☒ Material Change ☒ Datasheet/Product Doc change ☒ Manufacturing Site Transfer ☐ Product specific change ☒ Shipping/Packaging/Marking ☒ Manufacturing Process Change ☐ Other: _____		
Sites Affected:	ON Semiconductor Sites: ON Leshan, China ON Cebu Philippines ON S. Portland Maine	External Foundry/Subcon Sites: Subcon China, Foundry Japan	
Description and Purpose:			
FPCN22347 is issued to qualify new die source in Japan for TinyLogic® to increase the front end and back end capacity and standardizing materials.			
	Before Change (Existing flow)		After Change (New flow)
Assy Site	Subcon China	Onsemi Philippines	Onsemi Leshan
Wire	Au	Au	Cu
Lead frame	LF SC88 6L 70X86 PPF	LF SC 88 6L C194 STAMPED	SC88 Alloy 42 lead frame with Vgroove
Mold Compound	MC SUMITOMO G600 HF	MC GREEN PA CK5000A 13MMX3.9G	Henkel GR640 HV-L1
Die Attach	DA EPOXY ABLESTICK 2200D	DA EPXY HE ABLESTIK 84-1LMISR4 5CC	Eutectic
Plating	Preplated	100% Sn	100% Sn
Die Source	On South Portland	On South Portland	Foundry Japan



	From		To
	Subcon China	Onsemi Philippines	New Flow
Product marking change	针对于:SC70-5L, SC70-6L Year code Week code Pin 1 dot Plant Code (公司代码) Above marking is for subcon China, For Onsemi Philippines there will be no plant code identifier		MA = Device Marking M = Date Code* ■ = Pb-Free Package (Note: Microdot may be in either location) *Date Code orientation and/or position may vary depending upon manufacturing location.

For NC7SZ Family:

Existing datasheet

Corrected voltage level over recommended operating condition.

Description

Semiconductor's Ultra-High Speed (UHS) series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} operating range. The inputs and output are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 6V, independent of V_{CC} operating voltage.

Changed 2V high and low input voltages.

V_{IH}	HIGH Level Input Voltage	1.65 to 1.95 2.30 to 5.50	0.75 V_{CC} 0.70 V_{CC}	0.75 V_{CC} 0.70 V_{CC}	V
V_{IL}	LOW Level Input Voltage	1.65 to 1.95 2.30 to 5.50	0.25 V_{CC} 0.30 V_{CC}	0.25 V_{CC} 0.30 V_{CC}	V

Corrected IIN VCC condition

I_{in}	Input Leakage Current	0 to 5.5 0 $\geq V_{in} \geq 5.5$ V	≤ 1	≤ 10	μ A
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New

Description

Semiconductor's Ultra-High Speed (UHS) series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} operating range. The inputs and output are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 5.5V, independent of V_{CC} operating voltage.

V_{IH}	HIGH Level Input Voltage	1.65 to 1.95 2.30 to 5.50	0.65 V_{CC} 0.70 V_{CC}	0.65 V_{CC} 0.70 V_{CC}	V
V_{IL}	LOW Level Input Voltage	1.65 to 1.95 2.30 to 5.50	0.35 V_{CC} 0.30 V_{CC}	0.35 V_{CC} 0.30 V_{CC}	V

I_{in}	Input Leakage Current	1.65 to 5.5 0 $\geq V_{in} \geq 5.5$ V	≤ 1	≤ 10	μ A
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Existing datasheet

Positive and Negative threshold specs will be corrected on devices with hysteresis.

Symbol	Parameter	V _{CC} (V)	Conditions	T _A =+25°C			T _A =-40 to +85°C			Units
				Min.	Typ.	Max.	Min.	Typ.	Max.	
V ₊	Positive Threshold Voltage	1.65		0.60	1.00	1.40	0.60	1.00	1.40	V
		1.80		0.70	1.10	1.50	0.70	1.10	1.50	
		2.30		1.00	1.40	1.80	1.00	1.40	1.80	
		3.00		1.30	1.75	2.20	1.30	1.75	2.20	
		4.50		1.90	2.45	3.10	1.90	2.45	3.10	
		5.50		2.20	2.90	3.60	2.20	2.90	3.60	
V ₋	Negative Threshold Voltage	1.65		0.20	0.50	0.90	0.20	0.50	0.90	V
		1.80		0.25	0.55	0.90	0.25	0.55	0.90	
		2.30		0.40	0.75	1.15	0.40	0.75	1.15	
		3.00		0.60	1.00	1.50	0.60	1.00	1.50	
		4.50		1.00	1.43	2.00	1.00	1.43	2.00	
		5.50		1.20	1.70	2.30	1.20	1.70	2.30	

Min limits will be removed on all propagation delay and tri-state specifications

Symbol	Parameter	V _{CC}	Conditions	T _A =+25°C			T _A =-40 to +85°C			Units	Figure
				Min.	Typ.	Max.	Min.	Typ.	Max.		
t _{PLH} , t _{PLL}	Propagation Delay	1.65		2.0	6.4	13.2	2.0	6.4	13.2	ns	Figure 4 Figure 6
		1.80	C _L =15 pF, R _{th} =1 MΩ S ₁ =OPEN	2.0	5.3	11.0	2.0	5.3	11.0		
		2.50 to 2.0		0.8	3.4	7.5	0.8	3.4	7.5		
		3.30 to 3.0		0.5	2.5	5.2	0.5	2.5	5.2		
		5.00 to 5.0		0.5	2.1	4.5	0.5	2.1	4.5		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω S ₁ =OPEN	1.5	3.2	5.7	1.5	3.2	5.7		
t _{OL} , t _{OH}	Output Enable Time	1.65		2.0	8.4	15.0	2.0	8.4	15.0	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _{th} =500 Ω	2.0	7.0	12.5	2.0	7.0	12.5		
		2.50 to 2.0	R _{th} =500 Ω S ₁ =0ND for t _{OL} S ₁ =V _{CC} for t _{OH}	1.5	4.6	9.5	1.5	4.6	9.5		
		3.30 to 3.0		1.5	3.5	6.2	1.5	3.5	6.2		
		5.00 to 5.0		0.8	2.8	5.5	0.8	2.8	5.5		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω	2.0	6.5	13.2	2.0	6.5	13.2		
t _{UL} , t _{UL}	Output Disable Time	1.65		2.0	5.4	11.0	2.0	5.4	11.0	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _{th} =500 Ω	2.0	5.4	11.0	2.0	5.4	11.0		
		2.50 to 2.0	R _{th} =500 Ω S ₁ =0ND for t _{UL} S ₁ =V _{CC} for t _{UL}	1.5	3.5	6.0	1.5	3.5	6.0		
		3.30 to 3.0		1.0	2.8	5.7	1.0	2.8	5.7		
		5.00 to 5.0		0.5	2.1	4.7	0.5	2.1	4.7		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω	2.0	6.5	13.2	2.0	6.5	13.2		

New

Symbol	Parameter	V _{CC} (V)	Conditions	T _A =+25°C			T _A =-40 to +85°C			Units
				Min.	Typ.	Max.	Min.	Typ.	Max.	
V ₊	Positive Threshold Voltage	1.65		0.60	1.00	1.40	0.60	1.00	1.40	V
		1.80		0.70	1.10	1.50	0.70	1.10	1.50	
		2.30		1.00	1.40	1.80	1.00	1.40	1.80	
		3.00		1.30	1.75	2.20	1.30	1.75	2.20	
		4.50		1.90	2.45	3.10	1.90	2.45	3.10	
		5.50		2.20	2.90	3.60	2.20	2.90	3.60	
V ₋	Negative Threshold Voltage	1.65		0.20	0.50	0.90	0.20	0.50	0.90	V
		1.80		0.25	0.55	0.90	0.25	0.55	0.90	
		2.30		0.40	0.75	1.15	0.40	0.75	1.15	
		3.00		0.60	1.00	1.50	0.60	1.00	1.50	
		4.50		1.00	1.43	2.00	1.00	1.43	2.00	
		5.50		1.20	1.70	2.30	1.20	1.70	2.30	

Symbol	Parameter	V _{CC}	Conditions	T _A =+25°C			T _A =-40 to +85°C			Units	Figure
				Min.	Typ.	Max.	Min.	Typ.	Max.		
t _{PLH} , t _{PLL}	Propagation Delay	1.65		2.0	6.4	13.2	2.0	6.4	13.2	ns	Figure 4 Figure 6
		1.80	C _L =15 pF, R _{th} =1 MΩ S ₁ =OPEN	2.0	5.3	11.0	2.0	5.3	11.0		
		2.50 to 2.0		0.8	3.4	7.5	0.8	3.4	7.5		
		3.30 to 3.0		0.5	2.5	5.2	0.5	2.5	5.2		
		5.00 to 5.0		0.5	2.1	4.5	0.5	2.1	4.5		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω S ₁ =OPEN	1.5	3.2	5.7	1.5	3.2	5.7		
t _{OL} , t _{OH}	Output Enable Time	1.65		2.0	8.4	15.0	2.0	8.4	15.0	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _{th} =500 Ω	2.0	7.0	12.5	2.0	7.0	12.5		
		2.50 to 2.0	R _{th} =500 Ω S ₁ =0ND for t _{OL} S ₁ =V _{CC} for t _{OH}	1.5	4.6	9.5	1.5	4.6	9.5		
		3.30 to 3.0		1.5	3.5	6.2	1.5	3.5	6.2		
		5.00 to 5.0		0.8	2.8	5.5	0.8	2.8	5.5		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω	2.0	6.5	13.2	2.0	6.5	13.2		
t _{UL} , t _{UL}	Output Disable Time	1.65		2.0	5.4	11.0	2.0	5.4	11.0	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _{th} =500 Ω	2.0	5.4	11.0	2.0	5.4	11.0		
		2.50 to 2.0	R _{th} =500 Ω S ₁ =0ND for t _{UL} S ₁ =V _{CC} for t _{UL}	1.5	3.5	6.0	1.5	3.5	6.0		
		3.30 to 3.0		1.0	2.8	5.7	1.0	2.8	5.7		
		5.00 to 5.0		0.5	2.1	4.7	0.5	2.1	4.7		
		5.00 to 5.0	C _L =50 pF, R _{th} =500 Ω	2.0	6.5	13.2	2.0	6.5	13.2		

For NC7WZ Family:

Existing datasheet

Corrected voltage level over recommended operating condition.
Description

from ON Semiconductor's Ultra-High Speed (UHS) Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a very broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} range. The inputs and outputs are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 7V independent of V_{CC} operating voltage. Schmitt trigger inputs achieve typically 1V hysteresis between the positive and negative-going input threshold voltage at 5V.

Absolute max ratings of 7.0V will be changed to 6.5V

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	-0.5V to +7V
V _{IN}	DC Input Voltage	-0.5V to +7V
V _{OUT}	DC Output Voltage	-0.5V to +7V

Changed high and low input voltages.

V _{HI}	HIGH Level Input Voltage	1.65-1.95 2.3-5.5	0.75 x V _{CC} 0.7 x V _{CC}	0.75 x V _{CC} 0.7 x V _{CC}	V
V _{LI}	LOW Level Input Voltage	1.65-1.95 2.3-5.5	0.25 x V _{CC} 0.3 x V _{CC}	0.25 x V _{CC} 0.3 x V _{CC}	V

Corrected IIN VCC condition

I _{IN}	Input Leakage Current	0 to 5.5	0 ≤ V _{IN} ≤ 5.5 V	±1	±10	μA
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New

Description

from ON Semiconductor's Ultra-High Speed (UHS) Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a very broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} range. The inputs and outputs are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 5.5V independent of V_{CC} operating voltage. Schmitt trigger inputs achieve typically 1V hysteresis between the positive and negative-going input threshold voltage at 5V.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	-0.5V to +6.5V
V _{IN}	DC Input Voltage	-0.5V to +6.5V
V _{OUT}	DC Output Voltage	-0.5V to +6.5V

V _{HI}	HIGH Level Input Voltage	1.65-1.95 2.3-5.5	0.65 x V _{CC} 0.7 x V _{CC}	0.65 x V _{CC} 0.7 x V _{CC}	V
V _{LI}	LOW Level Input Voltage	1.65-1.95 2.3-5.5	0.35 x V _{CC} 0.3 x V _{CC}	0.35 x V _{CC} 0.3 x V _{CC}	V

I _{IN}	Input Leakage Current	1.85 to 5.5	0 ≤ V _{IN} ≤ 5.5 V	±1	±10	μA
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Existing datasheet

Positive and Negative threshold specs will be corrected on devices with hysteresis.

Symbol	Parameter	V _{cc} (V)	Conditions	T _a = +25°C			T _a = -40 to +85°C			T _a = -40 to +125°C			Units
				Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
V _{th}	Positive Threshold Voltage	1.65		0.60		1.40	0.60	1.40	0.60	1.40			V
		1.80		0.70		1.50	0.70	1.50	0.70	1.50			
		2.30		1.00		1.80	1.00	1.80	1.00	1.80			
		3.00		1.30		2.20	1.30	2.20	1.30	2.20			
		4.50		1.90		3.10	1.90	3.10	2.00	3.20			
		5.50		2.20		3.60	2.20	3.60	2.30	3.70			
V _{th}	Negative Threshold Voltage	1.65		0.20	0.50	0.80	0.20	0.80	0.30	0.90			V
		1.80		0.25	0.56	0.90	0.25	0.90	0.35	1.00			
		2.30		0.40	0.75	1.15	0.40	1.15	0.50	1.20			
		3.00		0.60	0.98	1.50	0.60	1.50	0.70	1.60			
		4.50		1.00	1.42	2.00	1.00	2.00	1.10	2.20			
		5.50		1.20	1.68	2.30	1.20	2.30	1.40	2.50			

New

Symbol	Parameter	V _{cc} (V)	Conditions	T _a = +25°C			T _a = -40 to +85°C			T _a = -40 to +125°C			Units
				Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
V _{th}	Positive Threshold Voltage	1.65				1.40			1.40			1.40	V
		1.80				1.50			1.50			1.50	
		2.30				1.80			1.80			1.80	
		3.00				2.20			2.20			2.20	
		4.50				3.10			3.10			3.20	
		5.50				3.60			3.60			3.70	
V _{th}	Negative Threshold Voltage	1.65		0.20	0.50	0.80	0.20	0.80	0.30	0.90			V
		1.80		0.25	0.56	0.90	0.25	0.90	0.35	1.00			
		2.30		0.40	0.75	1.15	0.40	1.15	0.50	1.20			
		3.00		0.60	0.98	1.50	0.60	1.50	0.70	1.60			
		4.50		1.00	1.42	2.00	1.00	2.00	1.10	2.20			
		5.50		1.20	1.68	2.30	1.20	2.30	1.40	2.50			

Min limits will be removed on all propagation delay and tri-state specifications

Symbol	Parameter	V _{cc} (V)	T _a = +25°C			T _a = -40 to +85°C			Units	Conditions	Figure Number
			Min.	Typ.	Max.	Min.	Typ.	Max.			
t _{PLH}	Propagation Delay	1.8 ± 0.15	2.0		12.0	2.0		13.0	ns	C _L = 15 pF R _G = 1 MΩ S1 = Open	Figures 1, 3
t _{PLH}	Propagation Delay	2.5 ± 0.2	1.0		7.5	1.0		8.0			
t _{PLH}	Propagation Delay	3.3 ± 0.3	0.8		5.3	0.8		5.5			
t _{PLH}	Propagation Delay	5.0 ± 0.5	0.5		4.5	0.5		4.8	ns	C _L = 50 pF R _G = 500Ω S1 = Open	Figures 1, 3
t _{PLH}	Propagation Delay	3.3 ± 0.3	1.2		5.7	1.2		6.0			
t _{PLH}	Propagation Delay	5.0 ± 0.5	0.8		5.0	0.8		5.3			
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0			1.0	ns	C _L = 50 pF R _G = 500Ω S1 = Open	Figures 1, 3
t _{OSLH}	Output to Output Slew (Note 5)	5.0 ± 0.5			0.8			0.8			
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0			1.0			
t _{PLZ}	Output Enable Time	1.8 ± 0.15	3.0		14.0	3.0		15.0	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	2.5 ± 0.2	1.8		8.5	1.8		9.0			
t _{PLZ}	Output Enable Time	3.3 ± 0.3	1.2		6.2	1.2		6.5			
t _{PLZ}	Output Enable Time	5.0 ± 0.5	0.8		5.5	0.8		5.8	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	1.8 ± 0.15	2.5		12.0	2.5		13.0			
t _{PLZ}	Output Enable Time	2.5 ± 0.2	1.8		8.0	1.8		8.5			
t _{PLZ}	Output Enable Time	3.3 ± 0.3	0.8		5.7	0.8		6.0	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	5.0 ± 0.5	0.3		4.7	0.3		5.0			
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0			13.0			

Symbol	Parameter	V _{cc} (V)	T _a = +25°C			T _a = -40 to +85°C			Units	Conditions	Figure Number
			Min.	Typ.	Max.	Min.	Typ.	Max.			
t _{PLH}	Propagation Delay	1.8 ± 0.15			12.0			13.0	ns	C _L = 15 pF R _G = 1 MΩ S1 = Open	Figures 1, 3
t _{PLH}	Propagation Delay	2.5 ± 0.2			7.5			8.0			
t _{PLH}	Propagation Delay	3.3 ± 0.3			5.3			5.5			
t _{PLH}	Propagation Delay	5.0 ± 0.5			4.5			4.8	ns	C _L = 50 pF R _G = 500Ω S1 = Open	Figures 1, 3
t _{PLH}	Propagation Delay	3.3 ± 0.3			5.7			6.0			
t _{PLH}	Propagation Delay	5.0 ± 0.5			5.0			5.3			
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0			1.0	ns	C _L = 50 pF R _G = 500Ω S1 = Open	Figures 1, 3
t _{OSLH}	Output to Output Slew (Note 5)	5.0 ± 0.5			0.8			0.8			
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0			1.0			
t _{PLZ}	Output Enable Time	1.8 ± 0.15			14.0			15.0	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	2.5 ± 0.2			8.5			9.0			
t _{PLZ}	Output Enable Time	3.3 ± 0.3			6.2			6.5			
t _{PLZ}	Output Enable Time	5.0 ± 0.5			5.5			5.8	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0			13.0			
t _{PLZ}	Output Enable Time	2.5 ± 0.2			8.0			8.5			
t _{PLZ}	Output Enable Time	3.3 ± 0.3			5.7			6.0	ns	C _L = 50 pF R _G = 500Ω S1 = GND for t _{PLZ} S1 = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	5.0 ± 0.5			4.7			5.0			
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0			13.0			

Reliability Data Summary:

QV DEVICE NAME: NLV27WZ14DFT2G

RMS: L51921

PACKAGE: SC88

Test	Specification	Condition	Interval	Results
HTOL	JESD22-A108	Ta=125°C, 100 % max rated Vcc	1008 hrs	0/288
HTSL	JESD22-A103	Ta= 150°C	1008 hrs	0/252
TC	JESD22-A104	Ta= -65°C to +150°C	500 cyc	0/297
HAST	JESD22-A110	130°C, 85% RH, 18.8psig, bias	96 hrs	0/273
uHAST	JESD22-A118	130°C, 85% RH, 18.8psig, unbiased	96 hrs	0/234
PC	J-STD-020 JESD-A113	MSL 1 @260 °C		0/804
RSH	JESD22- B106	Ta = 265C, 10 sec		0/30

Electrical Characteristic Summary:

Electrical characteristics available upon request.

**List of Affected Parts:**

Note: Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the [**PCN Customized Portal**](#).

Part Number	Qualification Vehicle
NC7SZ10P6X	NLV27WZ14DFT2G
NC7SZ11P6X	NLV27WZ14DFT2G
NC7SZ157P6X	NLV27WZ14DFT2G
NC7SZ175P6X	NLV27WZ14DFT2G
NC7SZ18P6X	NLV27WZ14DFT2G
NC7SZ19P6X	NLV27WZ14DFT2G
NC7SZ27P6X	NLV27WZ14DFT2G
NC7SZ332P6X	NLV27WZ14DFT2G
NC7SZ373P6X	NLV27WZ14DFT2G
NC7SZ374P6X	NLV27WZ14DFT2G
NC7SZ386P6X	NLV27WZ14DFT2G
NC7SZ57P6X	NLV27WZ14DFT2G
NC7SZ58P6X	NLV27WZ14DFT2G
NC7WZ04P6X	NLV27WZ14DFT2G
NC7WZ07P6X	NLV27WZ14DFT2G
NC7WZ14EP6X	NLV27WZ14DFT2G
NC7WZ14P6X	NLV27WZ14DFT2G
NC7WZ16P6X	NLV27WZ14DFT2G
NC7WZ17P6X	NLV27WZ14DFT2G
NC7WZU04P6X	NLV27WZ14DFT2G

Japanese translation of the notification starts here.
通知の日本語訳はここから始まります。

Note: *The Japanese version is for reference only. In case of any differences between the English and Japanese version, the English version shall control.*

注：日本語版は参照用です。英語版と日本語版の違いがある場合は、英語版が優先されます。



最終製品 / プロセス変更通知

文書番号# : FPCN22347X

発行日 : 14 January 2019

変更件名:	旧 FairchildTinyLogic® (SC70) についての Fab 拠点、組立て材料、および組立て拠点の変更、ならびにデータシートの更新																																		
初回出荷予定日:	21 April 2019																																		
連絡先情報:	現地のオン・セミコンダクター営業所または <logic.fpcn@onsemi.co>までお問い合わせください。																																		
サンプル:	現地のオン・セミコンダクター営業所または <PCN.samples@onsemi.com> までお問い合わせください。 サンプルは、この変更の初回通知、初回 PCN または最終 PCN の日付から 30 日以内に要求してください。																																		
その他の信頼性データ:	現地のオン・セミコンダクター営業所または <ChangKit.Mok@onsemi.com> までお問い合わせください。																																		
通知種別:	これは、お客様宛の最終製品 / プロセス変更通知 (FPCN) です。FPCN は、変更実施の 90 日前に発行されます。オン・セミコンダクターは、この通知の送付から 30 日以内に書面による問い合わせがない限り、この変更が承諾されたものとみなします。お問い合わせは、<PCN.Support@onsemi.com> 宛てにお願いします。																																		
変更部品の識別:	マーキング様式が変更されます。ラベルの CS (もしくは DIFFUSED IN) コードは US から JP に変更されます																																		
変更カテゴリ:	☒ ウェハファブの変更 ☒ アセンブリの変更 ☒ 試験の変更 ☐ その他 _____																																		
変更サブカテゴリ:	☐ 製造拠点の追加 ☒ 材料の変更 ☒ データシート/製品資料の変更 ☒ 製造拠点の移転 ☐ 製品仕様の変更 ☒ 出荷/パッケージング/表記 ☒ 製造プロセスの変更 ☐ その他 : _____																																		
影響を受ける拠点:	オン・セミコンダクター拠点: ON Leshan, China ON Cebu Philippines ON S. Portland Maine	外部製造工場 / 下請業者拠点: Subcon China, Foundry Japan																																	
説明および目的: FPCN22347 は TinyLogic® の生産能力を増強するため日本の新たなダイソースを認定し、組立て材料の標準化を図ることを通知するためのものです。																																			
	<table border="1"> <thead> <tr> <th></th> <th colspan="2">変更前の表記</th> <th>変更後の表記</th> </tr> </thead> <tbody> <tr> <td>組み立て拠点</td> <td>Subcon China</td> <td>Onsemi Philippines</td> <td>Onsemi Leshan</td> </tr> <tr> <td>ワイヤ</td> <td>Au</td> <td>Au</td> <td>Cu</td> </tr> <tr> <td>リードフレーム</td> <td>LF SC88 6L 70X86 PPF</td> <td>LF SC 88 6L C194 STAMPED</td> <td>SC88 Alloy 42 lead frame with Vgroove</td> </tr> <tr> <td>モールド・コンパウンド</td> <td>MC SUMITOMO G600 HF</td> <td>MC GREEN PA CK5000A 13MMX3.9G</td> <td>Henkel GR640 HV-L1</td> </tr> <tr> <td>ダイ接着剤</td> <td>DA EPOXY ABLESTICK 2200D</td> <td>DA EPXY HE ABLESTIK 84-1LMISR4 5CC</td> <td>Eutectic</td> </tr> <tr> <td>メッキ</td> <td>Preplated</td> <td>100% Sn</td> <td>100% Sn</td> </tr> <tr> <td>ダイソース</td> <td>On South Portland</td> <td>On South Portland</td> <td>Foundry Japan</td> </tr> </tbody> </table>				変更前の表記		変更後の表記	組み立て拠点	Subcon China	Onsemi Philippines	Onsemi Leshan	ワイヤ	Au	Au	Cu	リードフレーム	LF SC88 6L 70X86 PPF	LF SC 88 6L C194 STAMPED	SC88 Alloy 42 lead frame with Vgroove	モールド・コンパウンド	MC SUMITOMO G600 HF	MC GREEN PA CK5000A 13MMX3.9G	Henkel GR640 HV-L1	ダイ接着剤	DA EPOXY ABLESTICK 2200D	DA EPXY HE ABLESTIK 84-1LMISR4 5CC	Eutectic	メッキ	Preplated	100% Sn	100% Sn	ダイソース	On South Portland	On South Portland	Foundry Japan
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ダイソース	On South Portland	On South Portland	Foundry Japan																																



	変更前		変更後
	Subcon China	Onsemi Philippines	New Flow
製品表示変更	针对于:SC70-5L, SC70-6L Year code Week code Pin 1 dot Plant Code (公司代码) Above marking is for subcon China, For Onsemi Philippines there will be no plant code identifier		MA = Device Marking M = Date Code* ▪ = Pb-Free Package (Note: Microdot may be in either location) *Date Code orientation and/or position may vary depending upon manufacturing location.

NC7SZ ファミリーについて:

Existing datasheet

Corrected voltage level over recommended operating condition.

Description

Semiconductor's Ultra-High Speed (UHS) series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} operating range. The inputs and output are high-impedance when V_{CC} is 0V. Inputs tolerate voltages up to 6V, independent of V_{CC} operating voltage.

Changed 2V high and low input voltages.

V_{in}	HIGH Level Input Voltage	1.65 to 1.95	2.30 to 5.50	0.75V _{CC}	0.75V _{CC}	0.75V _{CC}	0.75V _{CC}	V
V_L	LOW Level Input Voltage	1.65 to 1.95	2.30 to 5.50	0.25V _{CC}	0.25V _{CC}	0.25V _{CC}	0.25V _{CC}	V

Corrected IIN VOC condition

I_{in}	Input Leakage Current	0 to 5.5	0 ≤ V _{in} ≤ 5.5 V	±1	±10	μA
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New

Description

Semiconductor's Ultra-High Speed (UHS) series of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} operating range. The inputs and output are high-impedance when V_{CC} is 0V. Inputs tolerate voltages up to 5.5V, independent of V_{CC} operating voltage.

V_{in}	HIGH Level Input Voltage	1.65 to 1.95	2.30 to 5.50	0.65V _{CC}	0.65V _{CC}	0.65V _{CC}	0.65V _{CC}	V
V_L	LOW Level Input Voltage	1.65 to 1.95	2.30 to 5.50	0.35V _{CC}	0.35V _{CC}	0.35V _{CC}	0.35V _{CC}	V

I_{in}	Input Leakage Current	1.65 to 5.5	0 ≤ V _{in} ≤ 5.5 V	±1	±10	μA
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Existing datasheet

Positive and Negative threshold specs will be corrected on devices with hysteresis.

Symbol	Parameter	V _{CC} (V)	Conditions	T _A =+25°C			T _A =-40 to +85°C		Units
				Min.	Typ.	Max.	Min.	Max.	
V ₊	Positive Threshold Voltage	1.65		0.60	1.00	1.40	0.60	1.40	V
		1.80		0.70	1.10	1.50	0.70	1.50	
		2.30		1.00	1.40	1.80	1.00	1.80	
		3.00		1.30	1.75	2.20	1.30	2.20	
		4.50		1.90	2.45	3.10	1.90	3.10	
		5.50		2.20	2.90	3.60	2.20	3.60	
V ₋	Negative Threshold Voltage	1.65		0.20	0.50	0.90	0.20	0.90	V
		1.80		0.25	0.55	0.90	0.25	0.90	
		2.30		0.40	0.75	1.15	0.40	1.15	
		3.00		0.60	1.00	1.50	0.60	1.50	
		4.50		1.00	1.43	2.00	1.00	2.00	
		5.50		1.20	1.70	2.30	1.20	2.30	

Min limits will be removed on all propagation delay and tri-state specifications

Timing Characteristics (Note: All timing parameters are measured at V _{CC} =1.65V, unless otherwise specified)										
Symbol	Parameter	V _{CC}	Conditions	T _A =+25°C			T _A =-40 to +85°C		Units	Figure
				Min.	Typ.	Max.	Min.	Max.		
t _{PLH}	Propagation Delay	1.65		2.0	6.4	13.2	2.0	13.8	ns	Figure 4 Figure 6
		1.80	C _L =15 pF, R _D =1 MΩ S ₁ =OPEN	2.0	5.3	11.0	2.0	11.5		
		2.50 to 2.0		0.8	3.4	7.5	0.8	8.0		
		3.00 to 3.0		0.5	2.5	5.2	0.5	5.5		
		5.00 to 5.0		0.5	2.1	4.5	0.5	4.8		
		5.50 to 5.0		1.5	3.2	5.7	1.5	6.0		
t _{OL}	Output Enable Time	1.65		2.0	8.4	15.0	2.0	15.6	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _D =500 Ω	2.0	7.0	12.5	2.0	13.0		
		2.50 to 2.0		1.5	4.0	9.5	1.5	9.0		
		3.00 to 3.0	S ₁ =0V for t _{OL} S ₂ =V _{CC} for t _{OL}	1.5	3.5	6.2	1.5	6.5		
		5.00 to 5.0	V _{DD} =2×V _{CC}	0.8	2.8	5.5	0.8	5.8		
		5.50 to 5.0		2.0	6.5	13.2	2.0	14.5		
t _{OH}	Output Disable Time	1.65		2.0	5.4	11.0	2.0	12.0	ns	Figure 4 Figure 6
		1.80	C _L =50 pF, R _D =500 Ω	2.0	5.4	11.0	2.0	12.0		
		2.50 to 2.0		1.5	3.5	8.0	1.5	8.5		
		3.00 to 3.0	S ₁ =0V for t _{OH} S ₂ =V _{CC} for t _{OH}	1.0	2.8	5.7	1.0	6.0		
		5.00 to 5.0	V _{DD} =2×V _{CC}	0.5	2.1	4.7	0.5	5.0		
		5.50 to 5.0		0.5	2.1	4.7	0.5	5.0		

New

Symbol	Parameter	V _{CC} (V)	Conditions	T _a =+25°C			T _a =-40 to +85°C		Units
				Min.	Typ.	Max.	Min.	Max.	
V ₊	Positive Threshold Voltage	1.65		0.60	1.00	1.40	0.60	1.40	V
		1.80		0.70	1.10	1.50	0.70	1.50	
		2.30		1.00	1.40	1.80	1.00	1.80	
		3.00		1.30	1.75	2.20	1.30	2.20	
		4.50		1.90	2.45	3.10	1.90	3.10	
		5.50		2.20	2.90	3.60	2.20	3.60	
V _s	Negative Threshold Voltage	1.65		0.20	0.50	0.90	0.20	0.90	V
		1.80		0.25	0.55	0.90	0.25	0.90	
		2.30		0.40	0.75	1.15	0.40	1.15	
		3.00		0.60	1.00	1.50	0.60	1.50	
		4.50		1.00	1.43	2.00	1.00	2.00	
		5.50		1.20	1.70	2.30	1.20	2.30	

Symbol	Parameter	V _{CC}	Conditions	T _A =+25°C			T _A =-40 to +85°C		Units	Figure
				Min.	Typ.	Max.	Min.	Max.		
t _{PLH}	Propagation Delay	1.65	C _L =15 pF, R _D =1 MΩ S ₁ =OPEN	2.0	6.4	13.2	2.0	13.8	ns	Figure 4 Figure 6
		1.80		2.0	5.3	11.0	2.0	11.5		
		2.50 to 2.0		0.8	3.4	7.5	0.8	8.0		
		3.00 to 3.0		0.5	2.5	5.2	0.5	5.5		
		5.00 to 5.0		0.5	2.1	4.5	0.5	4.8		
		5.50 to 5.0		1.5	3.2	5.7	1.5	6.0		
t _{OL}	Output Enable Time	1.65	C _L =50 pF, R _D =500 Ω R _U =500 Ω S ₁ =0V for t _{OL} S ₂ =V _{CC} for t _{OL} V _{DD} =2×V _{CC}	2.0	8.4	15.0	2.0	15.6	ns	Figure 4 Figure 6
		1.80		2.0	7.0	12.5	2.0	13.0		
		2.50 to 2.0		1.5	4.0	9.5	1.5	9.0		
		3.00 to 3.0		0.8	3.5	6.2	0.8	6.5		
		5.00 to 5.0		0.8	2.8	5.5	0.8	5.8		
		5.50 to 5.0		2.0	6.5	13.2	2.0	14.5		
t _{OL}	Output Disable Time	1.65	C _L =50 pF, R _D =500 Ω R _U =500 Ω S ₁ =0V for t _{OL} S ₂ =V _{CC} for t _{OL} V _{DD} =2×V _{CC}	2.0	5.4	11.0	2.0	12.0	ns	Figure 4 Figure 6
		1.80		2.0	5.4	11.0	2.0	12.0		
		2.50 to 2.0		1.5	3.5	8.0	1.5	8.5		
		3.00 to 3.0		1.0	2.8	5.7	1.0	6.0		
		5.00 to 5.0		0.5	2.1	4.7	0.5	5.0		
		5.50 to 5.0		0.5	2.1	4.7	0.5	5.0		

NC7WZ ファミリーについて:

Existing datasheet

Corrected voltage level over recommended operating condition.
Description

from ON Semiconductor's Ultra-High Speed (UHS) Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a very broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} range. The inputs and outputs are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 7V independent of V_{CC} operating voltage. Schmitt trigger inputs achieve typically 1V hysteresis between the positive and negative-going input threshold voltage at 5V.

Absolute max ratings of 7.0V will be changed to 6.5V

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	-0.5V to +7V
V _{IN}	DC Input Voltage	-0.5V to +7V
V _{OUT}	DC Output Voltage	-0.5V to +7V

Changed high and low input voltages.

V _{IH}	HIGH Level Input Voltage	1.65-1.95 2.3-5.5	0.75 × V _{CC} 0.7 × V _{CC}	0.75 × V _{CC} 0.7 × V _{CC}	V
V _{IL}	LOW Level Input Voltage	1.65-1.95 2.3-5.5	0.25 × V _{CC} 0.3 × V _{CC}	0.25 × V _{CC} 0.3 × V _{CC}	V

Corrected I_{IN} VCC condition

I _{IN}	Input Leakage Current	0 to 5.5 0 ≤ V _{IN} ≤ 5.5 V	±1	±10	μA
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New

Description

from ON Semiconductor's Ultra-High Speed (UHS) Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a very broad V_{CC} operating range. The device is specified to operate over the 1.65V to 5.5V V_{CC} range. The inputs and outputs are high impedance when V_{CC} is 0V. Inputs tolerate voltages up to 5.5V independent of V_{CC} operating voltage. Schmitt trigger inputs achieve typically 1V hysteresis between the positive and negative-going input threshold voltage at 5V.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	-0.5V to +6.5V
V _{IN}	DC Input Voltage	-0.5V to +6.5V
V _{OUT}	DC Output Voltage	-0.5V to +6.5V

V _{IH}	HIGH Level Input Voltage	1.65-1.95 2.3-5.5	0.65 × V _{CC} 0.7 × V _{CC}	0.65 × V _{CC} 0.7 × V _{CC}	V
V _{IL}	LOW Level Input Voltage	1.65-1.95 2.3-5.5	0.35 × V _{CC} 0.3 × V _{CC}	0.35 × V _{CC} 0.3 × V _{CC}	V

I _{IN}	Input Leakage Current	1.65 to 5.5 0 ≤ V _{IN} ≤ 5.5 V	±1	±10	μA
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Existing datasheet

Positive and Negative threshold specs will be corrected on devices with hysteresis.

Symbol	Parameter	V _{CC} (V)	Conditions	T _A = +25°C			T _A = -40 to +85°C			Units
				Min.	Typ.	Max.	Min.	Max.	Max.	
V _{th}	Positive Threshold Voltage	1.65		0.60		1.40	0.60	1.40		V
		1.80		0.70		1.50	0.70	1.50		
		2.30		1.00		1.80	1.00	1.80		
		3.00		1.30		2.20	1.30	2.20		
		4.50		1.90		3.10	1.90	3.10		
		5.50		2.30		3.60	2.30	3.70		
V _{th}	Negative Threshold Voltage	1.65		0.20	0.50	0.80	0.20	0.60	0.30	V
		1.80		0.25	0.56	0.90	0.25	0.90	0.35	
		2.30		0.40	0.75	1.15	0.40	1.15	0.50	
		3.00		0.60	0.96	1.50	0.60	1.50	0.70	
		4.50		1.00	1.42	2.00	1.00	2.00	1.10	
		5.50		1.20	1.66	2.30	1.20	2.30	1.40	

Min limits will be removed on all propagation delay and tri-state specifications

Symbol	Parameter	V _{CC} (V)	T _A = +25°C			T _A = -40 to +85°C			Units	Conditions	Figure Number
			Min.	Typ.	Max.	Min.	Max.	Max.			
t _{PLH}	Propagation Delay A _Q to Y _H	1.8 ± 0.15	2.0		12.0	2.0	13.0		ns	C _L = 15 pF R _D = 1 MΩ S _I = Open	Figures 1, 3
t _{PLH}	Propagation Delay A _Q to Y _H	2.5 ± 0.2	1.0		7.5	1.0	8.0				
t _{PLH}	Propagation Delay A _Q to Y _H	3.5 ± 0.5	0.6		5.2	0.6	5.5				
t _{PLH}	Propagation Delay A _Q to Y _H	5.0 ± 0.5	0.5		4.5	0.5	4.8		ns	C _L = 50 pF R _D = 500Ω S _I = Open	Figures 1, 3
t _{PLH}	Propagation Delay A _Q to Y _H	3.3 ± 0.3	1.2		5.7	1.2	6.0				
t _{PLH}	Propagation Delay A _Q to Y _H	5.0 ± 0.5	0.8		5.0	0.8	5.3				
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0		1.0		ns	C _L = 50 pF R _D = 500Ω S _I = Open	Figures 1, 3
t _{OSLH}	Output to Output Slew (Note 5)	5.0 ± 0.5			0.8		0.8				
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0		1.0				
t _{PLZ}	Output Enable Time	1.8 ± 0.15	3.0		14.0	3.0	15.0		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	2.5 ± 0.2	1.8		8.5	1.8	9.0				
t _{PLZ}	Output Enable Time	3.5 ± 0.5	1.2		6.2	1.2	6.5				
t _{PLZ}	Output Enable Time	5.5 ± 0.5	0.8		5.5	0.8	5.8		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	1.8 ± 0.15	2.5		12.0	2.5	13.0				
t _{PLZ}	Output Enable Time	2.5 ± 0.2	1.5		8.0	1.5	8.5				
t _{PLZ}	Output Enable Time	3.5 ± 0.5	0.8		5.7	0.8	6.0		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	5.0 ± 0.5	0.3		4.7	0.3	5.0				
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0		13.0				

New

Symbol	Parameter	V _{CC} (V)	Conditions	T _A = +25°C			T _A = -40 to +85°C			Units
				Min.	Typ.	Max.	Min.	Max.	Max.	
V _{th}	Positive Threshold Voltage	1.65				1.40		1.40	1.40	V
		1.80				1.50		1.50	1.50	
		2.30				1.80		1.80	1.80	
		3.00				2.20		2.20	2.20	
		4.50				3.10		3.10	3.20	
		5.50				3.60		3.60	3.70	
V _{th}	Negative Threshold Voltage	1.65		0.20	0.50		0.20		0.30	V
		1.80		0.25	0.56		0.25		0.35	
		2.30		0.40	0.75		0.40		0.50	
		3.00		0.60	0.96		0.60		0.70	
		4.50		1.00	1.42		1.00		1.10	
		5.50		1.20	1.66		1.20		1.40	

Symbol	Parameter	V _{CC} (V)	T _A = +25°C			T _A = -40 to +85°C			Units	Conditions	Figure Number
			Min.	Typ.	Max.	Min.	Max.	Max.			
t _{PLH}	Propagation Delay A _Q to Y _H	1.8 ± 0.15			12.0		13.0		ns	C _L = 15 pF R _D = 1 MΩ S _I = Open	Figures 1, 3
t _{PLH}	Propagation Delay A _Q to Y _H	2.5 ± 0.2			7.5		8.0				
t _{PLH}	Propagation Delay A _Q to Y _H	3.5 ± 0.5			5.2		5.5				
t _{PLH}	Propagation Delay A _Q to Y _H	5.0 ± 0.5			4.5		4.8		ns	C _L = 50 pF R _D = 500Ω S _I = Open	Figures 1, 3
t _{PLH}	Propagation Delay A _Q to Y _H	3.3 ± 0.3			5.7		6.0				
t _{PLH}	Propagation Delay A _Q to Y _H	5.0 ± 0.5			5.0		5.3				
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0		1.0		ns	C _L = 50 pF R _D = 500Ω S _I = Open	Figures 1, 3
t _{OSLH}	Output to Output Slew (Note 5)	5.0 ± 0.5			0.8		0.8				
t _{OSLH}	Output to Output Slew (Note 5)	3.3 ± 0.3			1.0		1.0				
t _{PLZ}	Output Enable Time	1.8 ± 0.15			14.0		15.0		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	2.5 ± 0.2			8.5		9.0				
t _{PLZ}	Output Enable Time	3.5 ± 0.5			6.2		6.5				
t _{PLZ}	Output Enable Time	5.5 ± 0.5			5.5		5.8		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0		13.0				
t _{PLZ}	Output Enable Time	2.5 ± 0.2			8.0		8.5				
t _{PLZ}	Output Enable Time	3.5 ± 0.5			5.7		6.0		ns	C _L = 50 pF R _D = 500 Ω S _I = GND for t _{PLZ} S _I = V _I for t _{PLZ} V _I = 2 × V _{CC}	Figures 1, 3
t _{PLZ}	Output Enable Time	5.0 ± 0.5			4.7		5.0				
t _{PLZ}	Output Enable Time	1.8 ± 0.15			12.0		13.0				

信頼性データの要約:

デバイス名 : NLV27WZ14DFT2G

RMS: L51921

パッケージ: SC88

テスト	仕様	条件	間隔	結果
HTOL	JESD22-A108	Ta=125°C, 100 % max rated Vcc	1008 hrs	0/288
HTSL	JESD22-A103	Ta= 150°C	1008 hrs	0/252
TC	JESD22-A104	Ta= -65°C to +150°C	500 cyc	0/297
HAST	JESD22-A110	130°C, 85% RH, 18.8psig, bias	96 hrs	0/273
uHAST	JESD22-A118	130°C, 85% RH, 18.8psig, unbiased	96 hrs	0/234
PC	J-STD-020 JESD-A113	MSL 1 @260 °C		0/804
RSH	JESD22- B106	Ta = 265C, 10 sec		0/30

電気特性の要約:

電気的特性の提出は要求に基づきます。



影響を受ける部品の一覧:

注: 部品一覧には標準部品番号 (既製品) のみが記載されています。本 PCN の影響を受けるカスタム部品番号は、PCN メールで提供される顧客個別の付録、または PCN カスタマイズポータルに記載されています。

部品番号	認定試験用ピークル
NC7SZ10P6X	NLV27WZ14DFT2G
NC7SZ11P6X	NLV27WZ14DFT2G
NC7SZ157P6X	NLV27WZ14DFT2G
NC7SZ175P6X	NLV27WZ14DFT2G
NC7SZ18P6X	NLV27WZ14DFT2G
NC7SZ19P6X	NLV27WZ14DFT2G
NC7SZ27P6X	NLV27WZ14DFT2G
NC7SZ332P6X	NLV27WZ14DFT2G
NC7SZ373P6X	NLV27WZ14DFT2G
NC7SZ374P6X	NLV27WZ14DFT2G
NC7SZ386P6X	NLV27WZ14DFT2G
NC7SZ57P6X	NLV27WZ14DFT2G
NC7SZ58P6X	NLV27WZ14DFT2G
NC7WZ04P6X	NLV27WZ14DFT2G
NC7WZ07P6X	NLV27WZ14DFT2G
NC7WZ14EP6X	NLV27WZ14DFT2G
NC7WZ14P6X	NLV27WZ14DFT2G
NC7WZ16P6X	NLV27WZ14DFT2G
NC7WZ17P6X	NLV27WZ14DFT2G
NC7WZU04P6X	NLV27WZ14DFT2G

Appendix A: Changed Products

Product	Customer Part Number	Qualification Vehicle
NC7SZ10P6X		NLV27WZ14DFT2G
NC7SZ11P6X		NLV27WZ14DFT2G
NC7SZ157P6X		NLV27WZ14DFT2G
NC7SZ175P6X		NLV27WZ14DFT2G
NC7SZ18P6X		NLV27WZ14DFT2G
NC7SZ19P6X		NLV27WZ14DFT2G
NC7SZ27P6X		NLV27WZ14DFT2G
NC7SZ332P6X		NLV27WZ14DFT2G
NC7SZ373P6X		NLV27WZ14DFT2G
NC7SZ374P6X		NLV27WZ14DFT2G
NC7SZ386P6X		NLV27WZ14DFT2G
NC7SZ57P6X		NLV27WZ14DFT2G
NC7SZ58P6X		NLV27WZ14DFT2G
NC7WZ04P6X		NLV27WZ14DFT2G
NC7WZ07P6X		NLV27WZ14DFT2G
NC7WZ14EP6X		NLV27WZ14DFT2G
NC7WZ14P6X		NLV27WZ14DFT2G
NC7WZ16P6X		NLV27WZ14DFT2G
NC7WZ17P6X		NLV27WZ14DFT2G
NC7WZU04P6X		NLV27WZ14DFT2G