

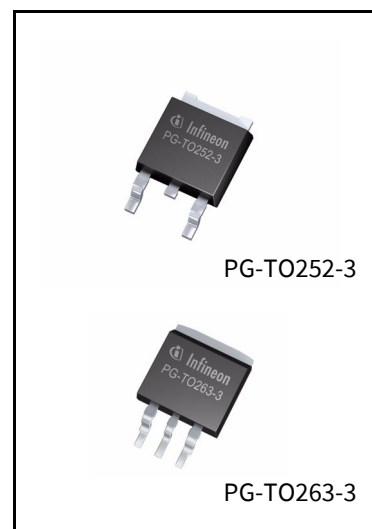
OPTIREG™ linear TLE4274

Low dropout voltage regulator



Features

- Output voltage 5 V or 10 V
- Output voltage tolerance $\leq \pm 4\%$
- Current capability 400 mA
- Low dropout voltage
- Very low current consumption
- Short-circuit proof
- Reverse polarity proof
- Suitable for use in automotive electronics
- Green Product (RoHS-compliant)



Potential applications

General automotive applications.

Product validation

Qualified for automotive applications. Product validation according to AEC-Q100.

Description

The OPTIREG™ linear TLE4274 is a low drop voltage regulator available in a PG-T0252-3 and PG-T0263-3 package. The IC regulates an input voltage up to 40 V to $V_{Qrated} = 5.0\text{ V (V50)}$ and 10 V (V10). The maximum output current is 400 mA. The IC is short-circuit proof and incorporates temperature protection that disables the IC at overtemperature. A 3.3 V and 2.5 V version is also available. For information about the low output voltage types please refer to the datasheet TLE4274 / 3.3 V; 2.5 V.

Type	Package	Marking
TLE4274DV50	PG-T0252-3	4274V50
TLE4274GV10	PG-T0263-3	4274V10

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Block diagram

1 Block diagram

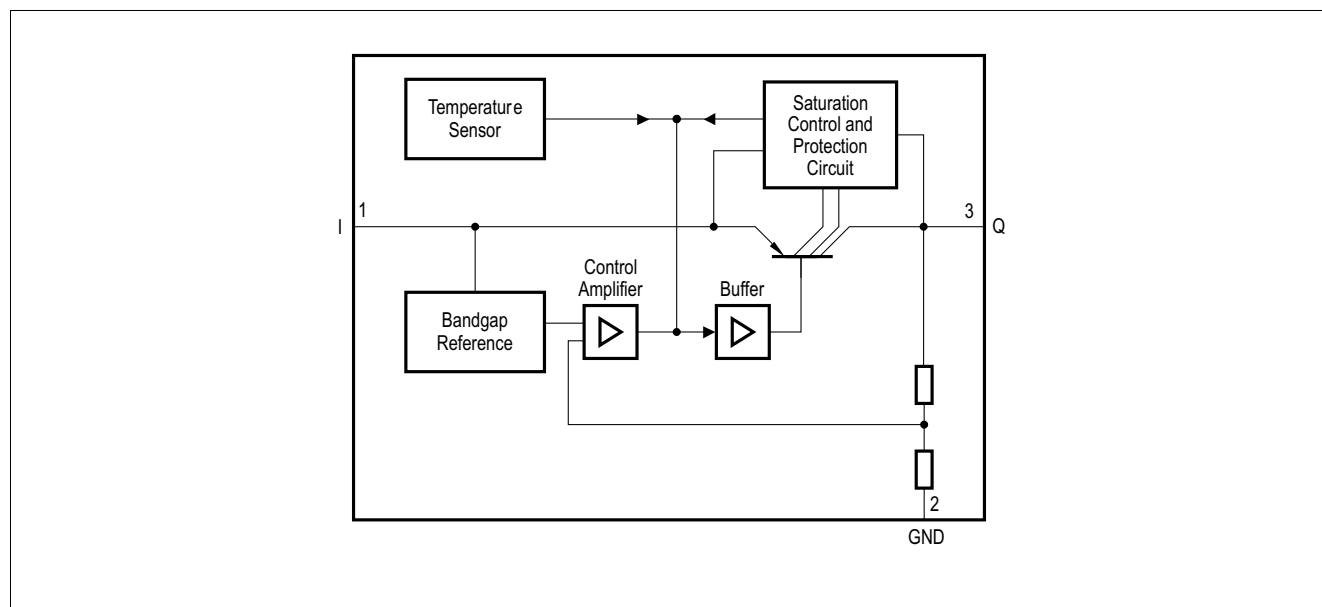


Figure 1 Block diagram

Pin configuration

2 Pin configuration

2.1 Pin assignment

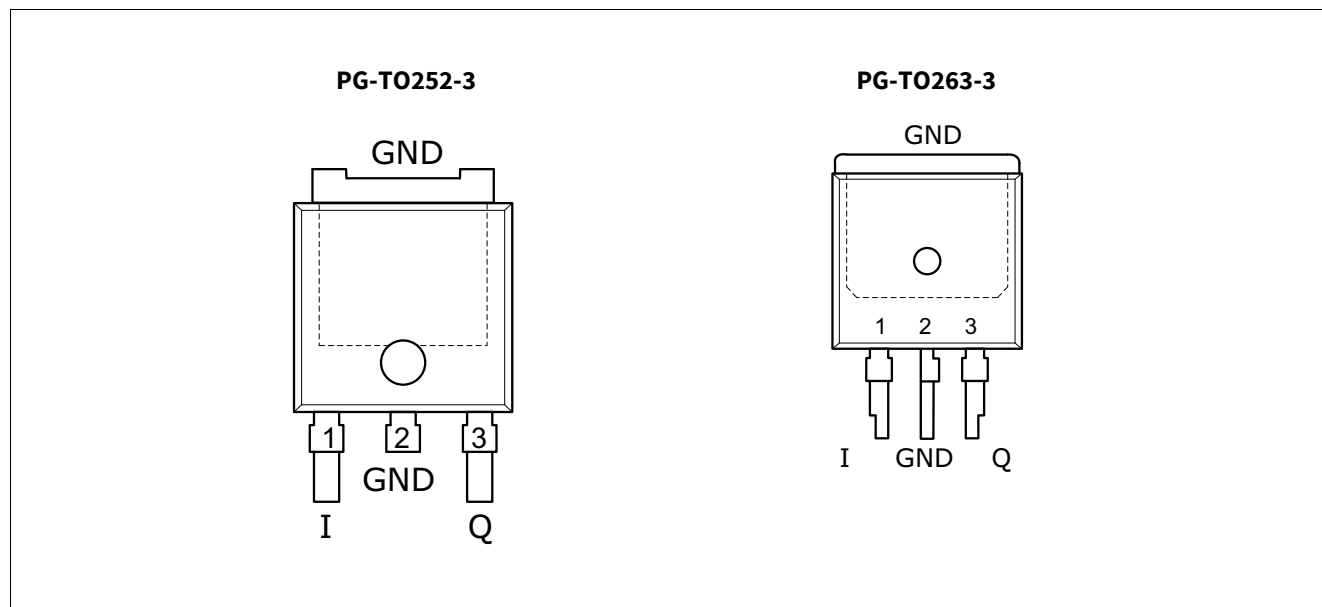


Figure 2 Pin configuration

2.2 Pin definitions and functions

Table 1 Pin definitions and functions

Pin	Symbol	Function
1	I	Input Block to ground directly at the IC with a ceramic capacitor
2	GND	Ground
3	Q	Output Block to ground with a capacitor $\geq 22 \mu\text{F}$, $\text{ESR} \leq 3 \Omega$
TAB	–	TAB Connect to heatsink and GND to improve thermal performance

3 General product characteristics

3.1 Absolute maximum ratings

Table 2 Absolute maximum ratings

$T_j = -40^{\circ}\text{C}$ to 150°C

Parameter	Symbol	Values			Unit	Note or Test Condition	Number
		Min.	Typ.	Max.			
Input							
Voltage	V_I	-42	–	45	V	–	P_3.1.1
Current	I_I	–	–	–	–	Internally limited	P_3.1.2
Output							
Voltage	V_Q	-1.0	–	40	V	–	P_3.1.3
Current	I_Q	–	–	–	–	Internally limited	P_3.1.4
Ground							
Current	I_{GND}	–	–	100	mA	–	P_3.1.5
Temperature							
Junction temperature	T_j	–	–	150	°C	–	P_3.1.6
Storage temperature	T_{stg}	-50	–	150	°C	–	P_3.1.7

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

3.2 Functional range

Table 3 Operating range

Parameter	Symbol	Values			Unit	Note or Test Condition	Number
		Min.	Typ.	Max.			
Input voltage, V50	V_I	5.5	–	40	V	–	P_3.2.1
Input voltage, V10	V_I	10.5	–	40	V	–	P_3.2.3
Junction temperature	T_j	-40	–	150	$^{\circ}\text{C}$	–	P_3.2.4
Thermal resistance							
Junction ambient	R_{thja}	–	–	78	K/W	¹⁾ PG-TO252-3	P_3.2.6
Junction ambient	R_{thja}	–	–	52	K/W	¹⁾ PG-TO263-3	P_3.2.7
Junction case	R_{thjc}	–	–	4	K/W	–	P_3.2.8

1) Worst case; regarding peak temperature, zero airflow mounted on PCB $80 \times 80 \times 1.5 \text{ mm}^3$, 300 mm^2 heat sink area.

Circuit description

4 Circuit description

4.1 Dimensioning information on external components

The input capacitor C_I is necessary for compensating line influences. Using a resistor of approximately $1\ \Omega$ in series with C_I , the oscillating of input inductivity and input capacitance can be damped. The output capacitor C_Q is necessary for the stability of the regulation circuit. Stability is specified at values $C_Q \geq 22\ \mu\text{F}$ and an ESR of $\leq 3\ \Omega$ within the operating temperature range.

4.2 Circuit description

The control amplifier compares a reference voltage to a voltage that is proportional to the output voltage and drives the base of the series transistor via a buffer. Saturation control as a function of the load current prevents any oversaturation of the power element. The IC also includes a number of internal circuits for protection against:

- Overload
- Overtemperature
- Reverse polarity

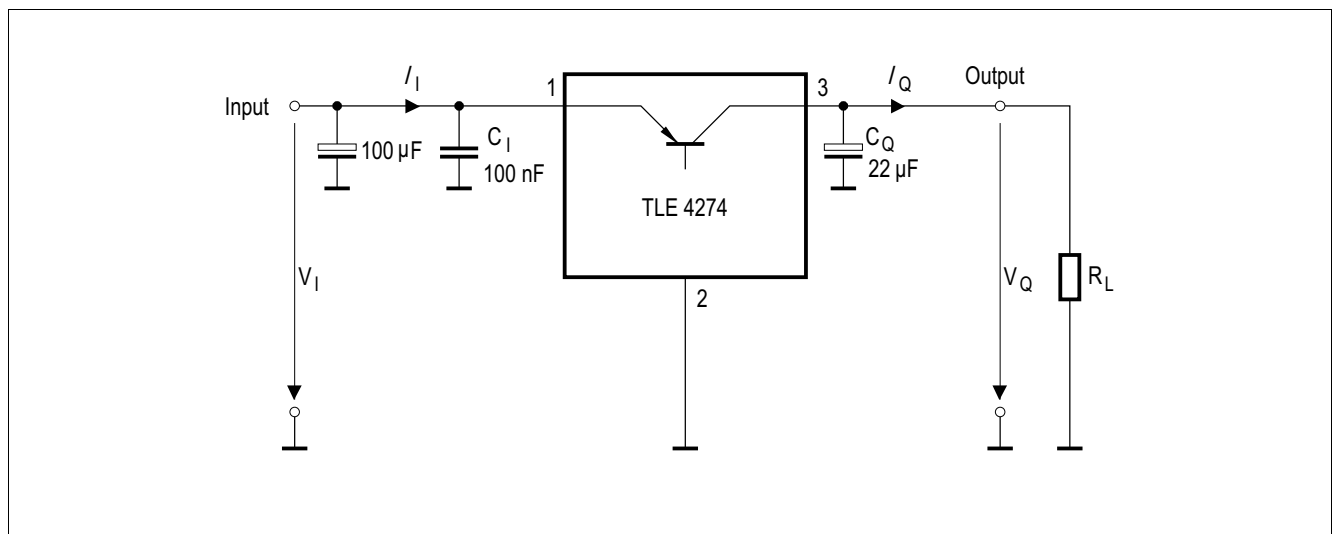


Figure 3 Measuring circuit

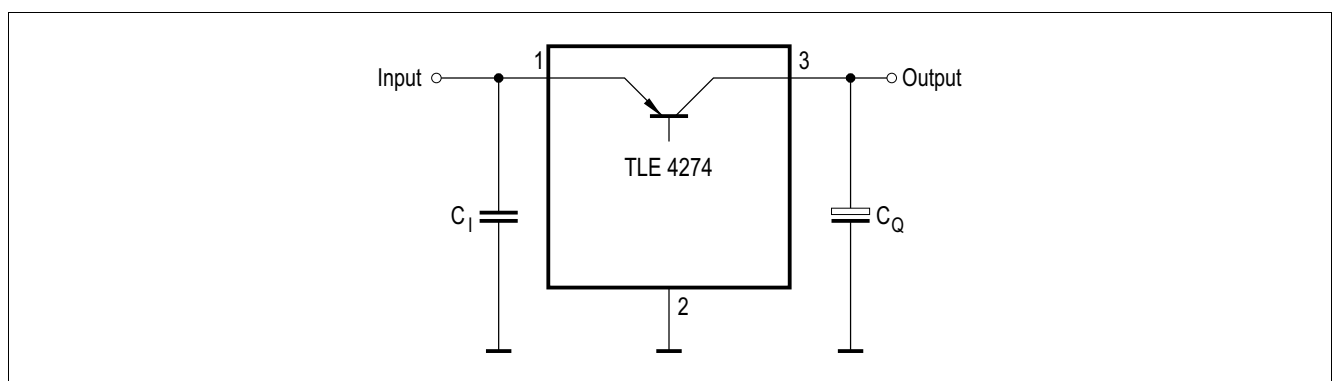


Figure 4 Application circuit

Electrical characteristics

5 Electrical characteristics

Table 4 Electrical characteristics

$V_I = 13.5 \text{ V}$; $T_j = -40^\circ\text{C}$ to 150°C (unless otherwise specified)

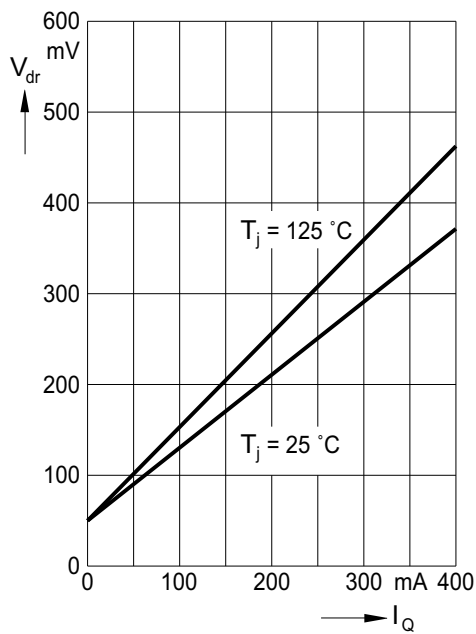
Parameter	Symbol	Values			Unit	Note or Test Condition	Number
		Min.	Typ.	Max.			
Output voltage V50-version	V_Q	4.8	5	5.2	V	$5 \text{ mA} < I_Q < 400 \text{ mA}$ $6 \text{ V} < V_I < 28 \text{ V}$	P_5.1.1
Output voltage V50-version	V_Q	4.8	5	5.2	V	$5 \text{ mA} < I_Q < 200 \text{ mA}$ $6 \text{ V} < V_I < 40 \text{ V}$	P_5.1.2
Output voltage V10-version	V_Q	9.6	10	10.4	V	$5 \text{ mA} < I_Q < 400 \text{ mA}$ $11 \text{ V} < V_I < 28 \text{ V}$	P_5.1.5
Output voltage V10-version	V_Q	9.6	10	10.4	V	$5 \text{ mA} < I_Q < 200 \text{ mA}$ $11 \text{ V} < V_I < 40 \text{ V}$	P_5.1.6
Output current limitation	I_Q	400	600	–	mA	¹⁾	P_5.1.7
Current consumption $I_q = I_I - I_Q$	I_q	–	100	220	mA	$I_Q = 1 \text{ mA}$	P_5.1.8
		–	8	15	mA	$I_Q = 250 \text{ mA}$	P_5.1.9
		–	20	30	mA	$I_Q = 400 \text{ mA}$	P_5.1.10
Drop voltage	V_{dr}	–	250	500	mV	$I_Q = 250 \text{ mA}$ $V_{dr} = V_I - V_Q$	P_5.1.11
Load regulation	ΔV_Q	–	20	50	mV	$I_Q = 5 \text{ mA}$ to 400 mA	P_5.1.12
Line regulation	ΔV_Q	–	10	25	mV	$\Delta V_I = 12 \text{ V}$ to 32 V $I_Q = 5 \text{ mA}$	P_5.1.13
Power supply ripple rejection	$PSRR$	–	60	–	dB	$f_r = 100 \text{ Hz}$ $V_r = 0.5 \text{ V}_{pp}$	P_5.1.14
Temperature output voltage drift	$\Delta V_Q / \Delta T$	–	0.5	–	mV/K	–	P_5.1.15

1) Measured when the output voltage V_Q has dropped 100 mV from the nominal value obtained at $V_I = 13.5 \text{ V}$.

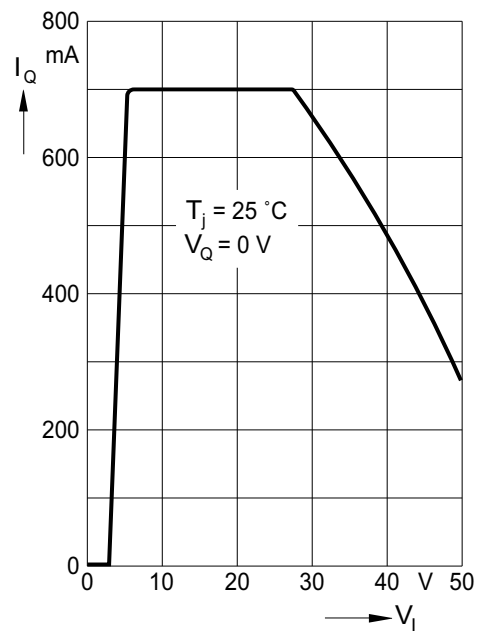
6 Typical performance characteristics

6.1 Typical performance characteristics (V50 and V10)

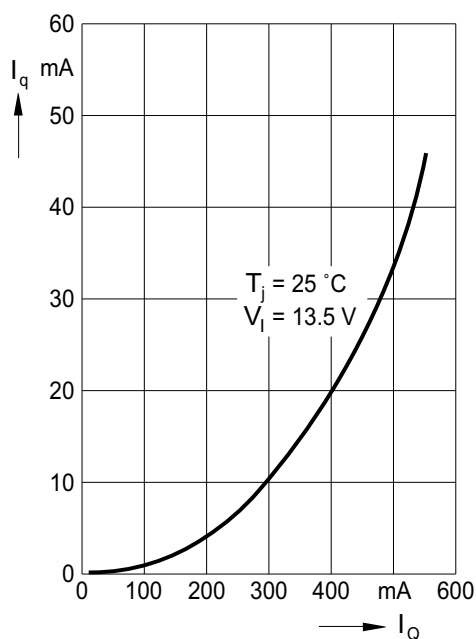
Drop voltage V_{dr} versus output current I_Q



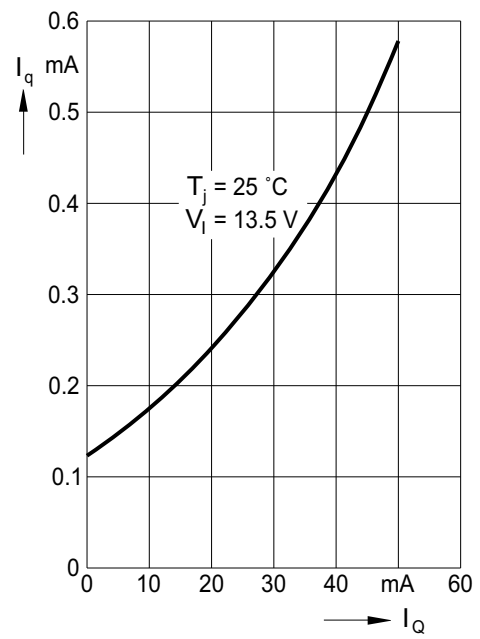
Output current I_Q versus input voltage V_I



Current consumption I_q versus output current I_Q (high load)



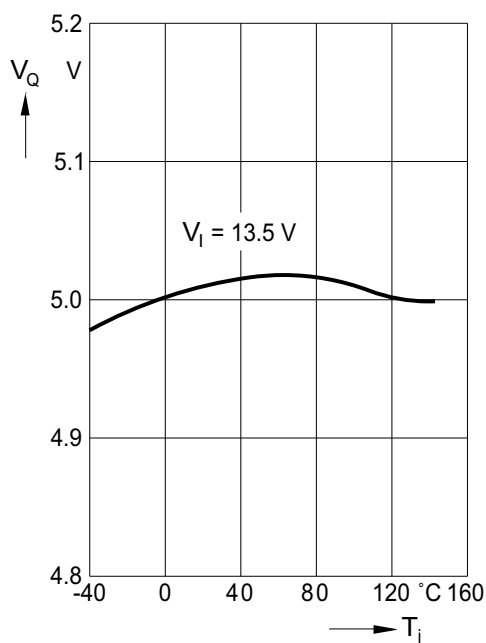
Current consumption I_q versus output current I_Q (low load)



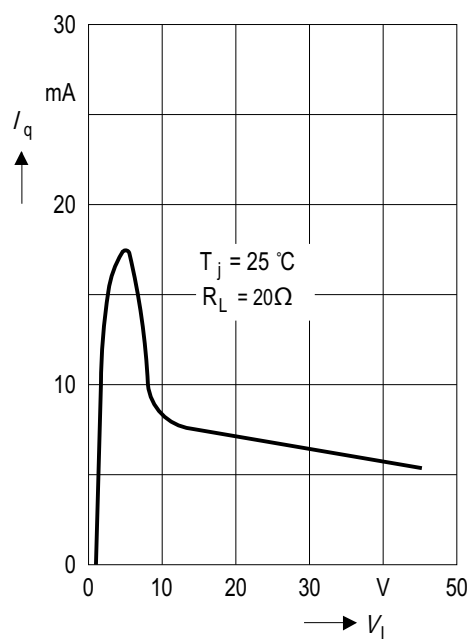
Typical performance characteristics

6.2 Typical performance characteristics (V50)

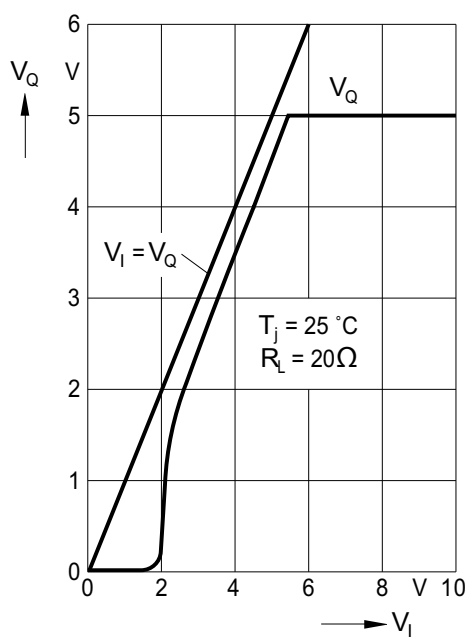
**Output voltage V_Q versus
junction temperature T_j**



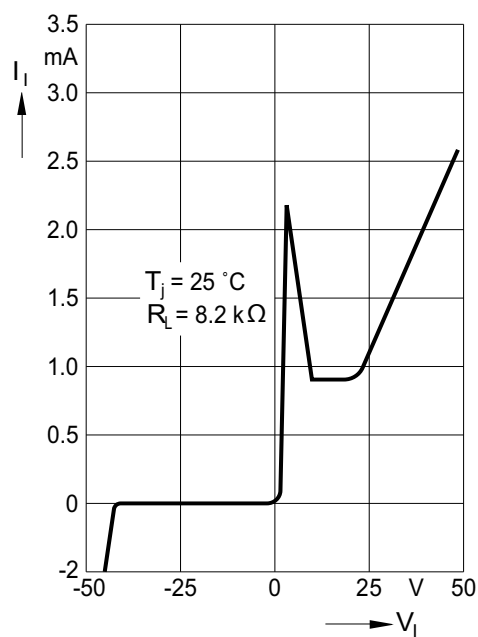
**Current consumption I_q versus
input voltage V_I**



**Output voltage V_Q versus
Input voltage V_I**



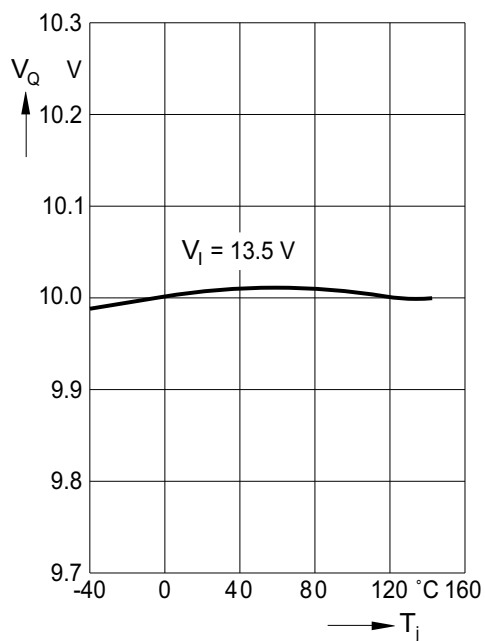
**Input current I_I versus
input voltage V_I**



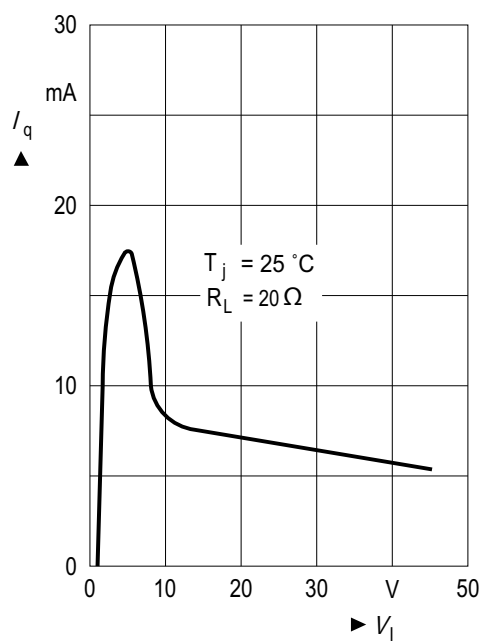
Typical performance characteristics

6.3 Typical performance characteristics (V10)

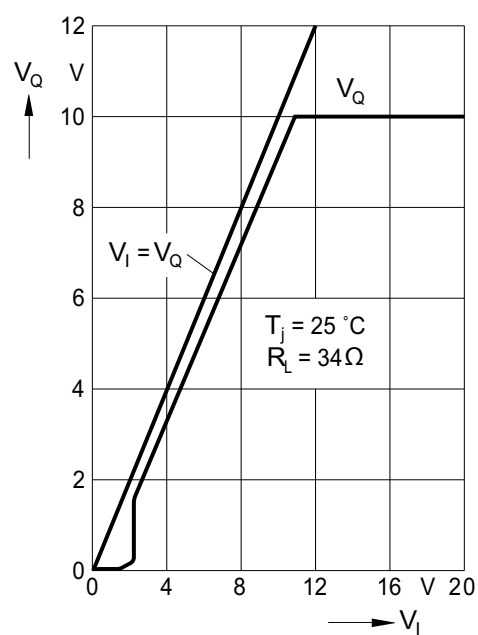
**Output voltage V_Q versus
junction temperature T_j**



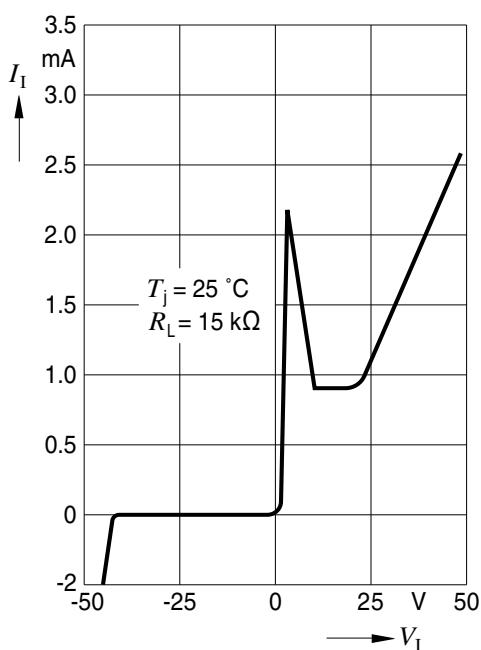
**Current consumption I_q versus
input voltage V_I**



**Output voltage V_Q versus
input voltage V_I**



**Input current I_I versus
input voltage V_I**



Package information

7 Package information

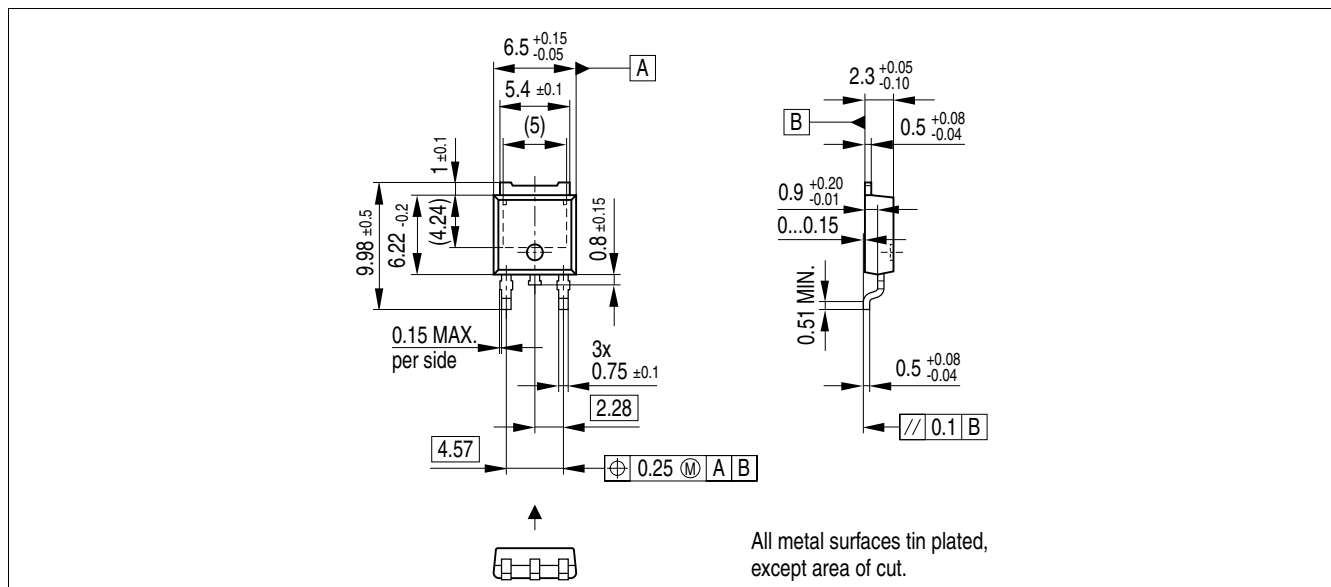


Figure 5 PG-T0252-3 (Plastic transistor, single outline)¹⁾

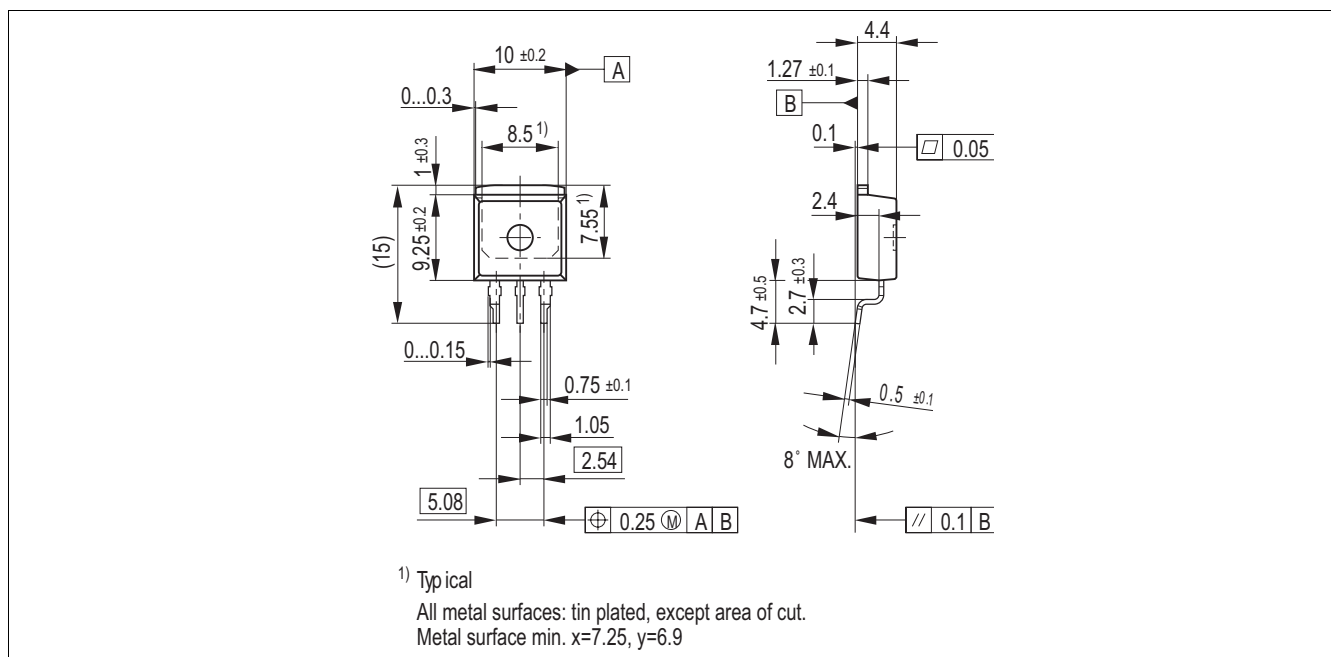


Figure 6 PG-T0263-3 (Plastic transistor, single outline)¹⁾

Green Product (RoHS-compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green Products are RoHS-compliant (Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

Further information on packages

<https://www.infineon.com/packages>

1) Dimensions in mm

Revision history

8 Revision history

Revision	Date	Changes
1.71	2025-02-06	• Editorial changes and template update • Removed discontinued product variants
1.7	2011-01-20	Updated Infineon logo Added “AEC” and “Green” logo Added “Green Product” and “AEC qualified” to the feature list Updated package names to “PG-xxx” Removed leadframe variant “P-TO-252-1” Added “Green Product” remark Disclaimer update Updated Package Outlines (added TAB potential)
1.5		Previous version

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