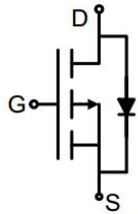
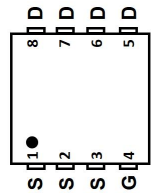
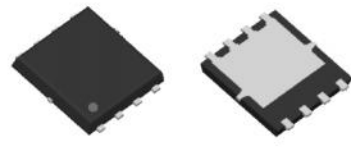


P-Channel Enhancement Mode Power MOSFET

<p>Description</p> <p>The GT065P06D5 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} -60V ● I_D (at $V_{GS} = -10V$) -103A ● $R_{DS(ON)}$ (at $V_{GS} = -10V$) < 7mΩ ● $R_{DS(ON)}$ (at $V_{GS} = -4.5V$) < 9mΩ ● 100% Avalanche Tested ● RoHS Compliant <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters 	 <p>Schematic diagram</p>  <p>pin assignment</p>  <p>DFN5X6-8L</p>
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Ordering Information

Device	Package	Marking	Packaging
GT065P06D5	DFN5X6-8L	GT065P06	5000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ C$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-60	V
Continuous Drain Current	I_D	$T_C = 25^\circ C$	-103
		$T_C = 100^\circ C$	-65
Pulsed Drain Current (note1)	I_{DM}	-412	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	178	W
Single pulse avalanche energy (note2)	E_{AS}	272	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ C$

Thermal Resistance

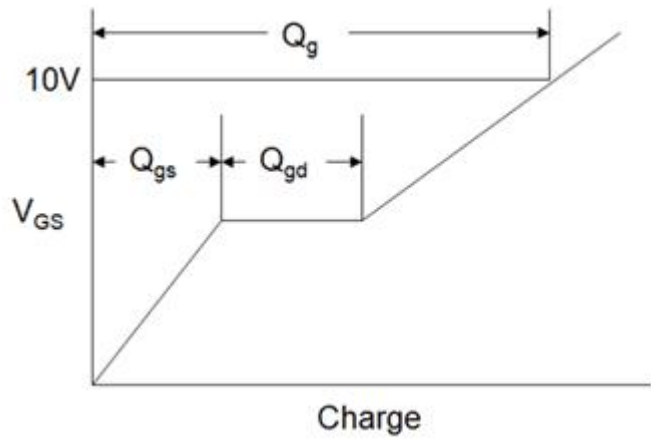
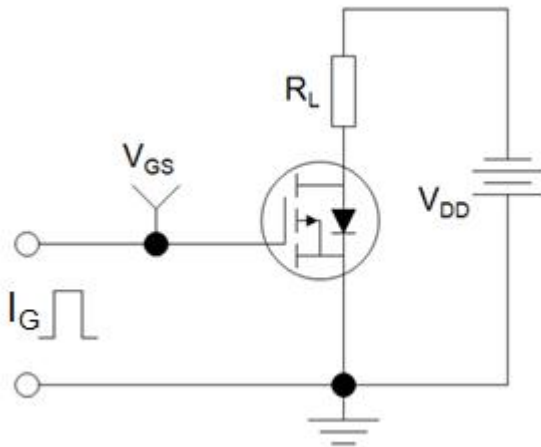
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	$^\circ C/W$
Thermal Resistance, Junction-to-Case	R_{thJC}	0.7	$^\circ C/W$

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-60	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -60V, V_{GS} = 0V$	--	--	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	-1.8	-2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -20A$	--	5.9	7.0	m Ω
		$V_{GS} = -4.5V, I_D = -15A$	--	7.4	9.0	
Forward Transconductance	g_{FS}	$V_{DS} = -5V, I_D = -20A$	--	48	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = -30V,$ $f = 1.0\text{MHz}$	--	5985	--	pF
Output Capacitance	C_{oss}		--	907	--	
Reverse Transfer Capacitance	C_{rss}		--	42	--	
Total Gate Charge	Q_g	$V_{DD} = -30V,$ $I_D = -20A,$ $V_{GS} = -10V$	--	99	--	nC
Gate-Source Charge	Q_{gs}		--	13	--	
Gate-Drain Charge	Q_{gd}		--	21	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -30V,$ $I_D = -20A,$ $R_G = 3\Omega$	--	20	--	ns
Turn-on Rise Time	t_r		--	18	--	
Turn-off Delay Time	$t_{d(off)}$		--	55	--	
Turn-off Fall Time	t_f		--	35	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	-103	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = -20A, V_{GS} = 0V$	--	--	-1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = -20A, V_{GS} = 0V$ $di/dt = -100A/\mu s$	--	71	--	nC
Reverse Recovery Time	T_{rr}		--	49	--	ns

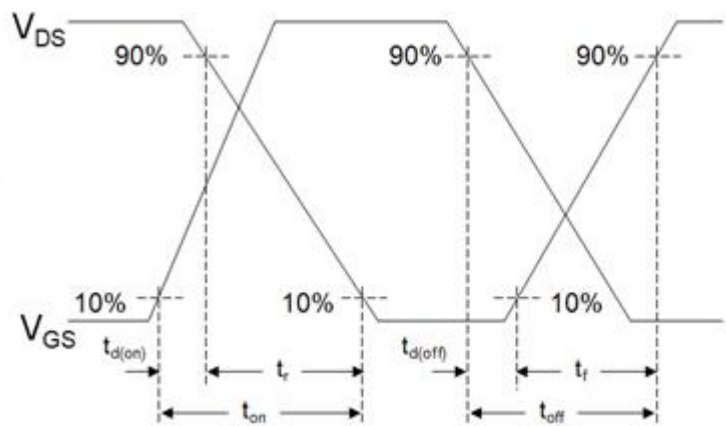
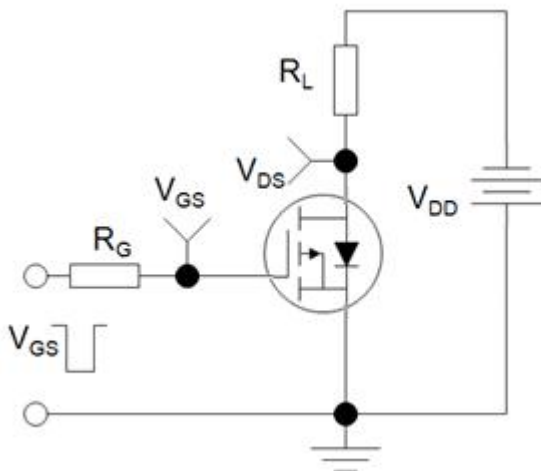
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition : $T_J = 25^\circ\text{C}, V_{DD} = -50V, V_{GS} = -10V, L = 0.5\text{mH}, R_G = 25\Omega$
The table shows the minimum avalanche energy, which is 702mJ when the device is tested until failure
3. Identical low side and high side switch with identical R_G

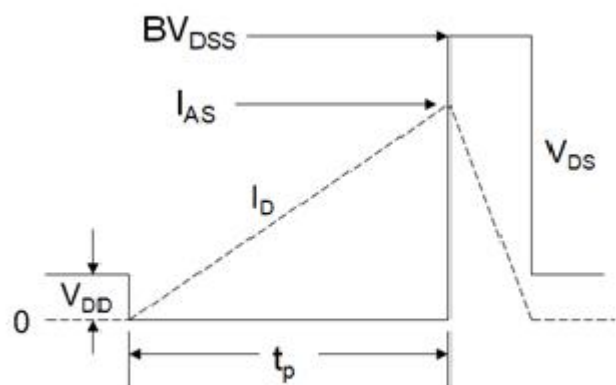
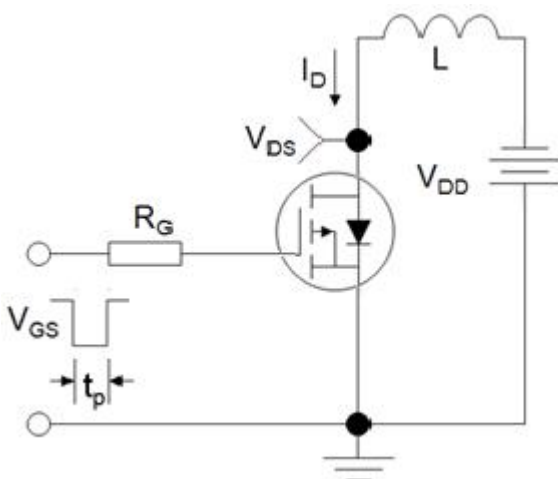
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

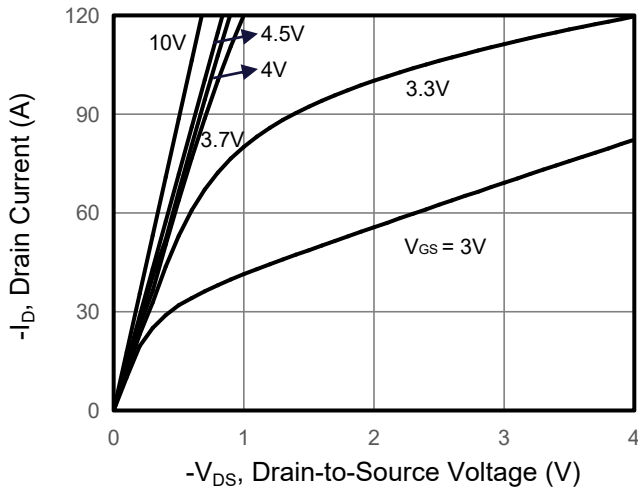


Figure 2. Transfer Characteristics

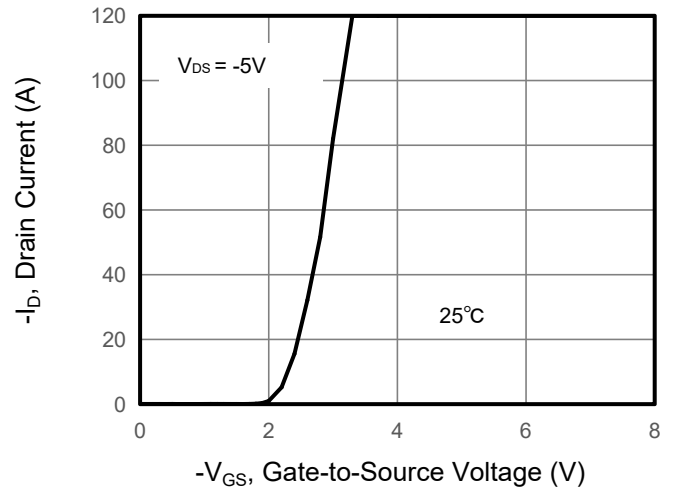


Figure 3. Drain Source On Resistance

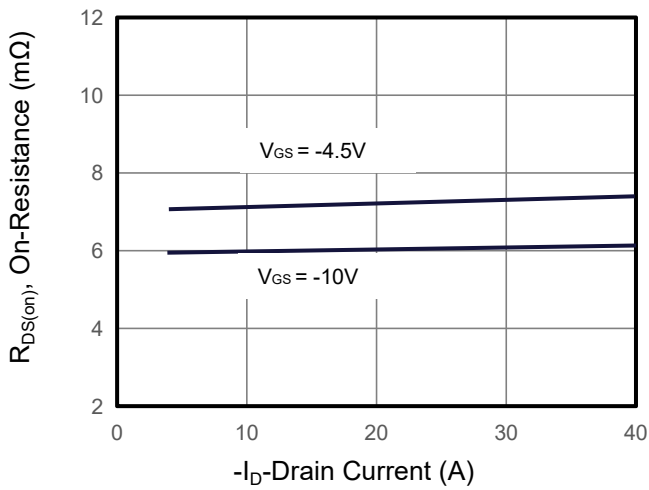


Figure 4. Gate Charge

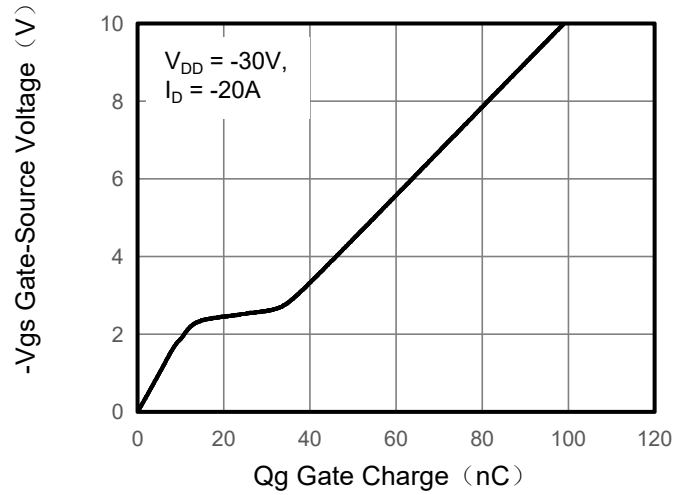


Figure 5. Capacitance

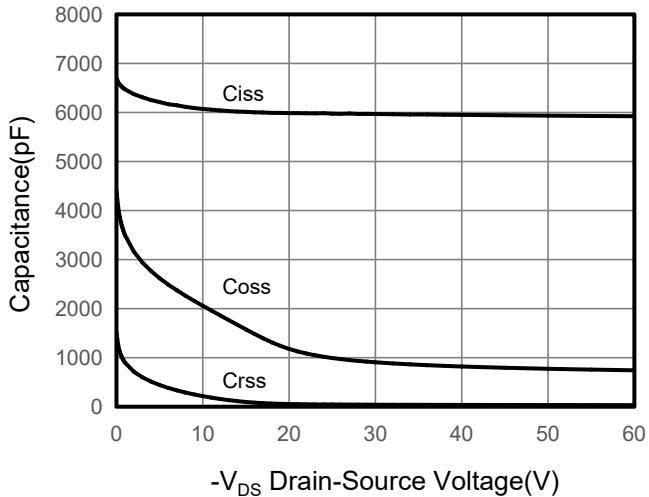
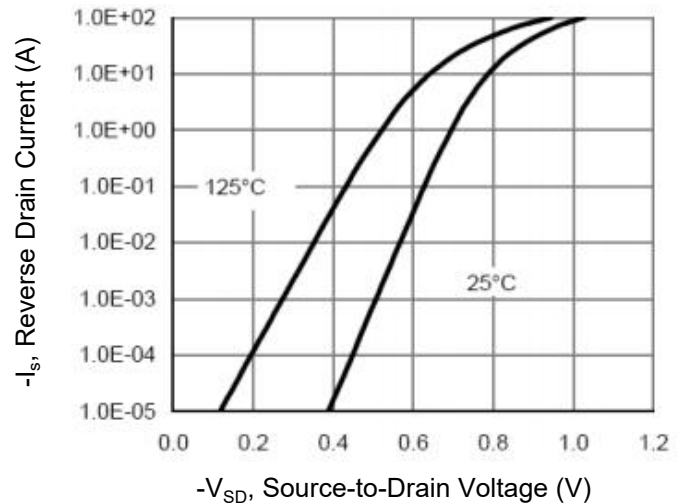


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

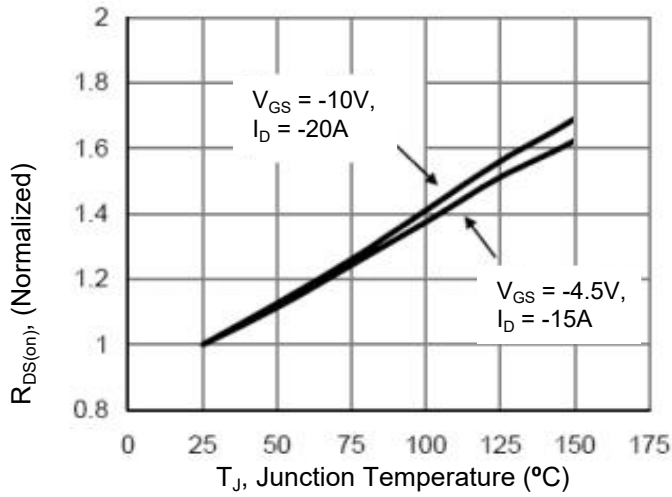


Figure 8. Safe Operation Area

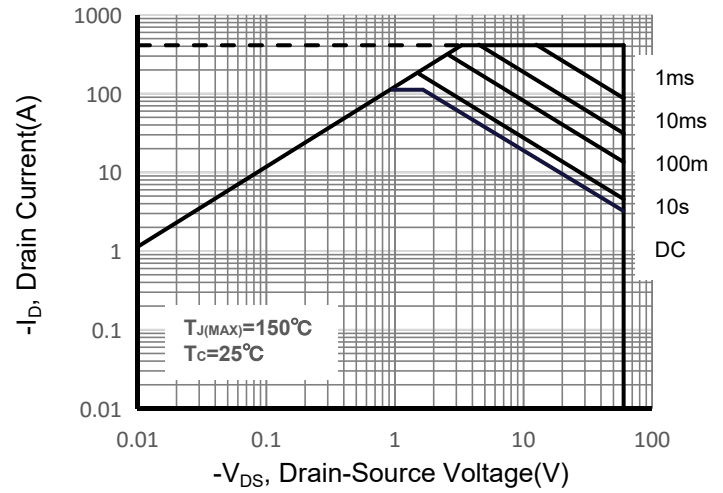


Figure 9. Maximum Continuous Drain Current vs Case Temperature

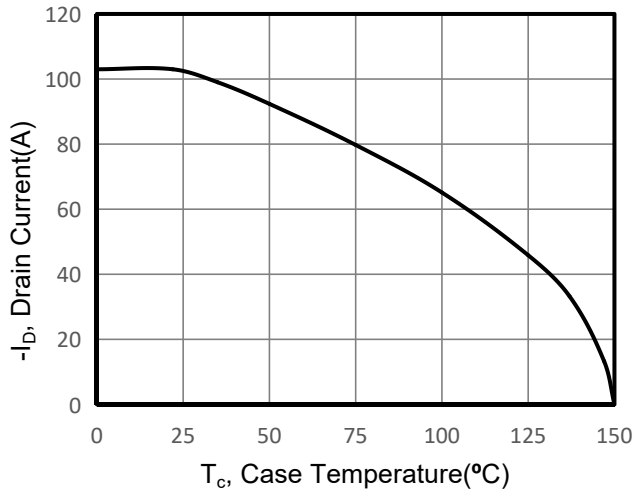
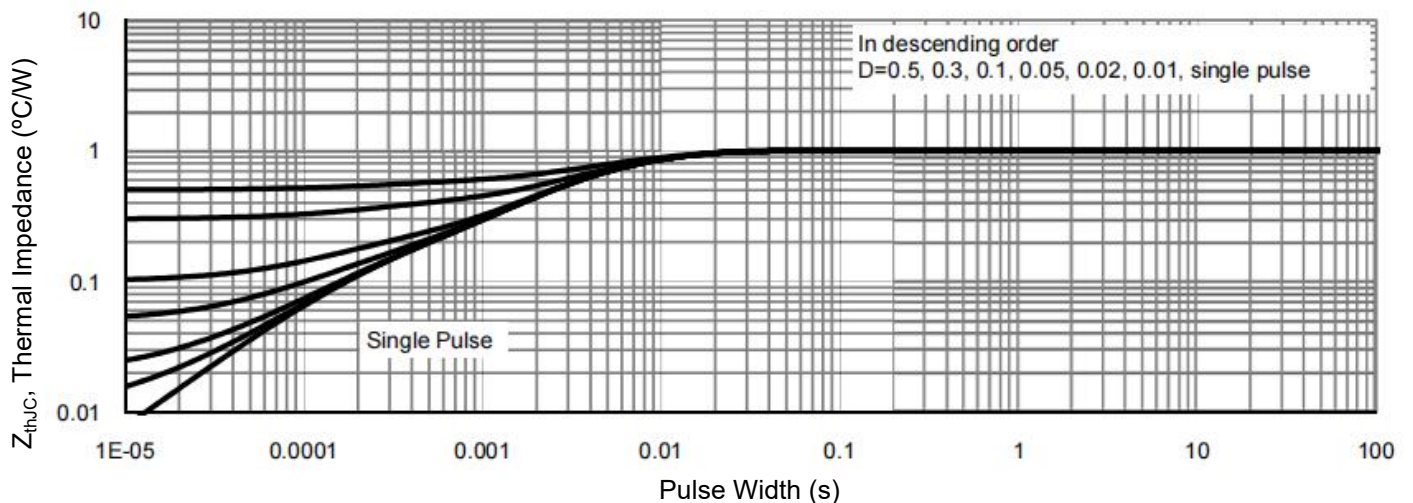
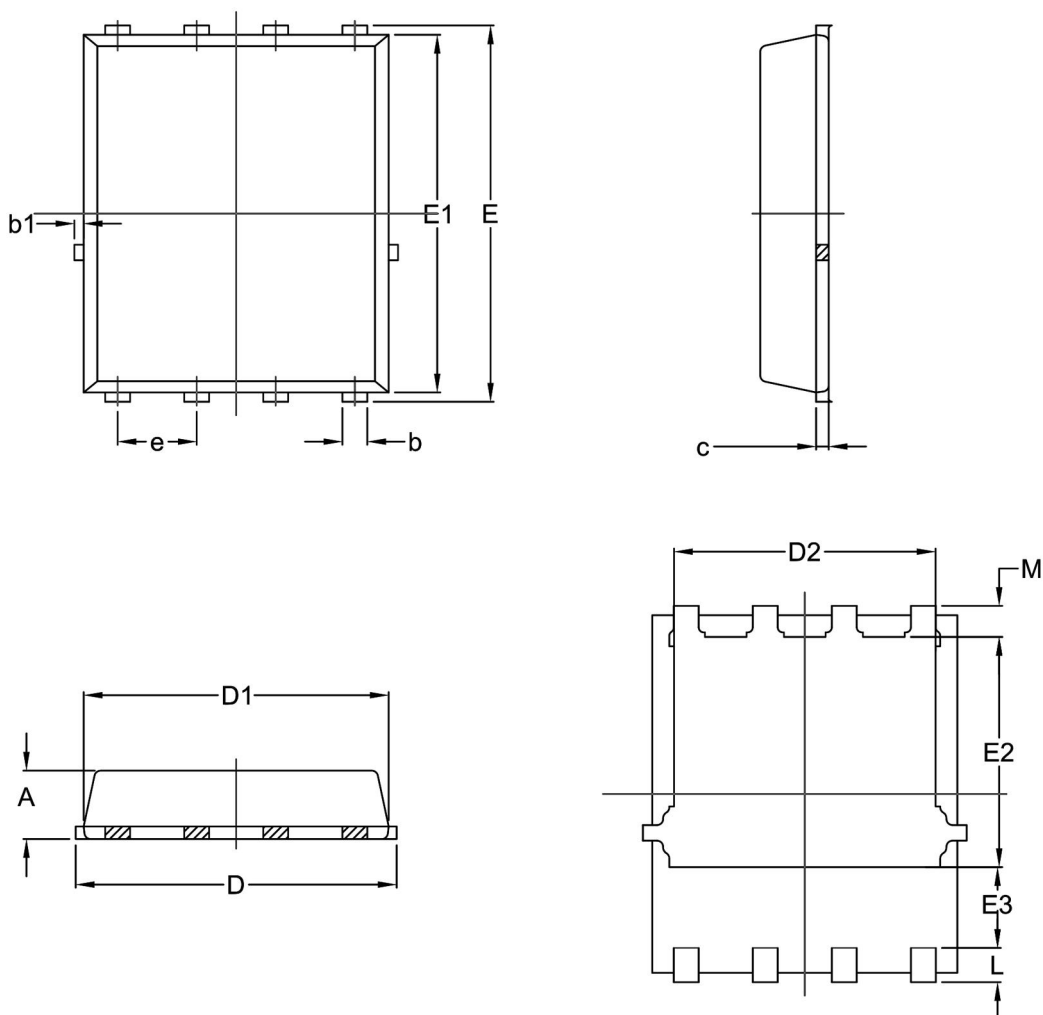


Figure 10. Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	1.00	1.10	1.20
b	0.30	0.40	0.50
b1	0.02	0.15	0.22
c	0.15	0.20	0.35
D	4.95	5.15	5.35
D1	4.80	4.90	5.00
D2	4.00	4.20	4.40
E	5.95	6.05	6.25
E1	5.65	5.75	5.85
E2	3.50	3.70	3.90
E3	1.10	/	/
e	1.27		
L	0.40	0.55	0.70
M	0.35	0.50	0.65