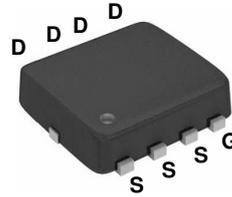
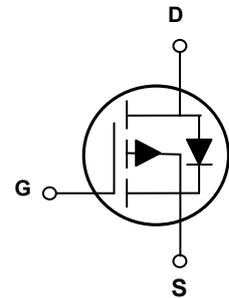


Main Product Characteristics

BV_{DSS}	-40V
$R_{DS(ON)}$	13m Ω (Max.)
I_D	-30A



PPAK3x3



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFN4013 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	$V_{(BR)DSS}$	-40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_C=25^\circ\text{C}$), $V_{GS}=10\text{V}^1$	I_D	-30	A
Drain Current-Continuous ($T_C=100^\circ\text{C}$), $V_{GS}=10\text{V}^1$		-21	
Drain Current-Pulsed ²	I_{DM}	-120	A
Pulsed Source Current (Body Diode) ²	I_{SM}	-120	A
Maximum Power Dissipation ($T_C=25^\circ\text{C}$) ³	P_D	25	W
Single Pulse Avalanche Energy ($L=0.3\text{mH}$)	E_{AS}	272	mJ
Single Pulse Avalanche Current ($L=0.3\text{mH}$)	I_{AS}	32	A
Junction-to-Ambient ($t \leq 10\text{s}$) ⁴	$R_{\theta JA}$	62	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	5	$^\circ\text{C/W}$
Operating Junction Temperature Range	T_J	-55 To +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 To +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-40	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$	-	-	-1	μA
		$V_{DS}=-40V, V_{GS}=0V, T_J=125^\circ C$	-	-	-50	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-10A$	-	10	13	m Ω
		$V_{GS}=-4.5V, I_D=-8A$	-	13.5	18	m Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1	-1.6	-3	V
Dynamic and Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=-20V, I_D=-20A, V_{GS}=-10V$	-	60	-	nC
Gate-Source Charge	Q_{gs}		-	8.8	-	
Gate-to-Drain Charge	Q_{gd}		-	12.5	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-20V, R_G=3\Omega, V_{GS}=-10V, R_L=16\Omega$	-	18	-	nS
Rise Time	t_r		-	4.8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	88	-	
Fall Time	t_f		-	27	-	
Input Capacitance	C_{iss}	$V_{DS}=-25V, V_{GS}=0V, F=1MHz$	-	3242	-	pF
Output Capacitance	C_{oss}		-	221	-	
Reverse Transfer Capacitance	C_{rss}		-	206	-	
Gate Resitance	R_g	$F=1MHz$	-	4.3	-	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Maximum Body-Diode Continuous Current	I_S	MOSFET symbol showing the integral reverse p-n junction diode.	-	-30	-	A
Maximum Body-Diode Pulse Current	I_{SM}		-	-120	-	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=-10A, T_J=25^\circ C$	-	-0.95	-1.2	V
Reverse Recovery Time	T_{rr}	$I_F=-10A, di/dt=100A/\mu s, T_J=25^\circ C$	-	17.5	-	nS
Reverse Recovery Charge	Q_{rr}		-	10.5	-	nC

Note:

1. Calculated continuous current based on maximum allowable junction temperature.
2. Repetitive rating; pulse width limited by max. junction temperature.
3. The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.
4. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ C$

Typical Electrical and Thermal Characteristic Curves

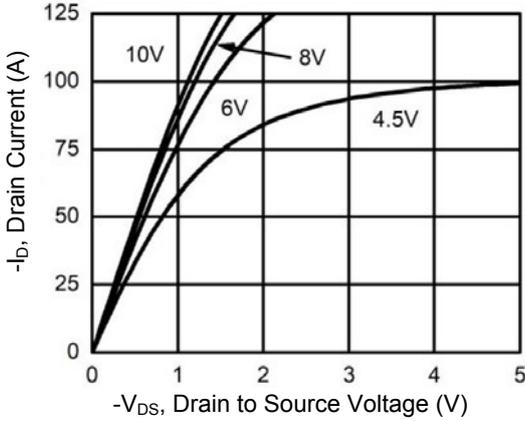


Figure 1. Output Characteristics

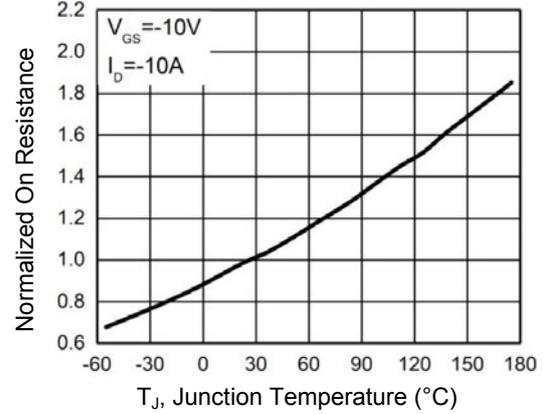


Figure 2. Normalized $R_{DS(on)}$ vs. T_J

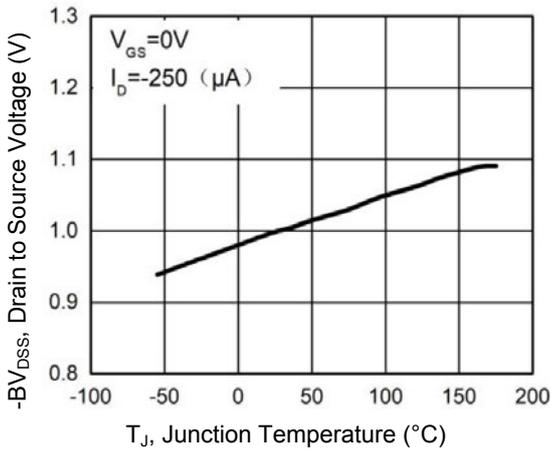


Figure 3. Drain to Source Voltage vs. T_J

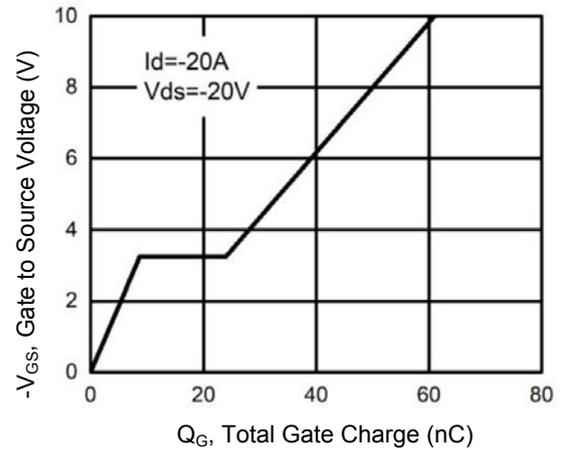


Figure 4. Gate Charge Characteristics

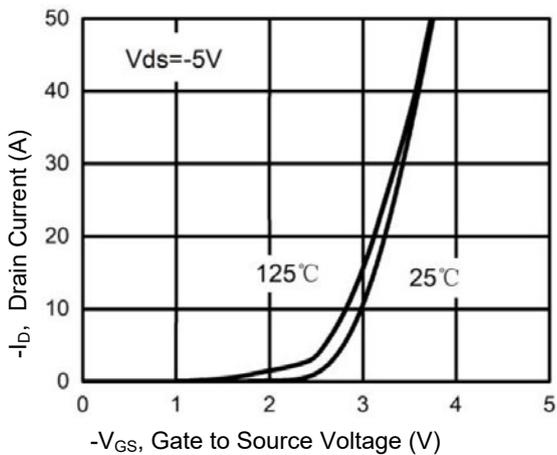


Figure 5. Transfer Characteristics

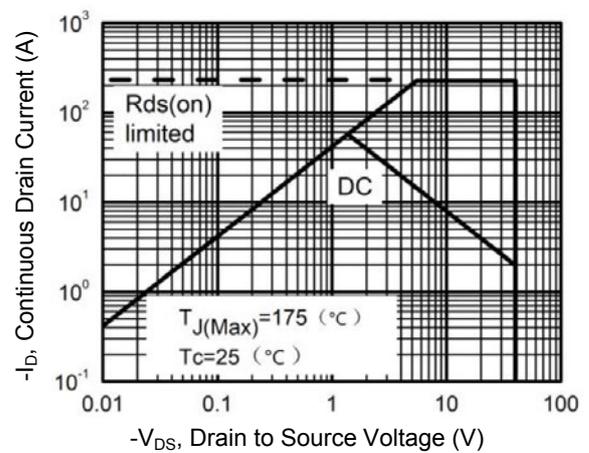


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

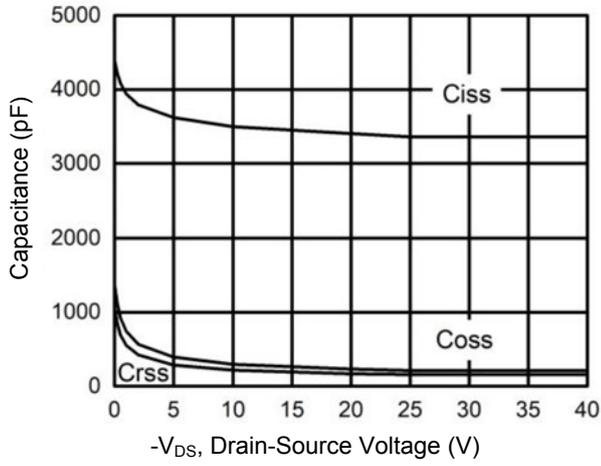


Figure 7. Capacitance Characteristics

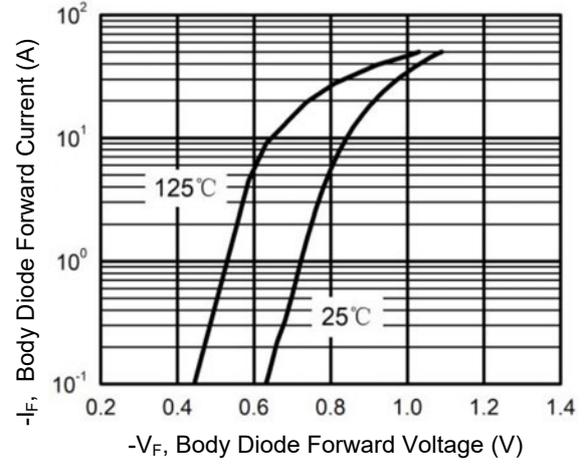
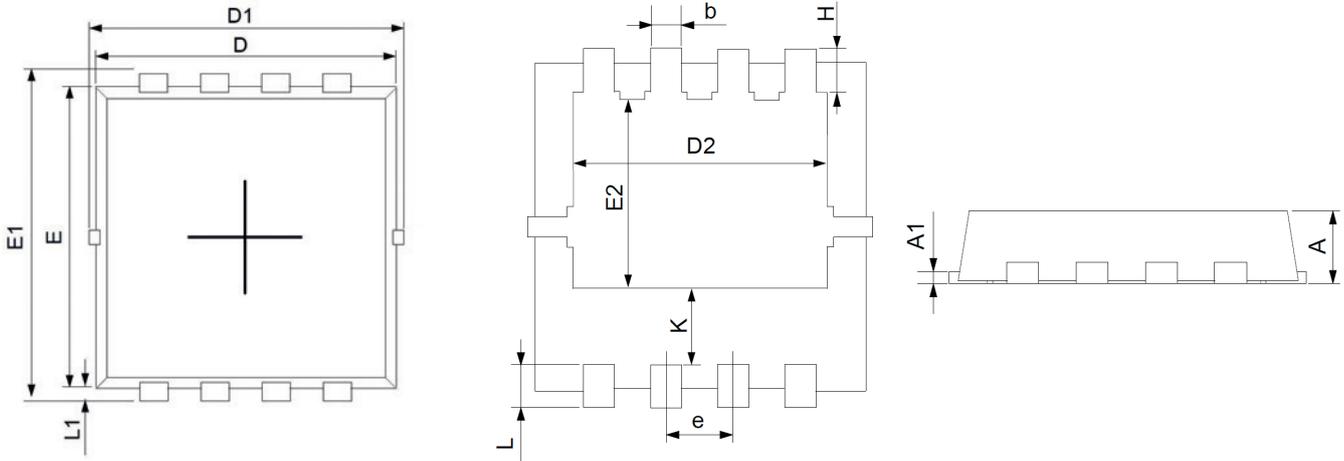


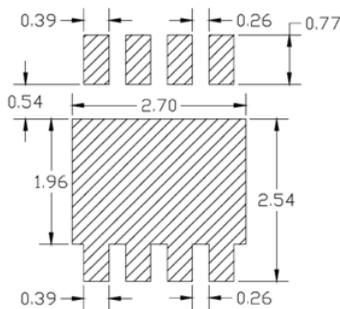
Figure 8. Body Diode Characteristics

Package Outline Dimensions (PPAK3x3)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.70	0.90	0.028	0.035
A1	0.14	0.20	0.006	0.008
D	3.05	3.25	0.120	0.128
E	2.90	3.10	0.114	0.122
D1	3.10	3.50	0.122	0.138
D2	2.35	2.50	0.093	0.098
E1	3.10	3.50	0.122	0.138
E2	1.64	1.84	0.065	0.072
b	0.25	0.35	0.010	0.014
K	0.59	0.79	0.023	0.031
e	0.55	0.75	0.022	0.030
L	0.25	0.55	0.010	0.022
L1	0.10	0.20	0.004	0.008
H	0.32	0.52	0.013	0.020

Recommended Pad Layout



Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purposes only

Order Information

Device	Package	Marking	Carrier	Quantity
GSFN4013	PPAK3x3	N4013	Tape & Reel	5,000pcs / Reel

For more information, please contact us at: inquiry@goodarksemi.com