



RF360
Europe GmbH

Data sheet

SAW RF filter
Automotive telematics
SDARS

Series/type:	B2623
Ordering code:	B39232B2623P810
Date:	January 06, 2020
Version:	2.0

DCN: 80-PA243-443 Rev. A

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Table of contents

1	Application	4
2	Features	4
3	Package	5
4	Pin configuration	5
5	Matching circuit	6
6	Characteristics	7
7	Maximum ratings	9
8	Transmission coefficient	10
9	Return loss	11
10	Group delay	12
11	Packing material	13
12	Marking	16
13	Soldering profile	17
14	ESD protection of SAW filters	18
15	Annotations	19
16	Cautions and warnings	20
17	Important notes	21

1 Application

- Low-loss RF filter for digital radio
- Impedance transformation from 50Ω to 100Ω
- Unbalanced to balanced operation
- Very low insertion attenuation
- Low amplitude ripple
- Usable pass band 25 MHz

2 Features

- Package size 1.1±0.1 mm × 0.9±0.1 mm
- Package height 0.45 mm (max.)
- Approximate weight 2 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- Overmold demonstrated with RF360 specific mold process
- Moisture Sensitivity Level 2a (MSL2a)
- AEC-Q200 qualified component family (Grade 1: -40 °C to +125 °C)

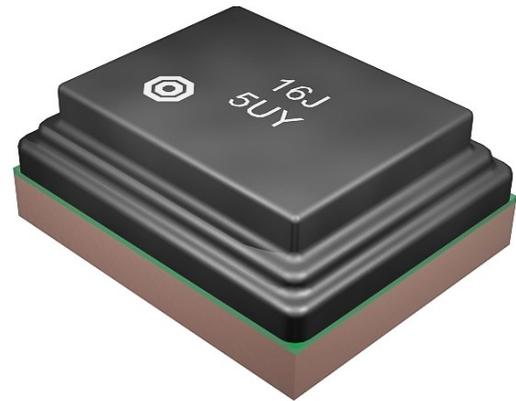
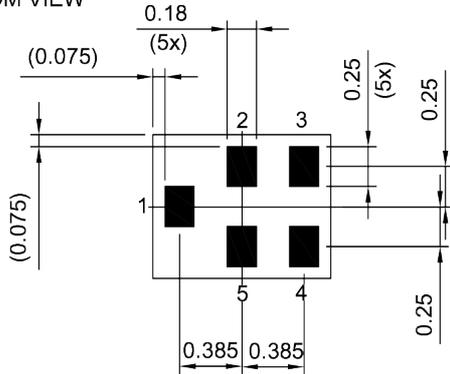


Figure 1: Picture of component with example of product marking.

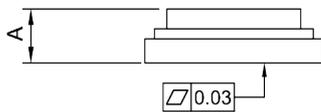
3 Package

BOTTOM VIEW

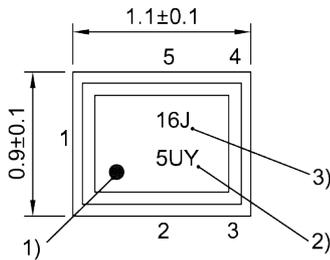


Pad and pitch tolerance ±0.05

SIDE VIEW

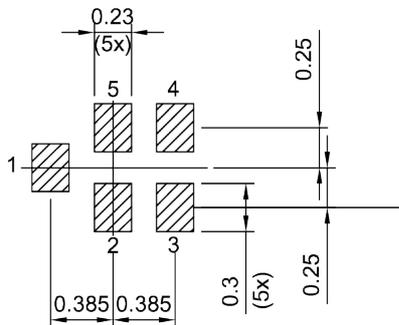


TOP VIEW



- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number

Land pattern
THRU VIEW



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.45 mm (max.). See Sec. Package information (p. 20).

4 Pin configuration

- 1 Input
- 3, 4 Output balanced
- 2, 5 Ground

5 Matching circuit

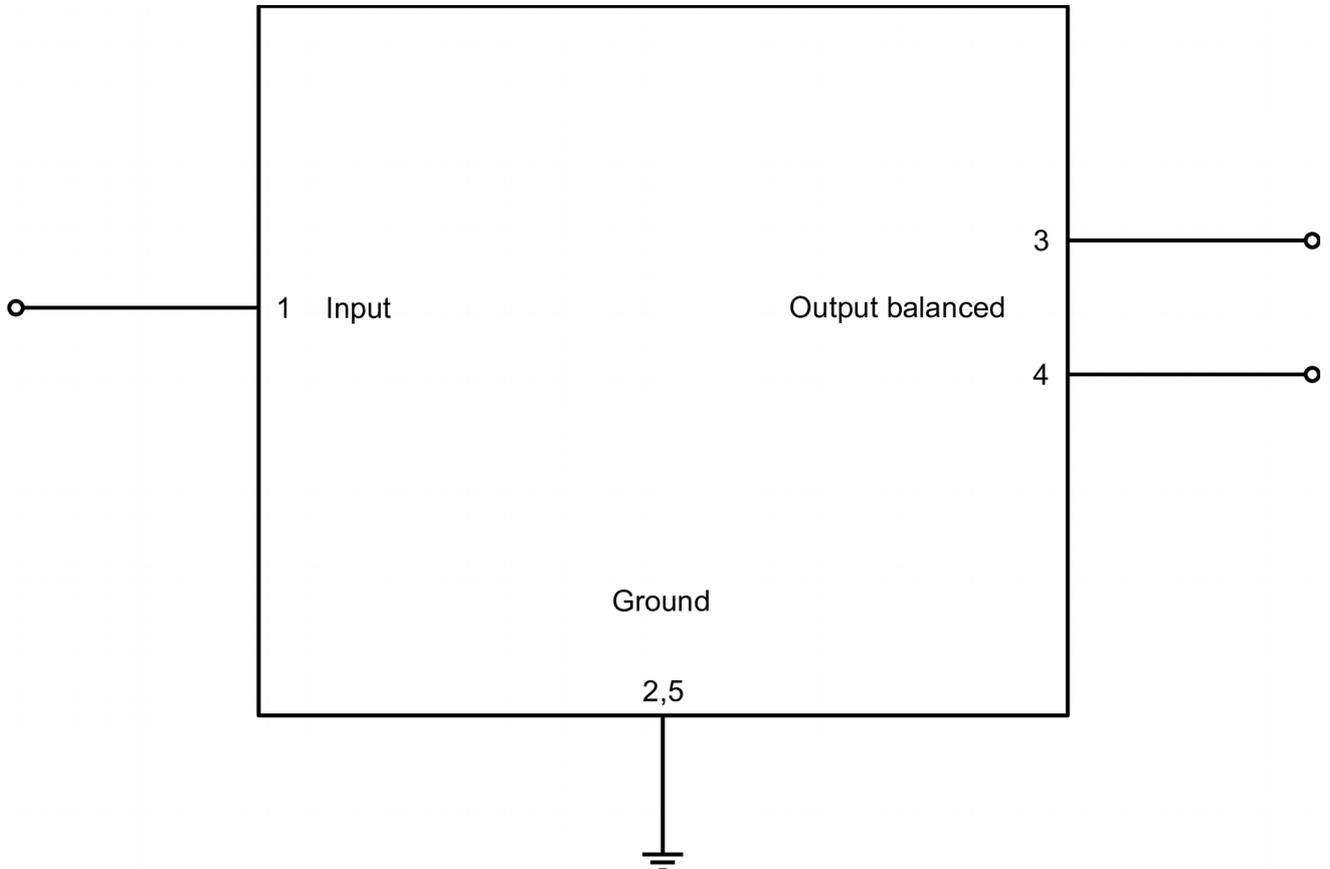


Figure 3: Schematic of matching circuit. No external matching components required.

6 Characteristics

Temperature range for specification	T_{SPEC}	= -40 °C ... +105 °C
Input terminating impedance	Z_{IN}	= 50 Ω
Output terminating impedance	Z_{OUT}	= 100 Ω

Characteristics			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}		
Center frequency		f_C	—	2332.5	—	MHz	
Minimum insertion attenuation	2320... 2345	MHz	α_{min}	1.5	—	dB	
Maximum insertion attenuation	2320... 2345	MHz	α_{max}	1.7	2.2 ¹⁾	dB	
	2320... 2345	MHz		1.7	2.3	dB	
Amplitude ripple (p-p)			$\Delta\alpha$				
	2320... 2345	MHz		0.2	0.7 ¹⁾	dB	
	2320... 2345	MHz		0.2	0.9	dB	
Group delay ripple			$\Delta\tau_{var}$				
	2320... 2345	MHz		2.0	9.0 ¹⁾	ns	
	2320... 2345	MHz		2.0	11	ns	
Minimum return loss			α				
@ input port	2320... 2345	MHz		9.0	16	dB	
@ output port	2320... 2345	MHz		8.5	13	dB	
Variation of amplitude imbalance			Δ_{var}				
	2320... 2345	MHz		-2.0 ¹⁾	1.3	2.5 ¹⁾	dB
	2320... 2345	MHz		-5.0	1.3	3.5	dB
Variation of phase imbalance			Θ_{var}				
	2320... 2345	MHz		-12 ¹⁾	3.2	12 ¹⁾	°
	2320... 2345	MHz		-15	3.2	15	°
Minimum attenuation (relative to α_{min})			$\alpha_{rel,min}$				
	88... 880	MHz		50	55	dB	
	880... 1710	MHz		41	45	dB	
	1710... 1910	MHz		38	44	dB	
	1910... 2090	MHz		38	42	dB	
	2090... 2190	MHz		35	39	dB	
	2190... 2250	MHz		30	35	dB	
	2250... 2275	MHz		17	28	dB	
	2300	MHz		—	5.5	dB	
	2305	MHz		—	3.8	dB	
	2310	MHz		—	3.2	dB	
	2315	MHz		—	2.7	dB	
	2350	MHz		—	2.8	dB	
	2355	MHz		—	2.9	dB	
	2360	MHz		—	3.4	dB	

Characteristics			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
	2365	MHz	—	3.7	—	dB
	2390	MHz	—	30	—	dB
	2400... 2425	MHz	19	39	—	dB
	2425... 2500	MHz	32	39	—	dB
	2500... 2600	MHz	35	45	—	dB
	2600... 2800	MHz	36	42	—	dB
	2800... 3000	MHz	36	42	—	dB
	3000... 4000	MHz	31	35	—	dB
	4000... 4800	MHz	27	32	—	dB
	4800... 6000	MHz	24	29	—	dB

¹⁾ Valid for temperature $T = -20\text{ °C} \dots +85\text{ °C}$.

7 Maximum ratings

Operable temperature	$T_{OP} = -40\text{ °C} \dots +125\text{ °C}$	
Storage temperature	$T_{STG}^{1)} = -40\text{ °C} \dots +125\text{ °C}$	
DC voltage	$ V_{DC} ^{2)} = 0\text{ V (max.)}$	
Input power	P_{IN}	
@ input port: 698 ... 2190 MHz	10 dBm	SC-FDMA for 100000 h @105°C.
@ input port: 2310 ... 2355 MHz	6.0 dBm	OFDM for 100000 h @105°C.
@ input port: 2320 ... 2345 MHz	6.0 dBm	OFDM for 100000 h @105°C.
@ input port: 2400 ... 5925 MHz	10 dBm	SC-FDMA for 100000 h @105°C.

¹⁾ Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C.

²⁾ In case of applied DC voltage blocking capacitors are mandatory.

8 Transmission coefficient

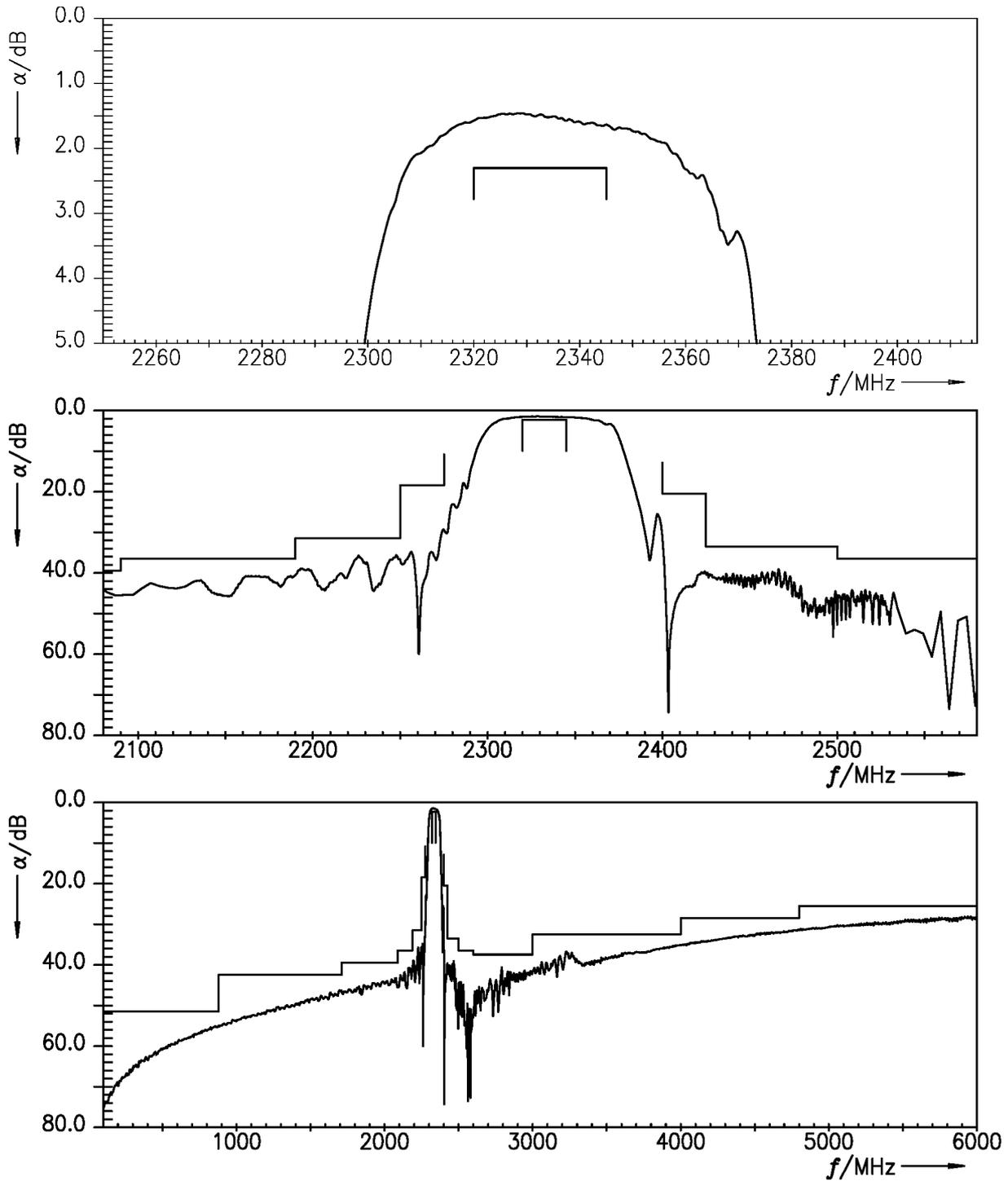


Figure 4: Attenuation.

9 Return loss

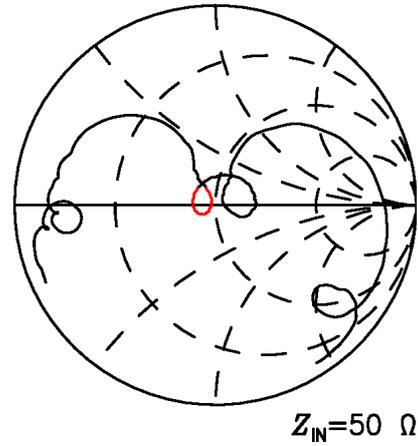
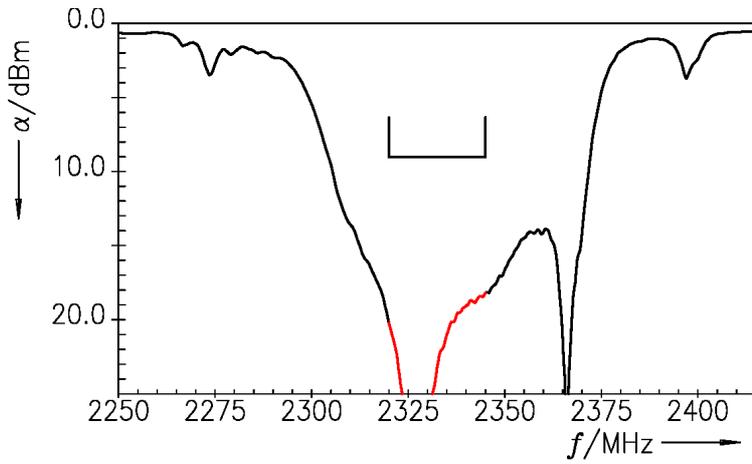


Figure 5: Return loss at input port.

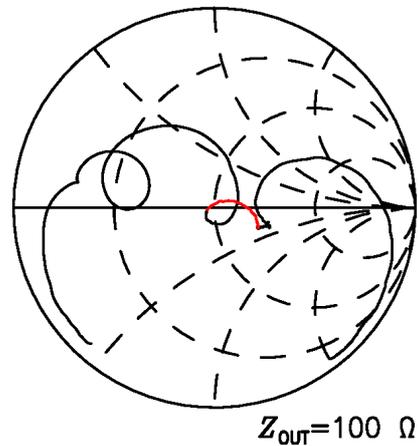
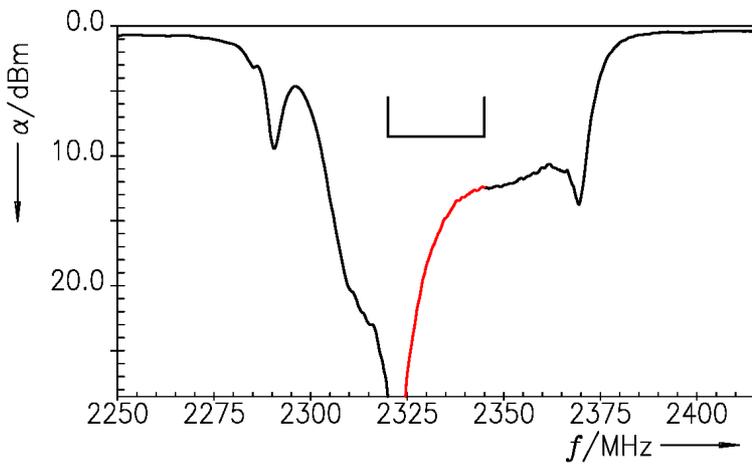


Figure 6: Return loss at output port.

10 Group delay

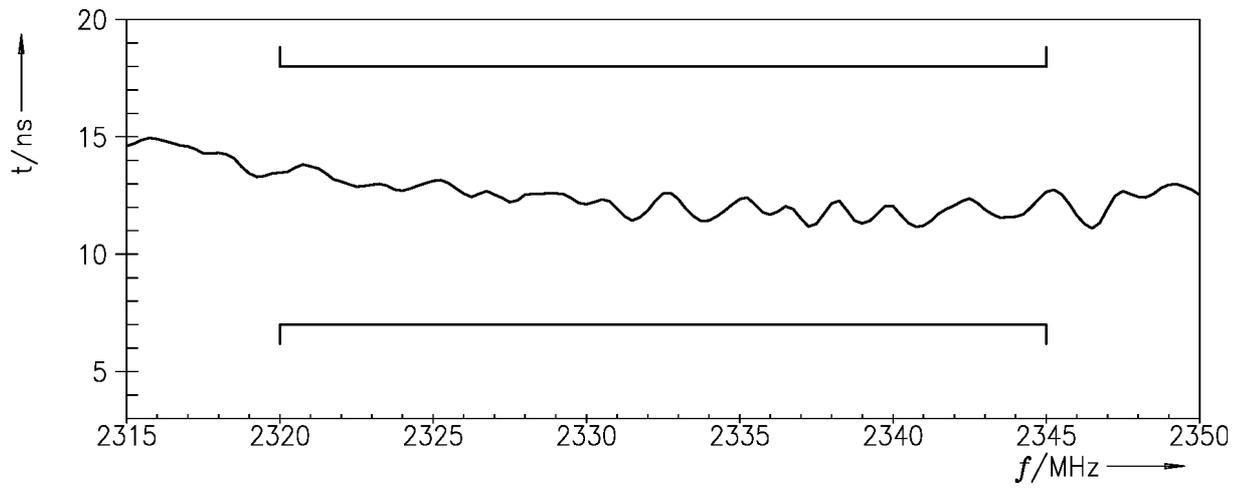


Figure 7: Group delay ripple.

11 Packing material

11.1 Tape

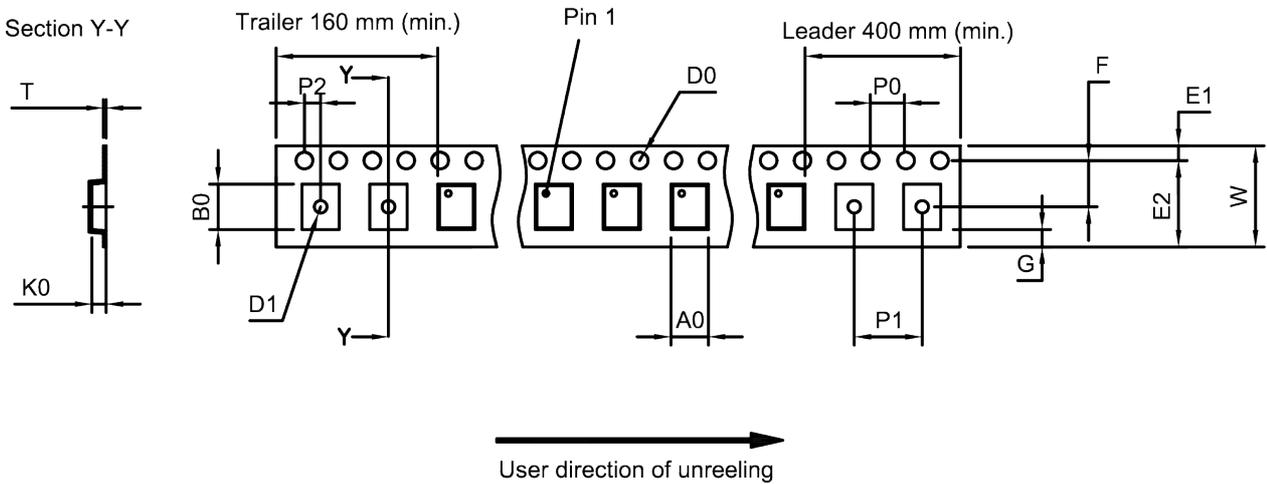


Figure 8: Drawing of tape (first-angle projection) for illustration only and not to scale. The valid tape dimensions are listed in Table 1.

A ₀	1.02±0.05 mm	E ₂	6.25 mm (min.)	P ₁	2.0±0.1 mm
B ₀	1.22±0.05 mm	F	3.5±0.05 mm	P ₂	2.0±0.05 mm
D ₀	1.55±0.05 mm	G	–	T	0.25±0.03 mm
D ₁	0.55±0.1 mm	K ₀	0.6±0.05 mm	W	8.0+0.3/-0.1 mm
E ₁	1.75±0.1 mm	P ₀	4.0±0.1 mm		

Table 1: Tape dimensions.

11.2 Reel with diameter of 180 mm

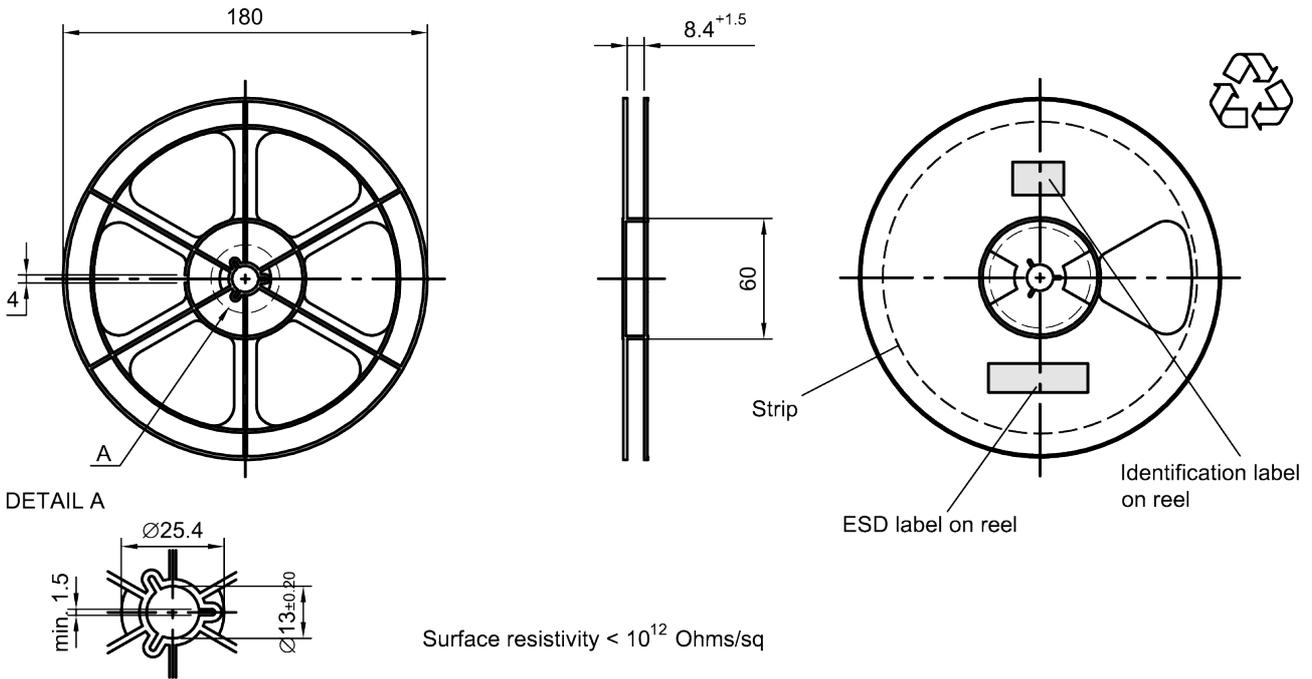


Figure 9: Drawing of reel (first-angle projection) with diameter of 180 mm.

Dimensions [mm]

X = 220+5

Y = 235+5

Sealing area 10±3

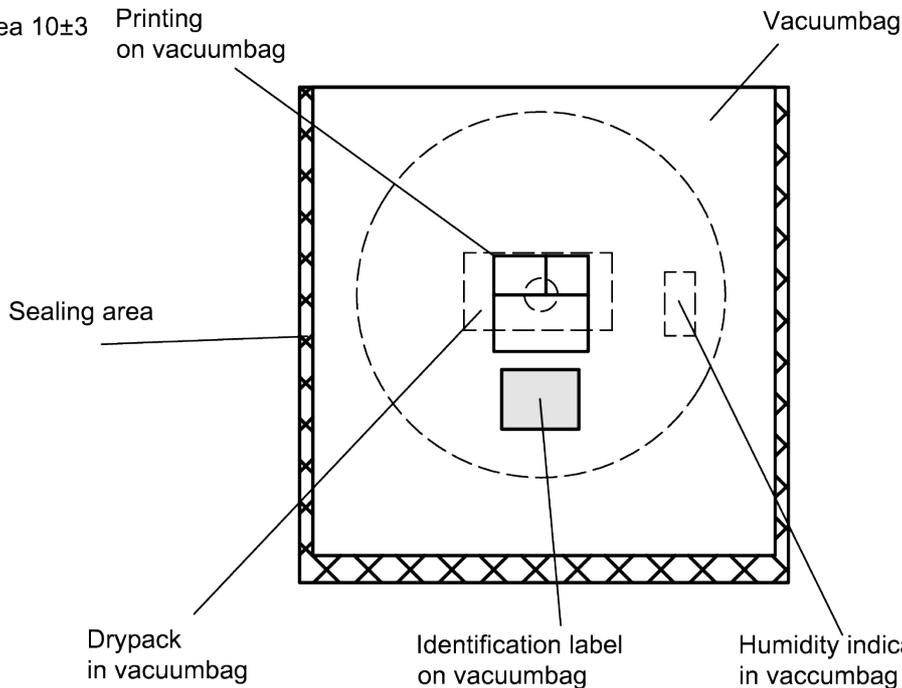


Figure 10: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

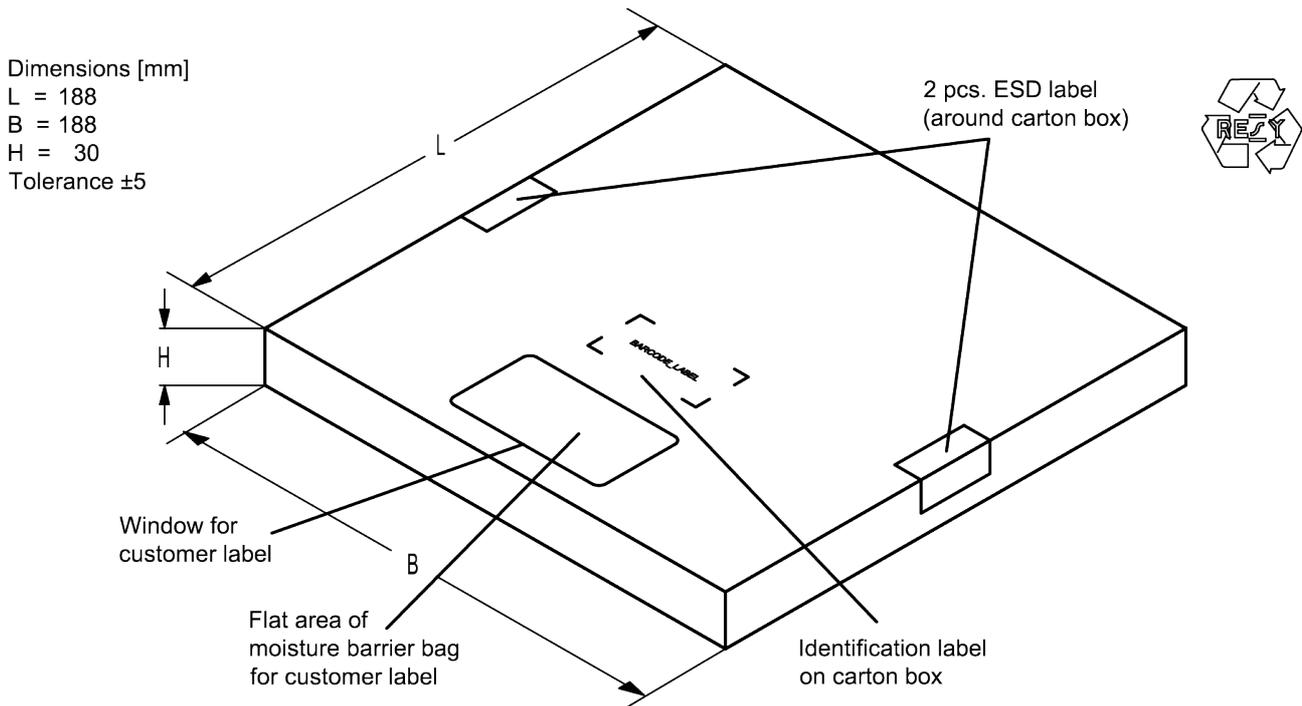


Figure 11: Drawing of folding box for reel with diameter of 180 mm.

12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, e.g., B3xxxxB**1234**xxxx,
is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device in decimal code.
16J => **1234**
 $1 \times 32^2 + 6 \times 32^1 + 18 (=J) \times 32^0 =$ **1234**
 The BASE32 code for product type B2623 is 2HZ.

■ Lot number:

The last 5 digits of the lot number, e.g., **12345**,
are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.
5UY => **12345**
 $5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0 =$ **12345**

Adopted BASE32 code for type number			
Decimal value	Base32 code	Decimal value	Base32 code
0	0	16	G
1	1	17	H
2	2	18	J
3	3	19	K
4	4	20	M
5	5	21	N
6	6	22	P
7	7	23	Q
8	8	24	R
9	9	25	S
10	A	26	T
11	B	27	V
12	C	28	W
13	D	29	X
14	E	30	Y
15	F	31	Z

Adopted BASE47 code for lot number			
Decimal value	Base47 code	Decimal value	Base47 code
0	0	24	R
1	1	25	S
2	2	26	T
3	3	27	U
4	4	28	V
5	5	29	W
6	6	30	X
7	7	31	Y
8	8	32	Z
9	9	33	b
10	A	34	d
11	B	35	f
12	C	36	h
13	D	37	n
14	E	38	r
15	F	39	t
16	G	40	v
17	H	41	\
18	J	42	?
19	K	43	{
20	L	44	}
21	M	45	<
22	N	46	>
23	P		

Table 2: Lists for encoding and decoding of marking.

13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
T ≥ 255 °C	–
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

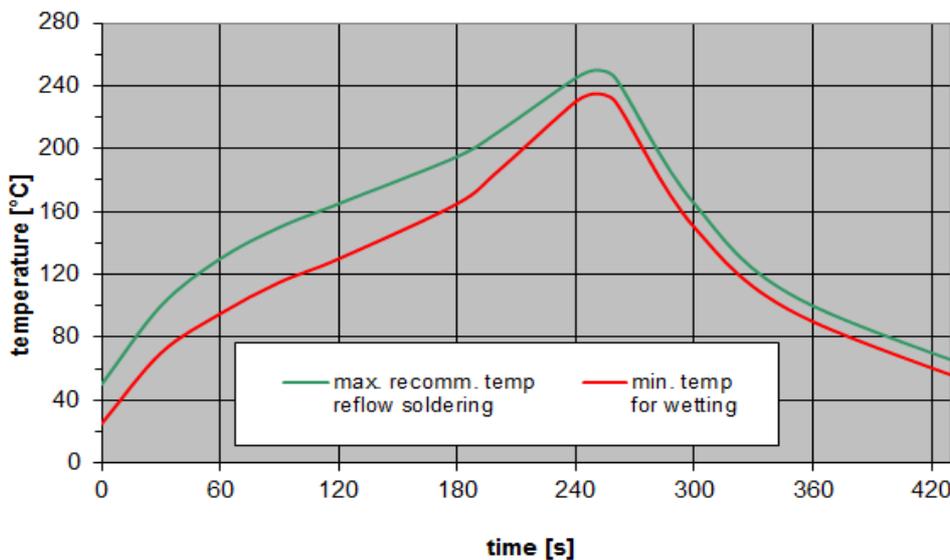


Figure 12: Recommended reflow profile for convection and infrared soldering – lead-free solder.

14 ESD protection of SAW filters

SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, “ESD matching” has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended “ESD matching” topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

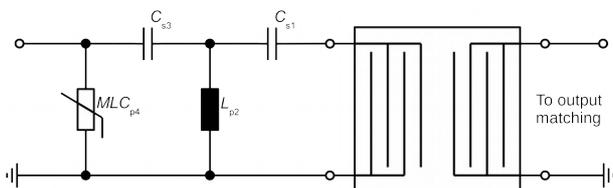


Figure 13: MLC varistor plus ESD matching.

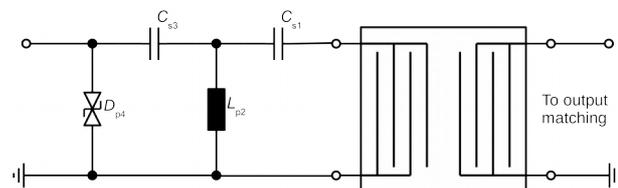


Figure 14: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified “ESD matching” topologies can be used alternatively.

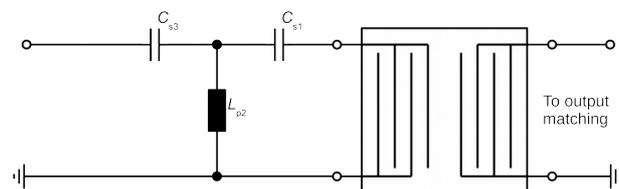


Figure 15: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: “**ESD protection for SAW filters**”. This report can be found under <https://rfe.qualcomm.com>.

15 Annotations

15.1 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

15.2 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

16 Cautions and warnings

16.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under <https://rfe.qualcomm.com/>.

16.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

16.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

16.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.

17 Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, RF360 Europe GmbH and its affiliates are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an RF360 product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous)**. Useful information on this will be found in our Material Data Sheets on the Internet (<https://rfe.qualcomm.com>). Should you have any more detailed questions, please contact our sales offices.
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