

General Description

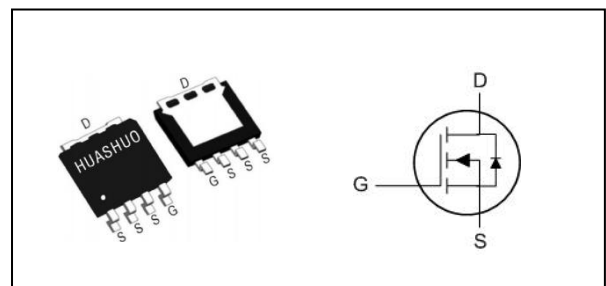
- 100% UIS Tested
- Advanced Trench Technology
- Low Gate Charge
- High Current Capability
- RoHS and Halogen-Free Compliant

Product Summary

V_{DS}	30	V
$R_{DS(ON),typ}$	0.35	m Ω
I_D	390	A

Applications

- Power Management in Desktop Computer
- DC/DC Converters

LFPAK5X6 Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	390	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	245	A
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	53	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	41	A
$I_D@T_A=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	33	A
I_{DM}	Pulsed Drain Current ²	700	A
EAS	Single Pulse Avalanche Energy ³	129	mJ
I_{AS}	Avalanche Current	50	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation ⁴	140	W
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	2.5	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.9	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =30A	---	0.35	0.6	mΩ
		V _{GS} =4.5V, I _D =30A	---	0.6	0.9	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.7	2.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	2.5	---	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =15V, V _{GS} =10V, I _D =30A	---	155	---	nC
Q _g	Total Gate Charge (4.5V)		---	80	---	
Q _{gs}	Gate-Source Charge		---	26	---	
Q _{gd}	Gate-Drain Charge		---	30	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =20V, V _{GS} =10V, R _G =3Ω, I _D =30A	---	16	---	ns
T _r	Rise Time		---	59	---	
T _{d(off)}	Turn-Off Delay Time		---	135	---	
T _f	Fall Time		---	114	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	8987	---	pF
C _{oss}	Output Capacitance		---	3930	---	
C _{rss}	Reverse Transfer Capacitance		---	728	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V _G =V _D =0V, Force Current	---	---	100	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =30A, dI/dt=100A/μs, T _J =25°C	---	104	---	nS
Q _{rr}	Reverse Recovery Charge		---	183	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=50A
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.
- Package limitation current is 100A.

Typical Characteristics

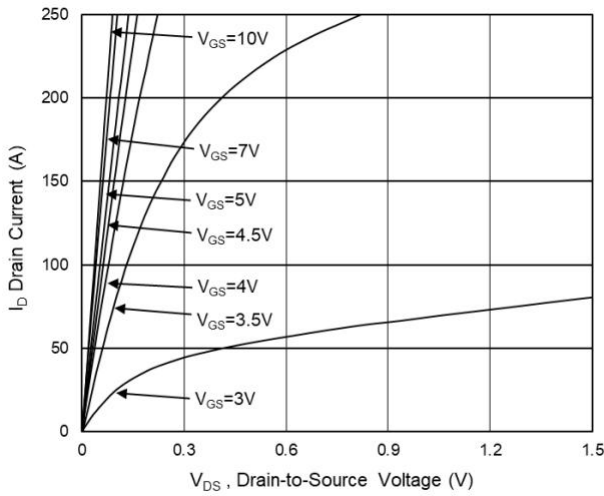


Fig.1 Typical Output Characteristics

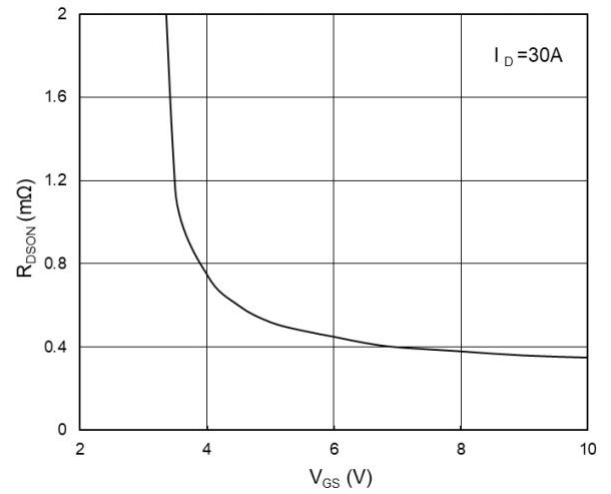


Fig.2 On-Resistance vs G-S Voltage

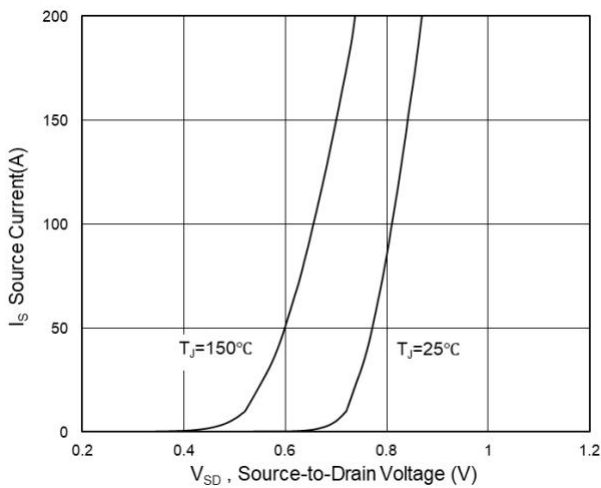


Fig.3 Source Drain Forward Characteristics

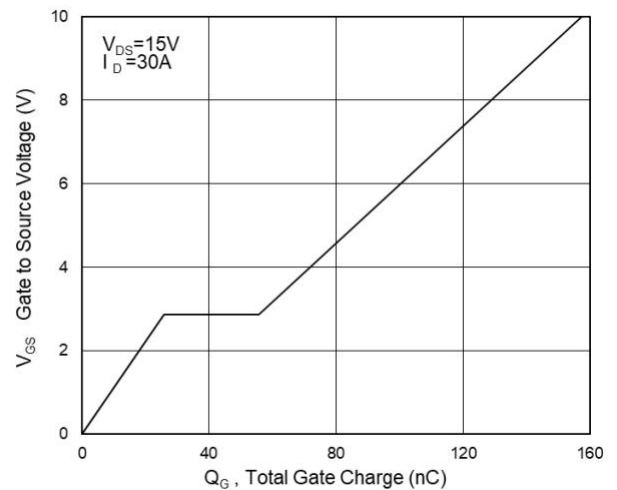


Fig.4 Gate-Charge Characteristics

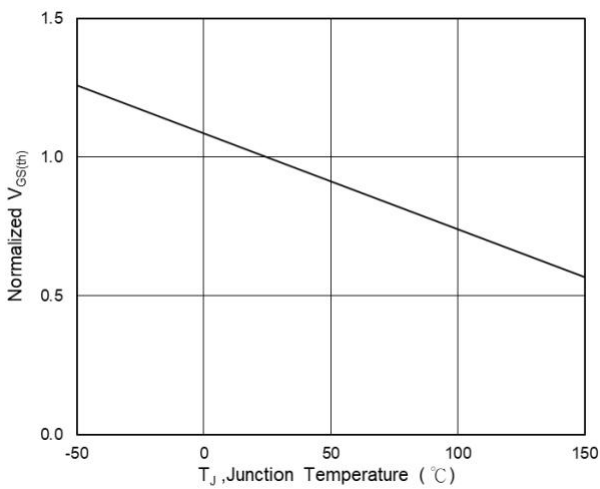


Fig.5 Normalized $V_{GS(th)}$ vs T_J

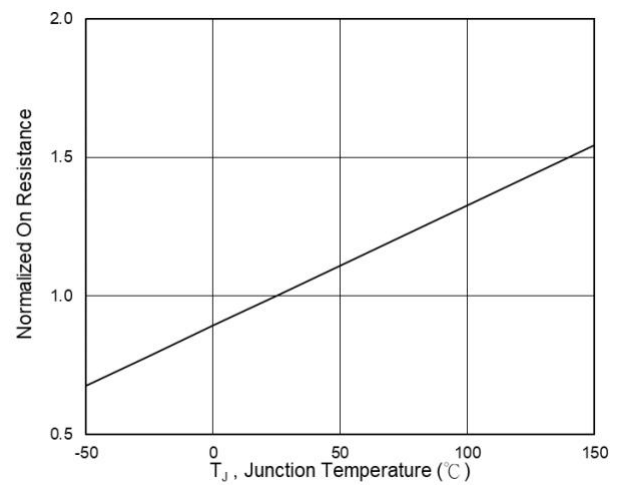


Fig.6 Normalized $R_{DS(on)}$ vs T_J



N-Ch 30V Fast Switching MOSFETs

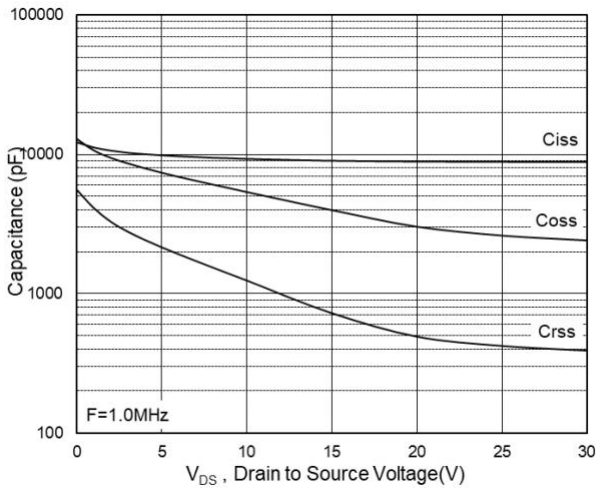


Fig.7 Capacitance

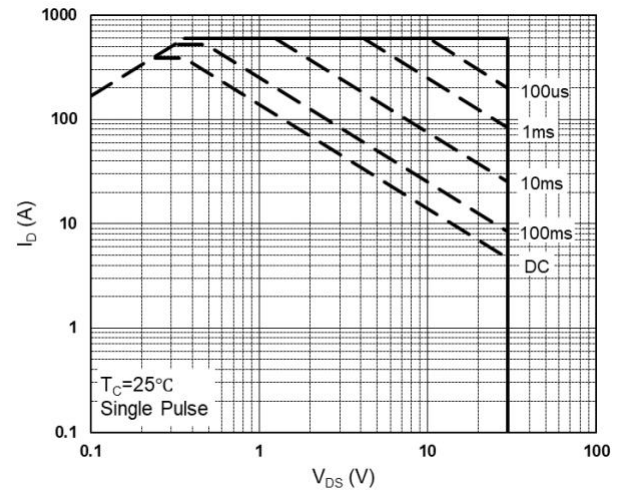


Fig.8 Safe Operating Area

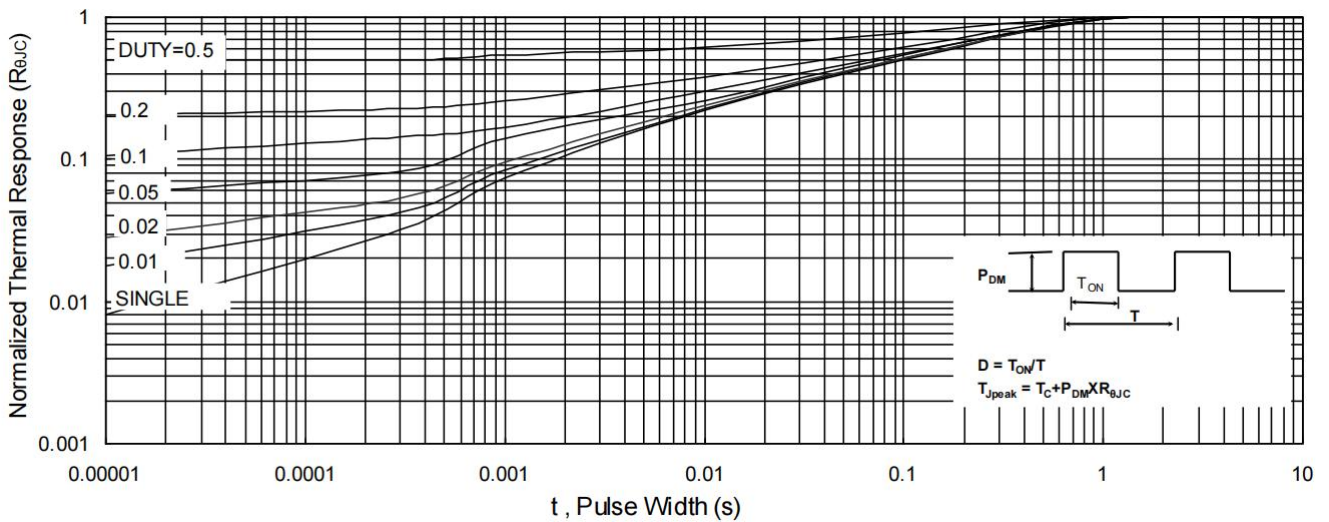


Fig.9 Normalized Maximum Transient Thermal Impedance

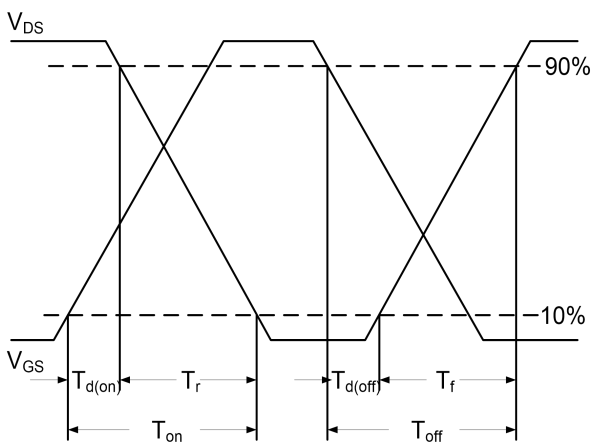


Fig.10 Switching Time Waveform

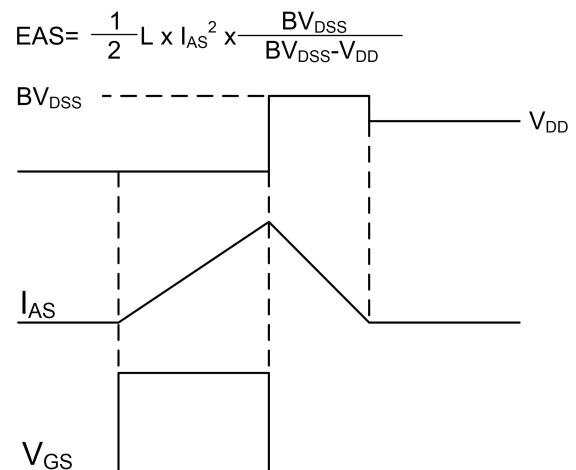


Fig.11 Unclamped Inductive Switching Waveform