

Silicon NPN Epitaxial Planer Low Frequency Power Amplifier

Features

- Small size package: SOT89-3L
- Large Maximum current: $I_C = 3A$
- Low collector to emitter saturation voltage: $V_{CE(sat)} = 0.3 V \text{ max. (at } I_C/I_B = 0.5 A/0.05 A)$
- High power dissipation: $P_C = 1W$
- Complementary pair with DXT751

Outline

(Package name: SOT89-3L)



The image shows the physical package, a top view with pins labeled 1 (B), 2 (C), 3 (E), and 4 (C), and a schematic diagram of an NPN transistor with pins labeled 1 (BASE), 2,4 (COLLECTOR), and 3 (EMITTER).

SOT89-3L

TOP VIEW
Schematic and Pin Configuration

Note: Marking is "KN2".

Absolute Maximum Ratings

($T_a = 25^\circ C$)

Item	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	80	V
Collector to emitter voltage	V_{CEO}	60	V
Emitter to base voltage	V_{EBO}	5	V
Collector current	I_C	3	A
Collector peak current	$i_c(\text{peak})$	6	A
Collector power dissipation	P_C	1	W
Junction temperature	T_j	150	$^\circ C$
Storage temperature	T_{stg}	-55 to +150	$^\circ C$

Note: *When using alumina ceramic board (25 x 60 x 0.7 mm)

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Collector to base breakdown voltage	$V_{(BR)CBO}$	80	—	—	V	$I_C = 100 \mu A, I_E = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	60	—	—	V	$I_C = 10 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	5	—	—	V	$I_E = 100 \mu A, I_C = 0$
Collector cutoff current	I_{CBO}	—	—	100	nA	$V_{CB} = 60 \text{ V}, I_E = 0$
Emitter cutoff current	I_{EBO}	—	—	100	nA	$V_{EB} = 4 \text{ V}, I_C = 0$
DC current transfer ratio	h_{FE}	—	—	300	—	$V_{CE} = 2 \text{ V}, I_C = 0.1 \text{ A}$
Collector to emitter saturation voltage	$V_{CE(sat)}$	—	0.16	0.3	V	$I_C = 1 \text{ A}, I_B = 100 \text{ mA}$
Base to emitter saturation voltage	$V_{BE(sat)}$	—	0.91	1.2	V	$I_C = 1 \text{ A}, I_B = 100 \text{ mA}$
Gain bandwidth product	f_T	100	145	—	MHz	$V_{CE} = 10 \text{ V}, I_C = -50 \text{ mA}, f = 100 \text{ MHz}$
Collector output capacitance	C_{ob}	—	—	30	pF	$V_{CC} = 10 \text{ V}, I_C = 500 \text{ mA},$

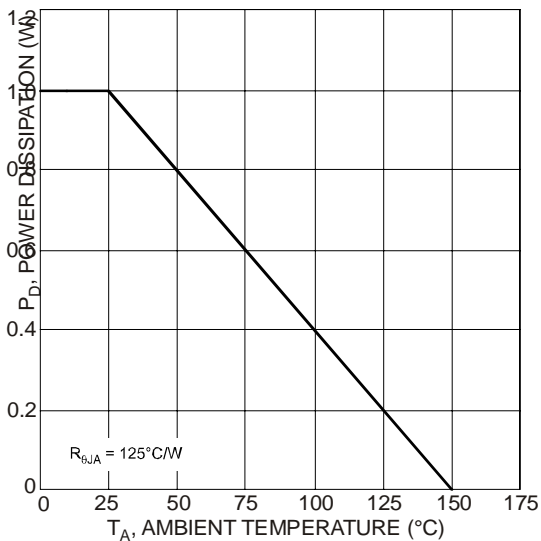


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

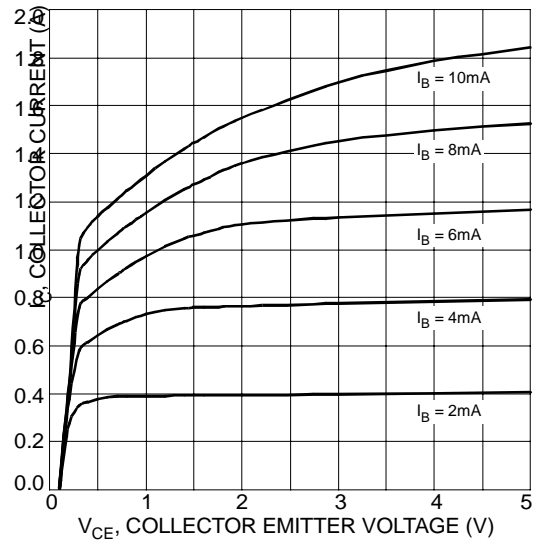


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

Electrical Characteristics

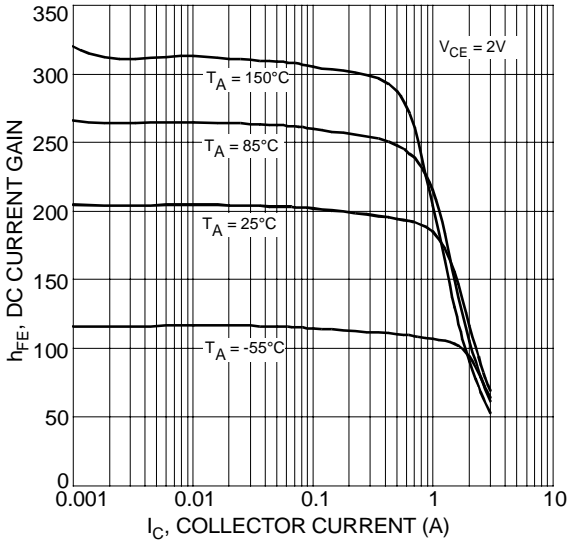


Fig. 3 Typical DC Current Gain vs. Collector Current

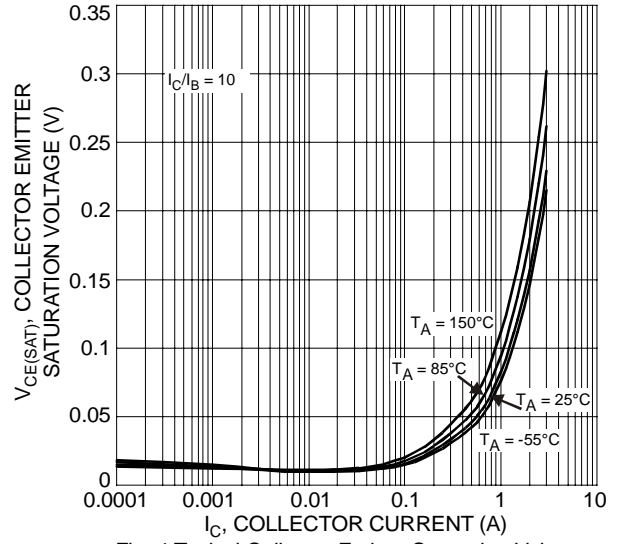


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

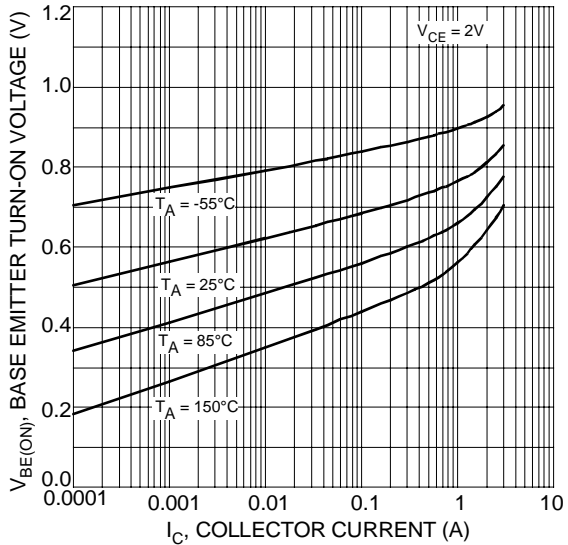


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

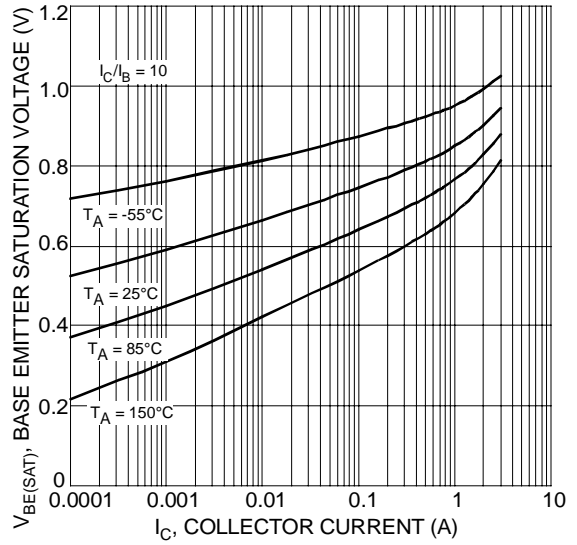


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

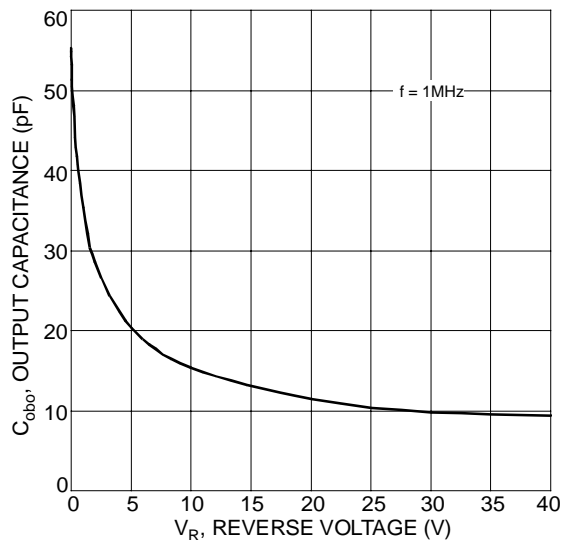


Fig. 7 Typical Output Capacitance Characteristics

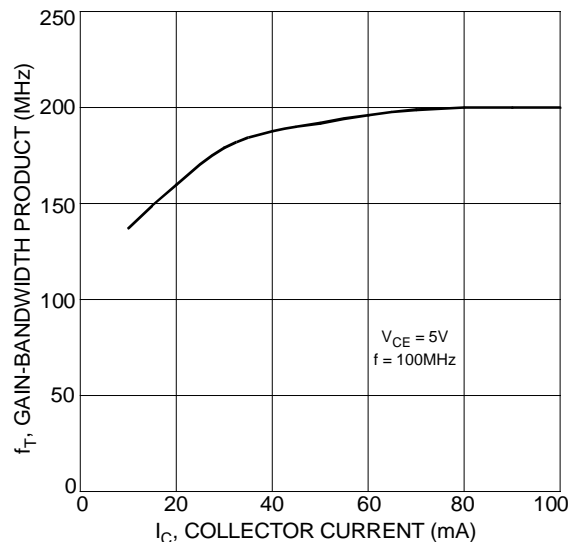


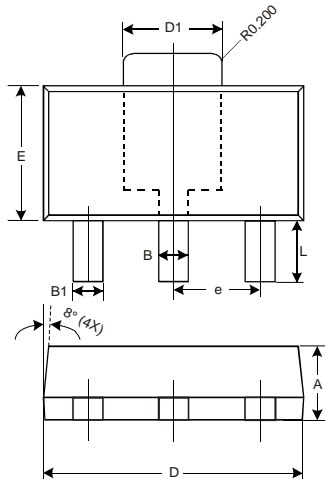
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

Electrical Characteristics

Ordering Information (Note 5)

Device	Packaging	Shipping
DXT651	SOT89-3L	2500/Tape & Reel

Package Outline Dimensions



SOT89-3L			
Dim	Min	Max	Typ
A	1.40	1.60	1.50
B	0.45	0.55	0.50
B1	0.37	0.47	0.42
C	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.50	1.70	1.60
E	2.40	2.60	2.50
e	—	—	1.50
H	3.95	4.25	4.10
L	0.90	1.20	1.05
All Dimensions in mm			

Suggested Pad Layout

