

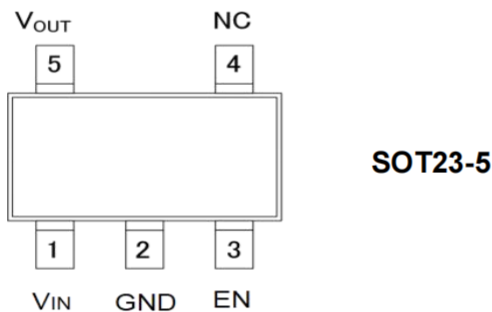
Features

- 0.8 μ A Current at no Load(TYP.)
- $\pm 2\%$ Output Accuracy
- 300mA Output Current
- Compact package: SOT23-5

Applications

- Portable consumer equipments
- Radio control systems
- Wireless Communication Equipments
- Ultra Low Power Micro controller

PIN CONFIGURATION



Pin Number	Pin Name	Pin Function
SOT23-5		
5	V_{OUT}	Output Pin
2	V_{SS}/GND	Ground
3	EN	Chip Enable Pin
1	V_{IN}	Power Input Pin
4	NC	No Internal Connection

Ordering Information

TLV74333PDBVR-TP

PACKAGE TYPE
SOT23-5

OUTPUT VOLTAGE

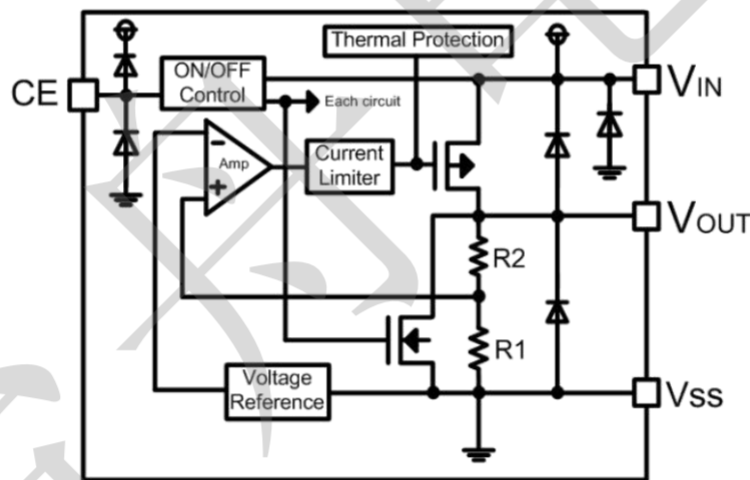
12: 1.2V	15: 1.5V
18: 1.8V	25: 2.5V
28: 2.8V	30: 3.0V
33: 3.3V	36: 3.6V

Absolute Maximum Ratings

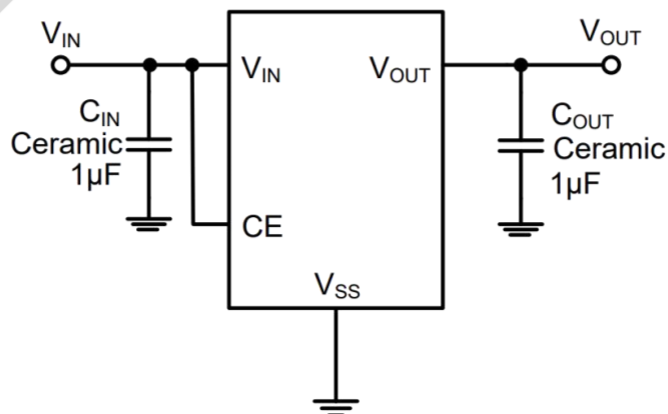
over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT
VIN	Continuous input voltage range	-0.3	7	V
VOUT	Output voltage range	-0.3	VIN+0.3	
Current	Maximum output current	Internally limited		mA
Temperature	Operating Temperature, Topr	-40	+85	°C
	Storage, Tstg	-40	+150	
	Welding temperature and time, Tsolder	+260, 10s		
Power Dissipation	Pd SOT23-5	300		mW

BLOCK DIAGRAM



Typical Application Circuit



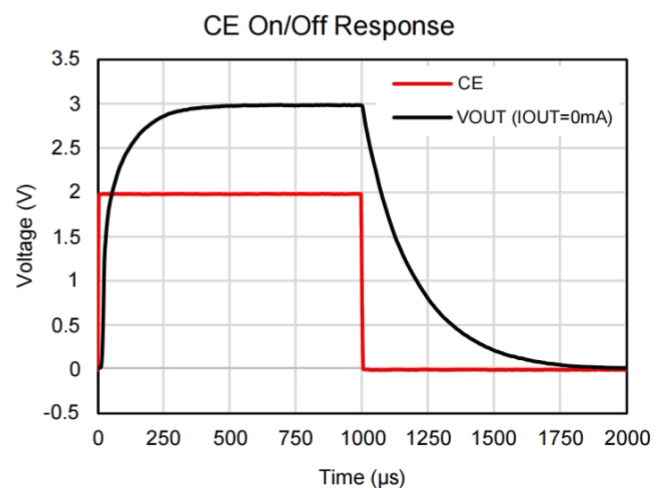
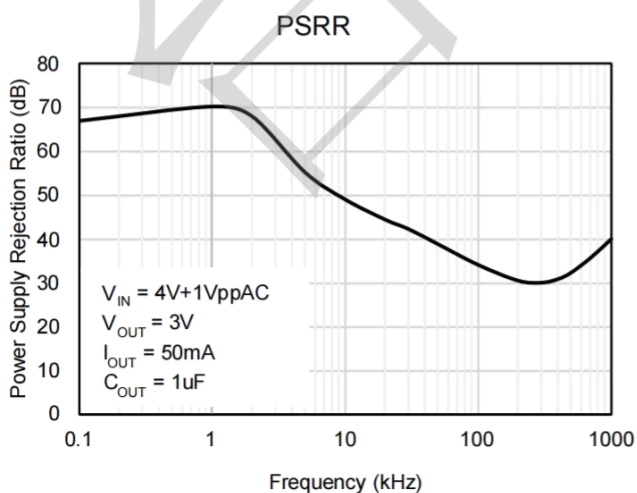
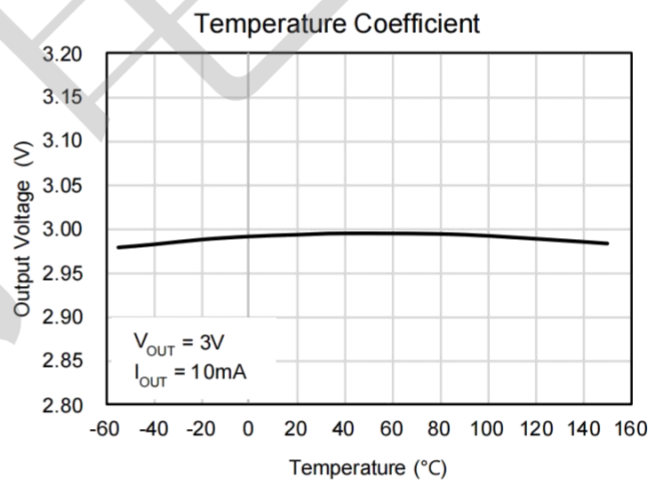
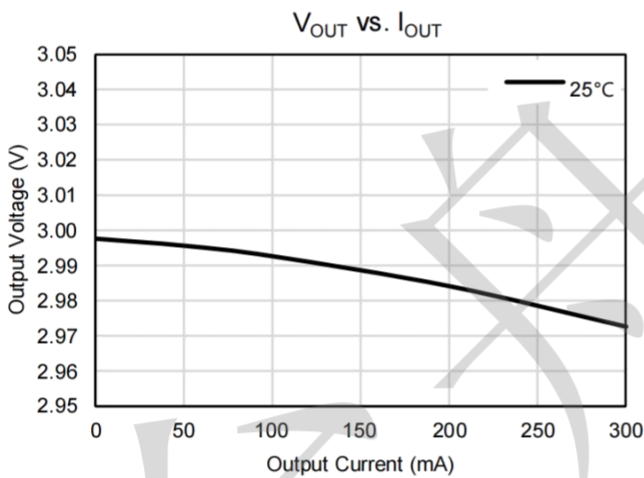
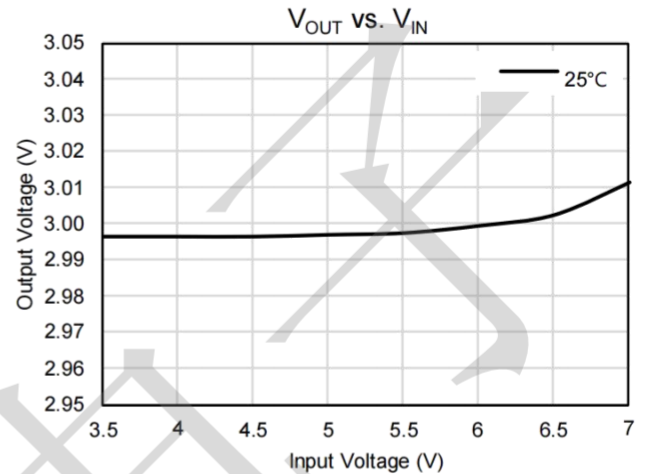
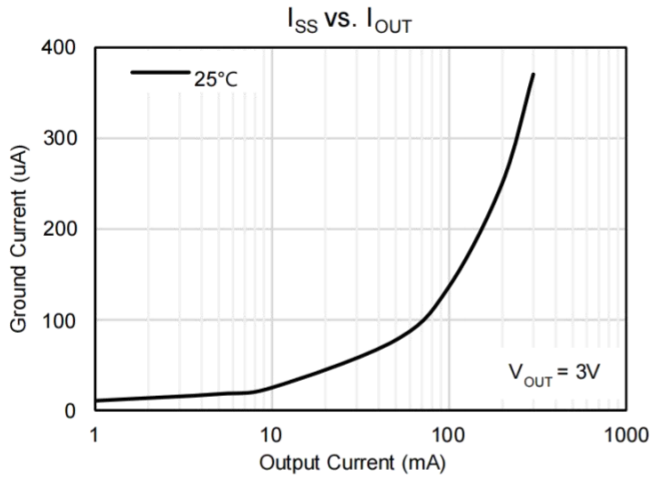
Electrical Characteristics (TA=25°C, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Input Voltage	V _{IN}	--	1.8	--	7.0	V	
Output Voltage	V _{OUT}	--	1.2	--	3.6	V	
DC Output Accuracy	--	I _{OUT} =1mA	-2	--	2	%	
Output Current	I _{OUT}	V _{IN} =1.8V	150	--	--	mA	
		V _{IN} =4.3V	350	--	--		
Dropout Voltage	V _{dif} ⁽²⁾	I _{OUT} =100mA, V _{OUT} =3.0V	--	110	--	mV	
Supply Current	I _{SS}	I _{OUT} =0	0.45	0.8	1.3	μA	
Standby Current	I _{STBY}	CE=V _{SS}	--	--	0.1	μA	
Load Regulation	ΔV _{OUT}	V _{IN} =V _{OUT} +1V 1mA≤I _{OUT} ≤100mA	--	5	--	mV	
Line Regulation	$\frac{\Delta V_{OUT}}{V_{OUT} \times \Delta V_{IN}}$	I _{OUT} =10mA V _{OUT} +1V≤V _{IN} ≤6V	--	0.03	0.2	%/V	
Temperature Coefficient	$\frac{\Delta V_{OUT}}{V_{OUT} \times \Delta T_A}$	I _{OUT} =10mA -40°C<T _A <85°C	--	50	--	ppm	
Output Current Limit	I _{LIM}	V _{OUT} =90%×V _{OUT} (V _{OUT} ≥3.0V)	350	--	--	mA	
Short Current	I _{SHORT}	V _{OUT} =V _{SS}	--	10	--	mA	
Power Supply Rejection Ratio	PSRR	I _{OUT} =50mA	100Hz	--	65	--	dB
			1kHz	--	70	--	
			10kHz	--	50	--	
			100kHz	--	35	--	
CE "High" Voltage	V _{CE} "H"	--	1.5	--	V _{IN}	V	
CE "Low" Voltage	V _{CE} "L"	--	--	--	0.3	V	
COUT Auto-Discharge Resistance	R _{DISCHRG}	V _{IN} =5V, V _{OUT} =3.0V V _{CE} =V _{SS}	--	200	--	Ω	

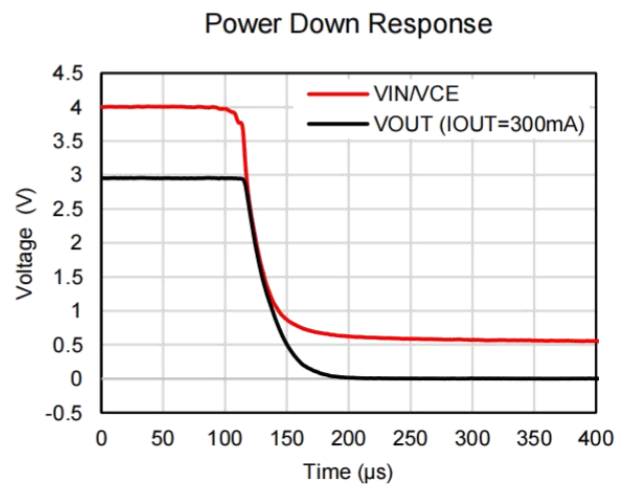
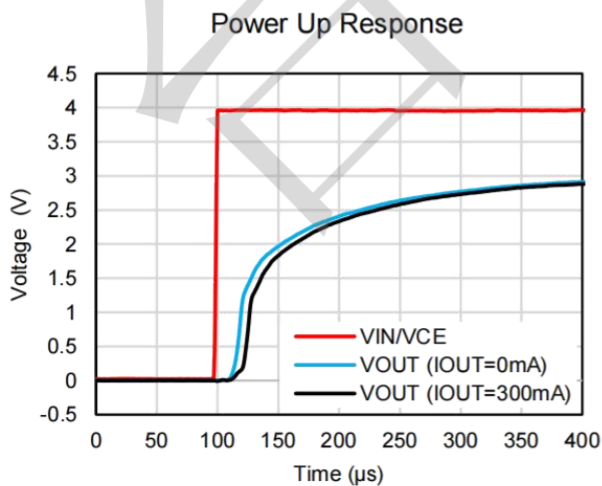
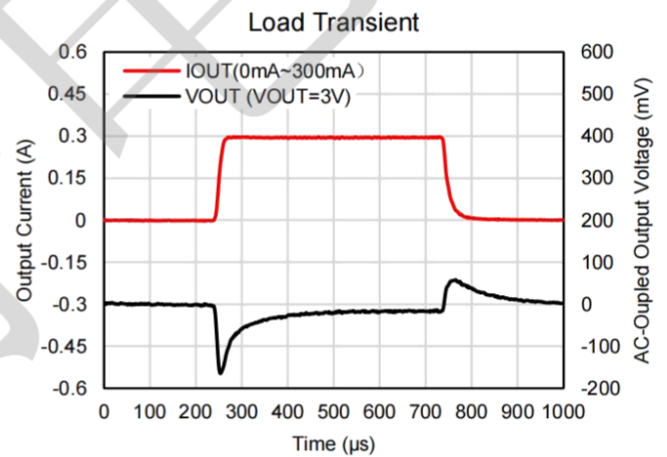
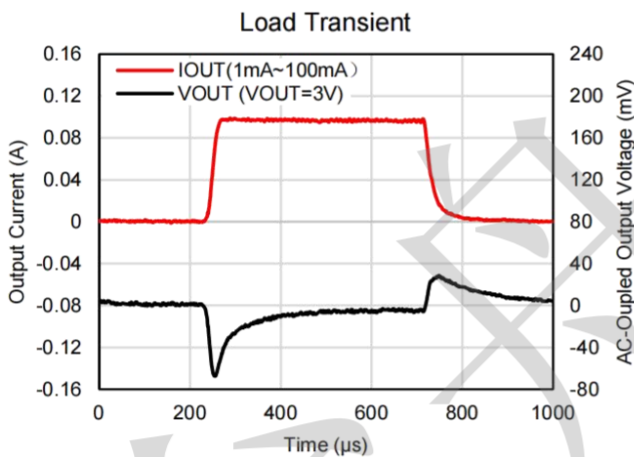
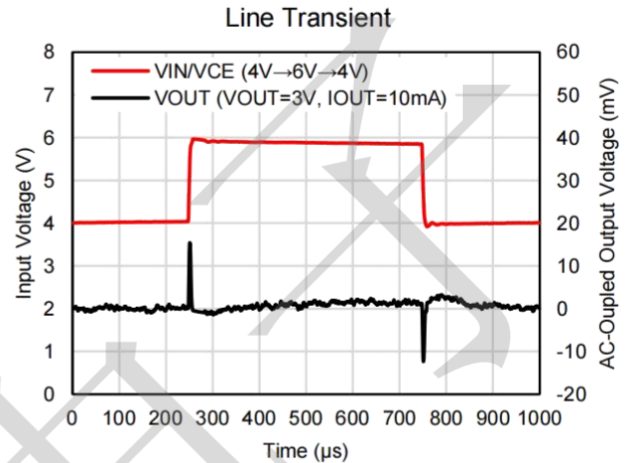
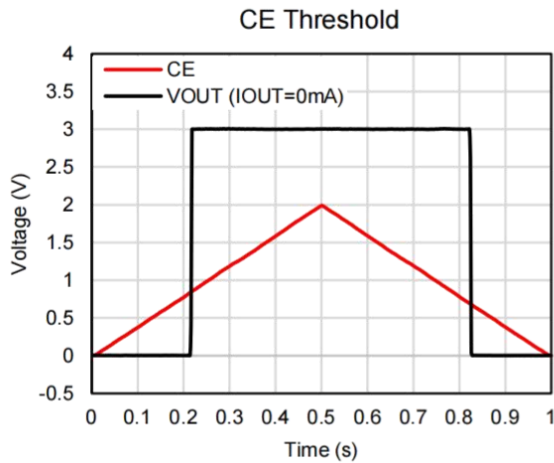
Note:

1. Test condition: the device is mounted on FR-4 substrate PC board, with minimum recommended pad layout.
2. V_{dif} :The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals To 98% Of V_{OUT} .

Typical Operating Characteristics (25 °C, unless otherwise noted)

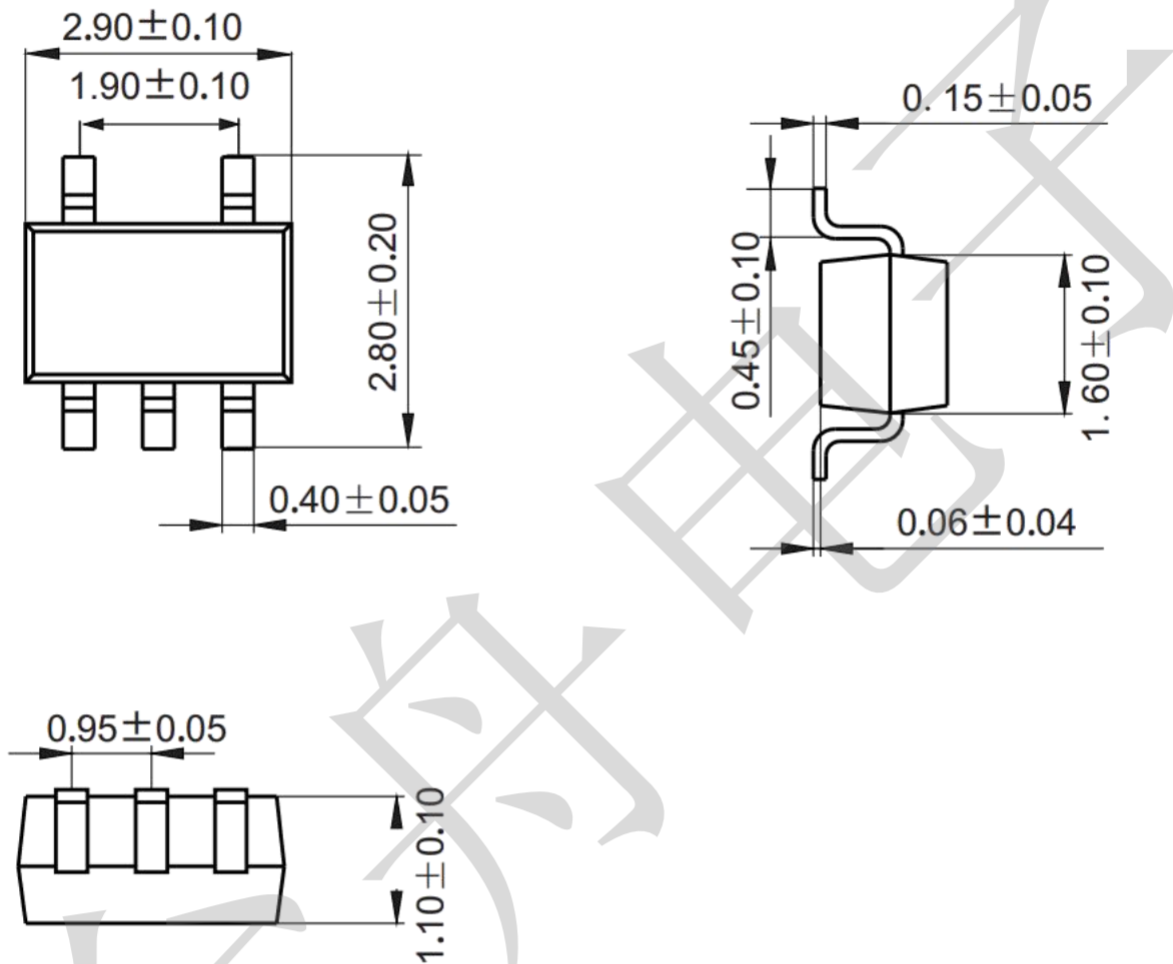


Typical Operating Characteristics (25 °C, unless otherwise noted)



Package Outline Dimensions (unit: mm)

SOT23-5



Mounting Pad Layout (unit: mm)

