
Using the ATECC508A to Perform Asymmetric Authentication of a Remote Device

Introduction

This application demonstrates the authentication of a remote device with a host (The Curiosity PIC32MZ EF Development Board and the secure click board using the cryptography module ATECC508A) by using an asymmetric authentication method, where the host verifies the signature from the remote through the public key of the remote. The application allows adding information to the configuration, using the configuration data, and key data to configure a secure click board. The application flow is realized through an interactive user interface through the serial terminal program (Tera term) interfaced through the USB of a computer.

For more information on the features and layout of the Curiosity PIC32MZ EF Development Board, refer to the [PIC32MZ EF Curiosity Development Board User's Guide](#) (DS70005282).

For more information on the features of the ATECC508A module, refer to the [Product Data Sheet](#).

1. Required Tools and Applications

The following Microchip development tools are required to run the ATECC508A Asymmetric Authentication demonstration:

- Curiosity PIC32MZ EF Development Board (DM320104), available from [Microchip Direct](#)
- Download and install latest version of [MPLAB X Integrated Development Environment \(IDE\)](#)
- Download and install the latest version of [MPLAB XC32 C/C++ Compiler](#)
- Optionally download and install the latest version of [MPLAB® Harmony Integrated Software Framework](#)

Note:

1. Using the MPLAB Harmony Integrated Software Framework will extend the functionality of this project by adding new modules, software frameworks, and libraries to the project.
2. This application project is developed on the following tools:
 - MPLAB X IDE v4.05
 - MPLAB XC32 C Compiler v1.44
 - MPLAB Harmony v2.05
 - MPLAB X IDE plug-in: MPLAB Harmony Configurator (MHC) v2.0.5.2

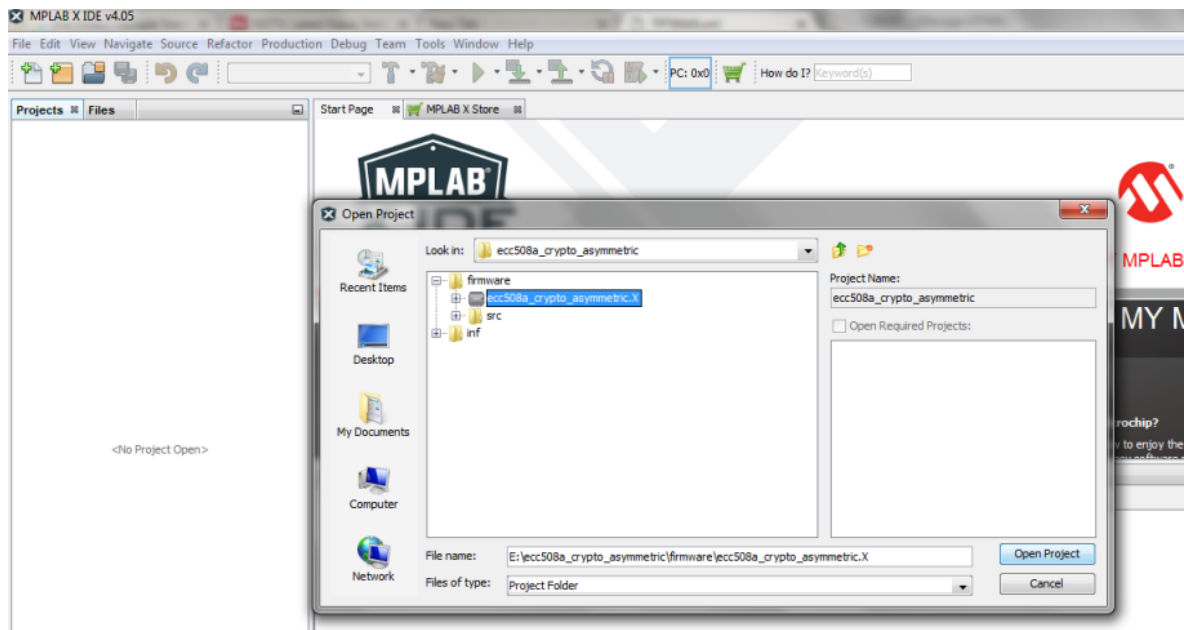
The following click board from Mikroelektronika is used: Secure click board ([MIKROE-2522](#)) – 2 Nos.

2. Building the Application

To build the application, use the following procedure:

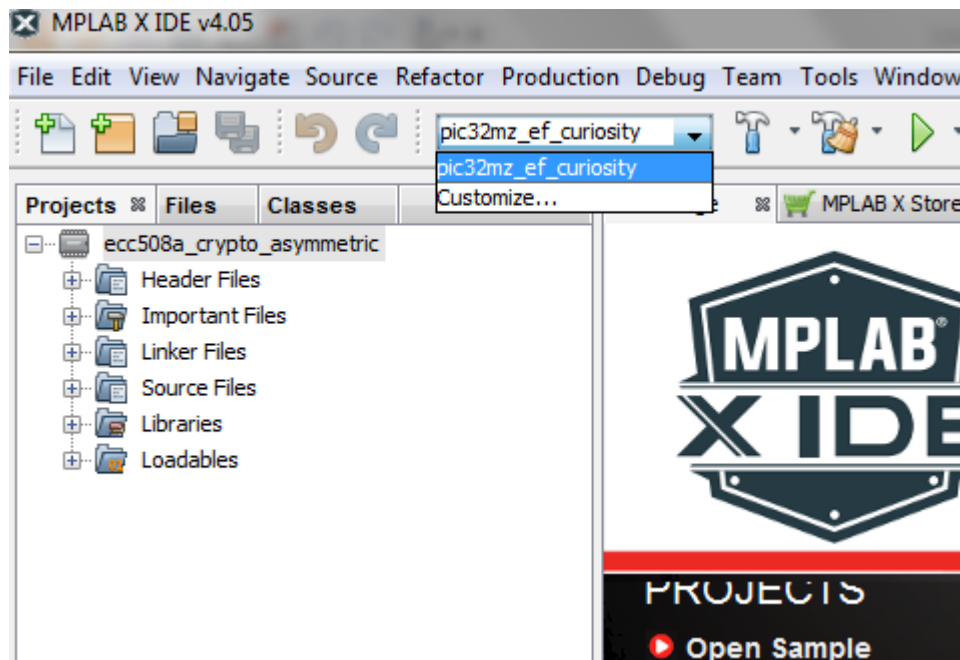
1. Download the `ecc508a_crypto_asymmetric` project to the local computer. This project can be found under the *Curiosity Demo Examples* tab at <http://www.microchip.com/Developmenttools/ProductDetails.aspx?PartNO=DM320104>.
2. To build the project, in MPLAB X, *File > Open Project* then us the `ecc508a_crypto_asymmetric.X` project from `<path-of-project-in-your-pc>/ecc508a_crypto_asymmetric/firmware` in MPLAB X IDE, as shown below.

Figure 2-1. Opening the Project



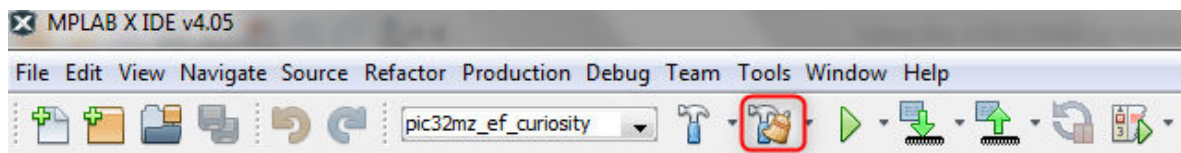
3. The project has only one configuration, `pic32mz_ef_curiosity`, for the Curiosity PIC32MZ EF Development Board. This is the default configuration when the project is open.

Figure 2-2. Choosing the Project Configuration



4. The *pic32mz_ef_curiosity* configuration sets up MPLAB X IDE to build and run the demonstration application on the Curiosity PIC32MZ EF Development Board, with the PIC32MZ2048EFM100 microcontroller.
5. The USB CDC class library is configured to enable interactive user interface control.
6. The I²C driver is configured to use the I²C1 instance of the peripheral at 100 kHz clock frequency. The application interacts with the ATECC508A (available on the Secure click board) over I²C1 to implement the host side of the application.
7. The I²C driver is configured to use the I²C2 instance of the peripheral at 100 kHz clock frequency. The application interacts with the ATECC508A (available on the Secure click board) over I²C2 to implement the remote side of the application.
8. Clean and Build the project.

Figure 2-3. Clean and Build



9. Check the Build log, at the bottom of the MPLAB X IDE interface.

Figure 2-4. Build Log

Search Results	Output
REAL ICE	ecc508a_crypto_asymmetric (Clean, Build, ...) %
<pre> "E:\Installations\Microchip\xc32\v1.44\bin\xc32-gcc.exe" -g -x c -c -mprocessor=32MZ2048EFM100 -i "E:\Installations\Microchip\xc32\v1.44\bin\xc32-gcc.exe" -g -x c -c -mprocessor=32MZ2048EFM100 -i "E:\Installations\Microchip\xc32\v1.44\bin\xc32-gcc.exe" -g -x c -c -mprocessor=32MZ2048EFM100 -i "E:\Installations\Microchip\xc32\v1.44\bin\xc32-gcc.exe" -g -x c -c -mprocessor=32MZ2048EFM100 -i "E:\Installations\Microchip\xc32\v1.44\bin\xc32-gcc.exe" -g -x c -c -mprocessor=32MZ2048EFM100 -i "E:\Installations\Microchip\xc32\v1.44\bin\\"xc32-bin2hex dist/pic32mz_ef_curiosity/production/e make[2]: Leaving directory 'E:/ecc508a_crypto_asymmetric/firmware/ecc508a_crypto_asymmetric.X' make[1]: Leaving directory 'E:/ecc508a_crypto_asymmetric/firmware/ecc508a_crypto_asymmetric.X' BUILD SUCCESSFUL (total time: 39s) Loading code from E:/ecc508a_crypto_asymmetric/firmware/ecc508a_crypto_asymmetric.X/dist/pic32mz_ Loading completed </pre>	

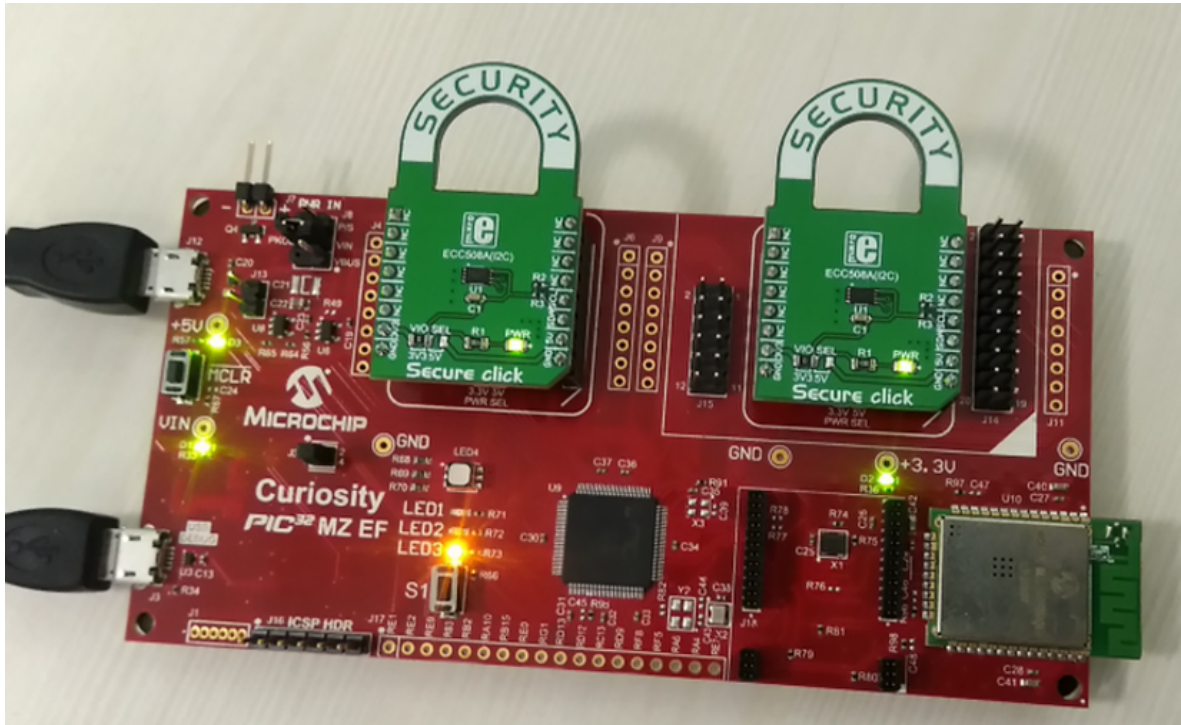
Note: Often a project will not compile on a Windows™ computer due to a limitation in the path length. The Windows operating system has a maximum path length of 260 characters. This limitation causes file paths to be truncated when attempting to compile, which leads to files not being found by the compiler. Try placing the project in the top level directory, usually C: /. For more information, refer to the Maximum Path Length Limitation section of the Naming Files, Paths, and Namespaces, which is available on the [Microsoft Developer Network site](https://docs.microsoft.com/en-us/windows/win32/fileio/naming-a-file).

3. Configuring the Hardware

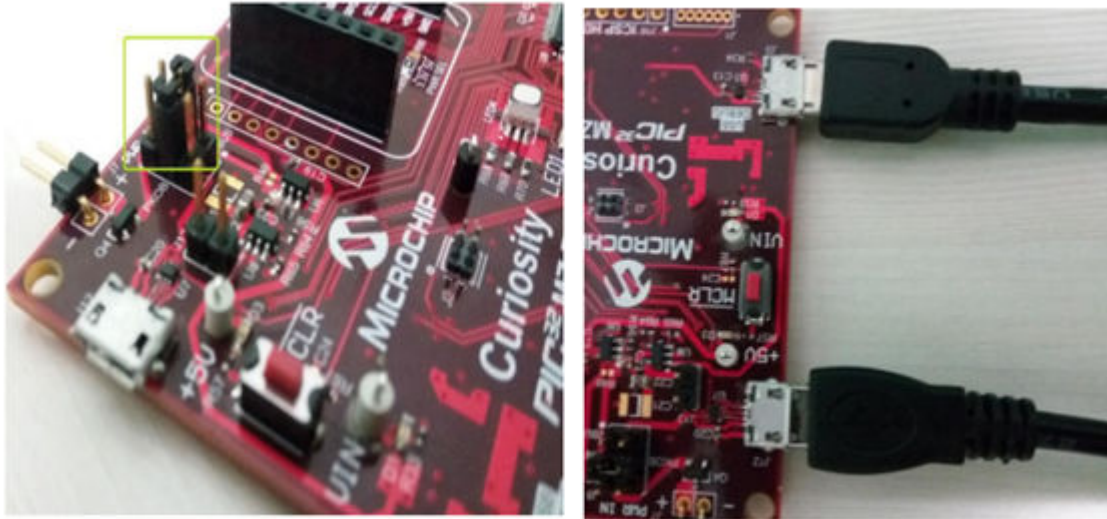
To configure the hardware, use the following steps:

1. For the host operation, mount a Secure click board on the mikroBUS socket J5.
2. For the remote device operation, mount a Secure click board on the mikroBUS socket J10.

Figure 3-1. Hardware Configuration



3. Power the Curiosity PIC32MZ EF Development Board from the host computer through a Type-A male to Micro-B USB cable connected to the Micro-B port (J3). The cable is not included with the kit. Ensure that a jumper is placed in the J8 header (between 4 and 3) to select the supply from the debug USB connector.
4. Ensure that the jumper is not present in the J13 header to use the Curiosity board in Device mode. In Device mode, the board acts as a USB device to the computer. Plug in a USB cable with a Micro-B type connector to Micro-B port (J12), and plug the other end into the computer.



For additional information on the hardware features and configurations, refer to the User's Guide:

[PIC32MZ EF Curiosity Development Board User's Guide](#) (DS70005282).

4. Running the Demonstration

This application demonstrates the use of the ATECC508A module to authenticate and verify if the device is secure or not. The authentication method used is Asymmetric.

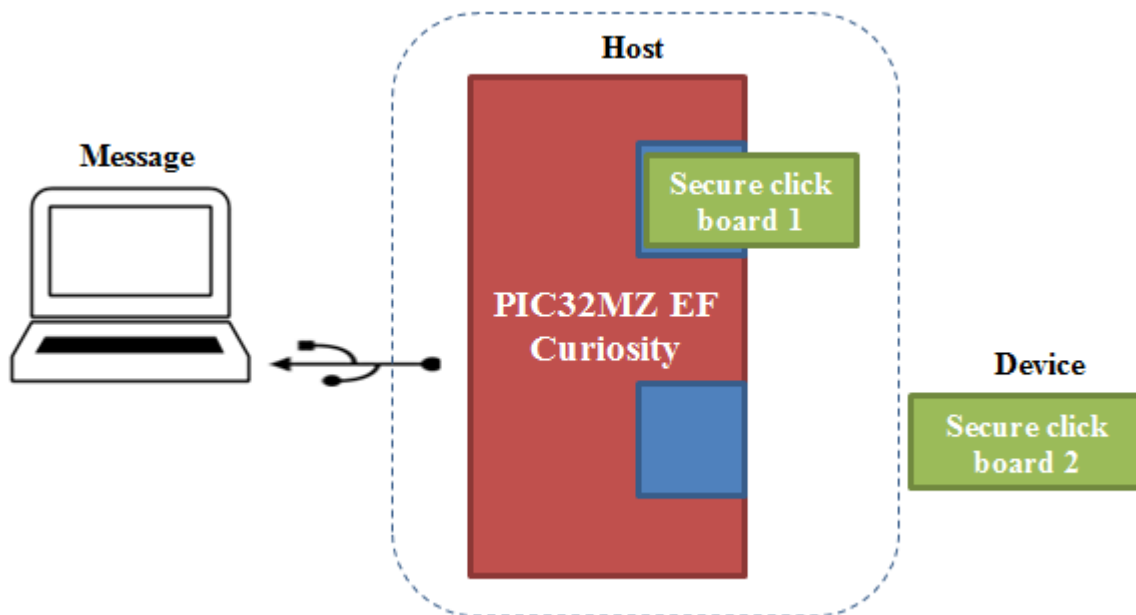
In asymmetric authentication, a verifier checks the authenticity of remote by validating the signature.

Asymmetric authentication is based on the use of two keys. One of the keys needs to be kept secret. This key is called the *Private Key*. The second key is mathematically related to the Private Key and is called the *Public Key*. The public key is openly shared. The key owner will use the Public Key to authenticate the signature.

In this application, a secure hardware key storage device (ATECC508A on a Secure click board) is used to generate a signature by the remote, and the host uses the public key of the remote, and verifies the signature.

The following figure represents the functional block diagram of the application.

Figure 4-1. Functional Block Diagram

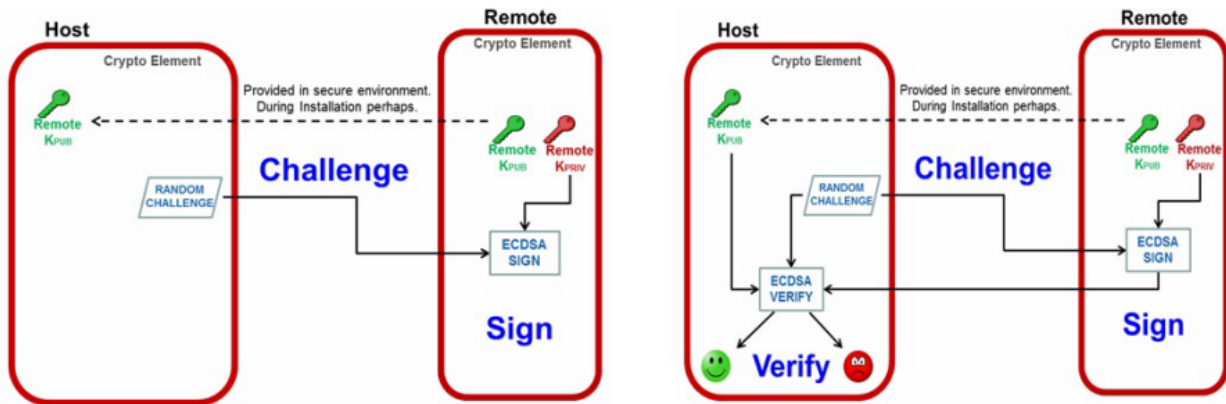


Note: The *Secure click board 1* and *Secure click board 2* house the ATECC508A module and interface with the Curiosity PIC32MZ EF Development Board microcontroller over I²C interface.

Authentication Process

The host sends a random challenge to the remote device. The remote device responds with a signature. However, the host only needs the public key from the remote (not a secret key) to verify the signature on the challenge.

Figure 4-2. Authentication Process

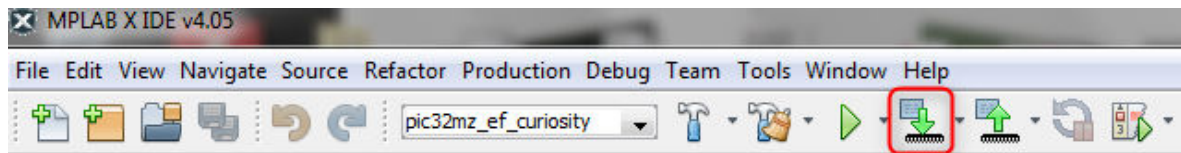


If the signature verification matches, then the remote device has successfully responded to the challenge and the host can trust the remote device.

The following steps are used to run the demonstration:

1. Open the project in MPLAB X IDE and select the `pic32mz_ef_curiosity` project configuration.
2. Build the code and program the device by clicking on the program button as shown below.

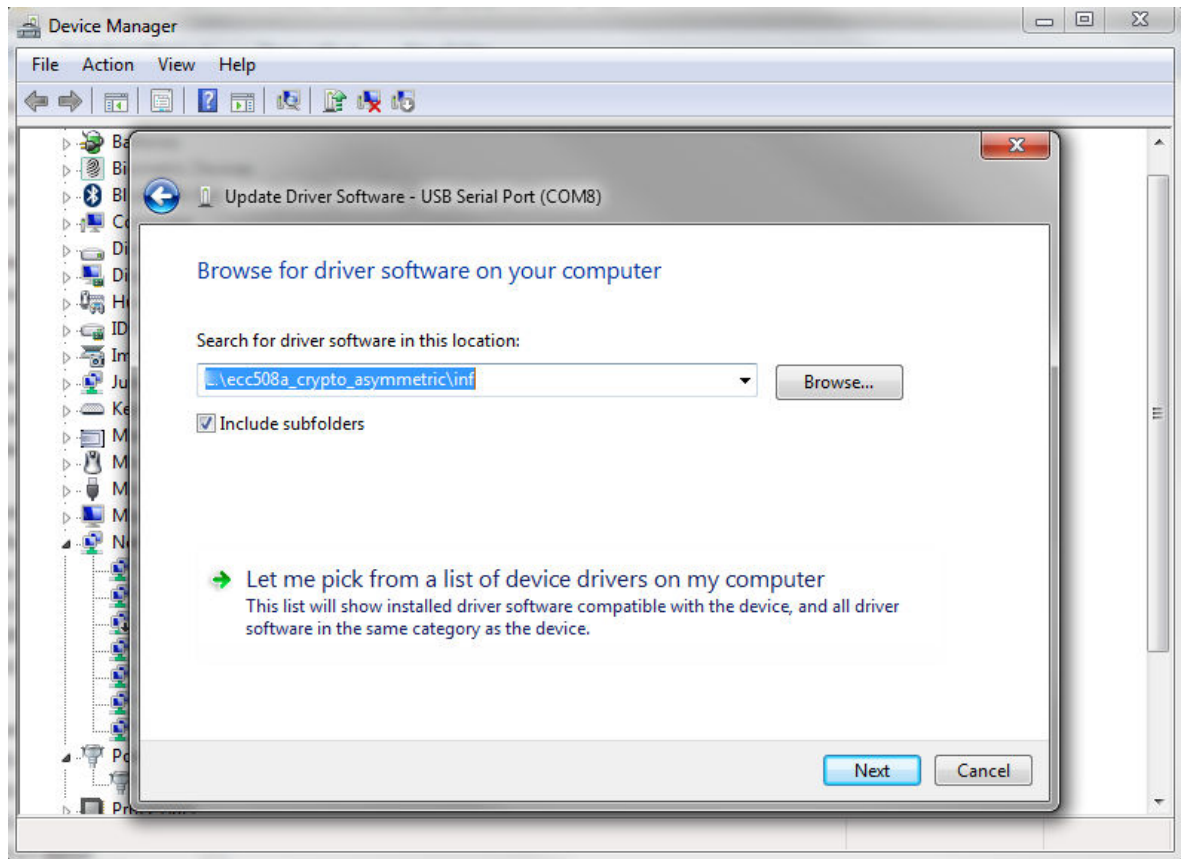
Figure 4-3. Build and Program



3. After power up, the demonstration is active. This is indicated by a yellow LED (LED3) on the board.
4. Plug in a USB cable with a Micro-B type connector to the Micro-B port (J12) of the Curiosity board, and plug the other end into the computer.
5. If this is the first time using this device with a personal computer, there may be a prompt for a `.inf` file.
6. Select the *Install from a list or specific location (Advanced)* option. Specify the path *from / ecc508a_crypto_asymmetric/inf directory*.

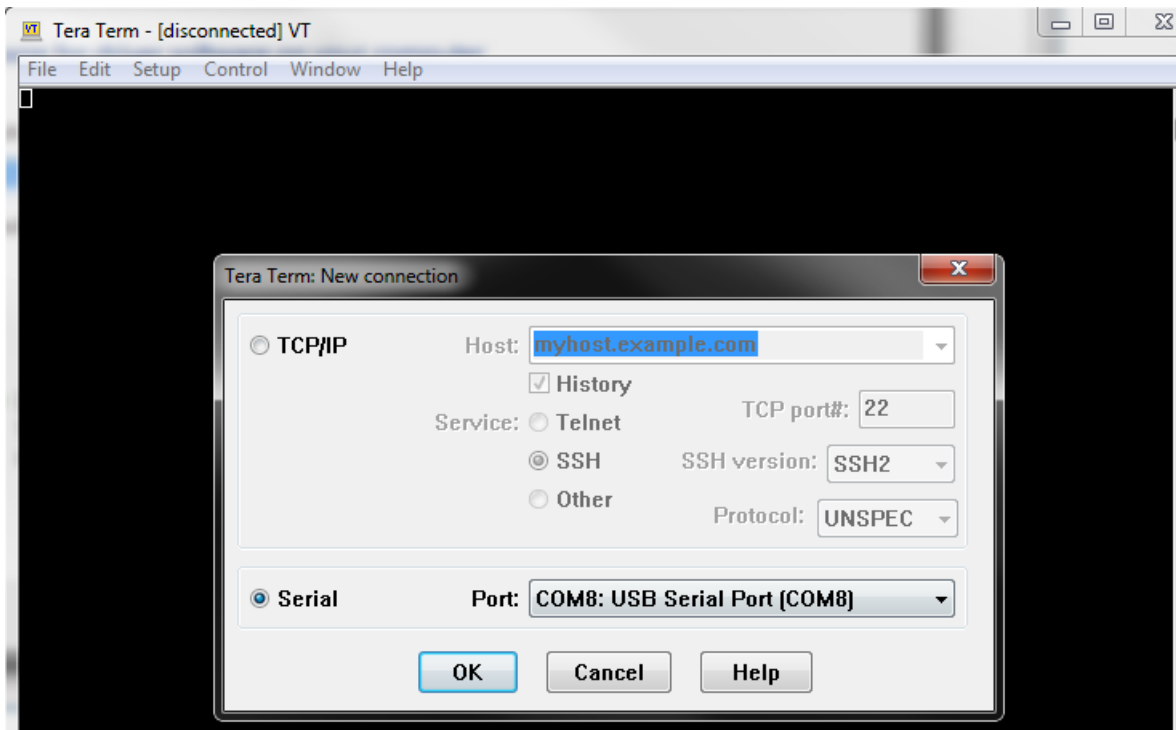
Note: Optionally, to specify the driver, open the device manager and expand the Ports (COM & LPT) tab, then right click on *Update Driver Software*.

Figure 4-4. Update Driver Software



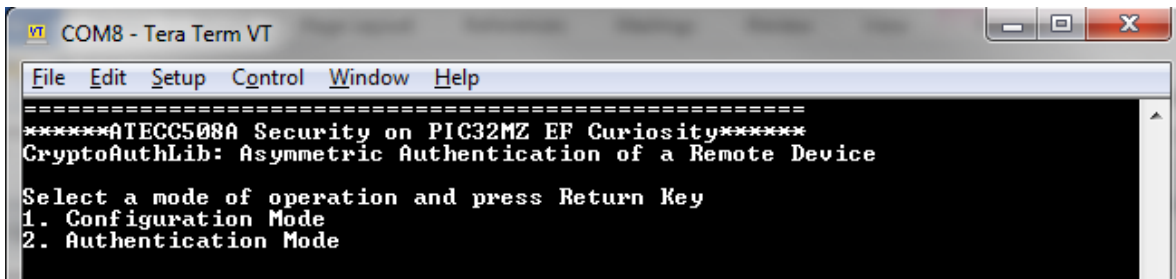
7. Once the device is installed, open a terminal program, such as *Tera Term* or *HyperTerminal*. Select the appropriate COM port for the terminal. The following figure shows the COM port selection for the Tera Term terminal program.

Figure 4-5. COM Port Selection



8. Once the Tera Term screen is displayed, Press the *Enter* key. The following modes of operation will be displayed.

Figure 4-6. Mode of Operation Menu



9. The application demonstration offers the following two modes of operation.
 - **Configuration Mode:** This mode is used to configure a blank ATECC508A module with the configuration data and keys to be stored. A blank ATECC508A device is in an unlocked state. This operation performs a lock on the Configuration Zone and Data Zones on the ATECC508A device. The locking operation is a one time operation and is irreversible.
 - **Authentication Mode:** This mode performs the secured authentication of the Remote ATECC508A device.
10. **Selecting Option 1 - Configuration Mode**
 - The display prompts the user to perform an action

Figure 4-7. Configuration Mode



- When the Secure click board is plugged-in, the application enters into Configuration Write mode, and prints the existing or default configuration

Note: The existing or default configuration may be different from what is shown in the following figure.

Figure 4-8. Writing Configuration

```
--Writing Configuration--
0xc0, 0x00, 0x55, 0x00, 0x8f, 0x8f, 0x8f, 0x8f,
0x8f, 0x8f, 0x8f, 0x42, 0x8f, 0x0f, 0xc2, 0x8f,
0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f,
0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f,
0x0f, 0x0f, 0x9f, 0x8f, 0xff, 0xff, 0xff, 0xff,
0x00, 0x00, 0x00, 0x00, 0xff, 0xff, 0xff, 0xff,
0xff, 0xff, 0xff, 0xff, 0xff, 0xff, 0xff, 0xff,
0xff, 0xff, 0xff, 0xff, 0x00, 0x00, 0x00, 0x00,
0xff, 0xff, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00,
0x1e, 0x00, 0x1e, 0x00, 0x1e, 0x00, 0x1c, 0x00,
0x13, 0x00, 0x5c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
```

- If the plugged-in board is a brand new Secure click board, the application will write the new configuration to the configuration zone and lock it. The application will display the following messages:
 - **Configuration Write Complete**
 - **Locking Configuration Zone..**
 - **Configuration Zone Lock Complete**
 - This would be followed by the writing of the new data (data slot contents and new keys) to the data zone and locking it. It would display the following messages:
 - **Writing Data Zone**
 - **Data Zone Write Complete**
 - **Locking Data Zone..**
 - **Data Zone Lock Complete**
 - ****Host board Configuration Done****
 - If the plugged-in board is already configured, the application displays the following message: ATCA already configured.
 - Once the host configuration is completed, The display prompts the user to choose an action: Plug in remote Secure Click board in Mikro Bus Interface 2 and press S1.
 - When the Secure click board is plugged-in, the application enters the configuration write mode and prints the existing or default configuration
- Note:** The existing or default configuration could be different from what is shown in the following figure.

Figure 4-9. Default Configuration

```
--Writing Configuration--
0xc0, 0x00, 0x55, 0x00, 0x8f, 0x8f, 0x8f, 0x8f,
0x8f, 0x8f, 0x8f, 0x42, 0x8f, 0x0f, 0xc2, 0x8f,
0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f,
0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f, 0x0f,
0x00, 0x00, 0x00, 0x00, 0xff, 0xff, 0xff, 0xff,
0x00, 0x00, 0x00, 0x00, 0xff, 0xff, 0xff, 0xff,
0xff, 0xff, 0xff, 0xff, 0xff, 0xff, 0xff, 0xff,
0xff, 0xff, 0xff, 0xff, 0x00, 0x00, 0x00, 0x00,
0xff, 0xff, 0x00, 0x00, 0x00, 0x00, 0x00, 0x00,
0x1e, 0x00, 0x1e, 0x00, 0x1e, 0x00, 0x1c, 0x00,
0x13, 0x00, 0x5c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00, 0x1c, 0x00,
```

- If the plugged-in board is a brand new Secure click board, the application will write the new configuration to the configuration zone and lock it. It will display the following messages:
 - **Configuration Write Complete**
 - **Locking Configuration Zone..**
 - **Configuration Zone Lock Complete**
- This would be followed by writing of the new data (data slot contents and new keys) to the data zone and locking it. It would display the following messages:
 - **Writing Data Zone**
 - **Data Zone Write Complete**
 - **Locking Data Zone..**
 - **Data Zone Lock Complete**
 - ****Remote board Configuration Done****
- If the plugged-in board is already configured, the application displays the following message: ATCA already configured.
- The application provides a user the option to return to main menu: Press a key followed by 'enter' key to return to Options Menu.

11. Selecting option 2 - Authentication Mode

- **Fail Case:**
 - By default, if the application fails to authenticate the Remote Secure click (plugged in J10), it will be indicated by a red LED (LED1) on the board.

Figure 4-10. Fail Case

```

-----In Authentication Mode-----
Random from host
0x33, 0x59, 0x7b, 0x71, 0xba, 0x75, 0xa9, 0x64,
0x11, 0x75, 0x40, 0xbc, 0x15, 0xce, 0xfb, 0x2d,
0xb1, 0x6d, 0x18, 0xd2, 0x27, 0x98, 0x91, 0xa5,
0x8f, 0x8b, 0xef, 0xfb, 0xd1, 0x8f, 0x63, 0x53,

Signature from remote
0xc0, 0x36, 0x3a, 0x8b, 0x0c, 0x87, 0x8f, 0x52,
0x90, 0x42, 0x4a, 0xc1, 0xe8, 0xd2, 0x71, 0x28,
0xca, 0xb3, 0x0f, 0x04, 0xf6, 0x9f, 0x30, 0x31,
0xff, 0xec, 0xf5, 0x5c, 0xc5, 0xe3, 0xed, 0x0e,
0x69, 0x53, 0x87, 0xc3, 0xb0, 0x4c, 0x03, 0xa2,
0x9c, 0x9a, 0x57, 0x44, 0xfc, 0x43, 0xc2, 0x72,
0x7a, 0x96, 0xd6, 0x85, 0x8e, 0xd5, 0xd7, 0x24,
0x8e, 0x36, 0xea, 0x63, 0xc0, 0xde, 0x01, 0x9c,

Remote disposable public key
0x07, 0x8c, 0xe2, 0x2c, 0x19, 0xbc, 0xf4, 0x09,
0x4c, 0x56, 0xcb, 0x9a, 0x3c, 0xa0, 0x93, 0x74,
0xc7, 0xc2, 0xd2, 0x2d, 0x15, 0x76, 0x74, 0x9d,
0xdd, 0x8a, 0x6a, 0x22, 0xae, 0x67, 0xee, 0xc3,
0x97, 0x90, 0x73, 0xa2, 0x39, 0x72, 0x68, 0x30,
0x7e, 0xdf, 0x21, 0xa0, 0x37, 0xad, 0xcc, 0x2d,
0x7a, 0x0b, 0xa5, 0xd6, 0xac, 0x64, 0xab, 0x01,
0x00, 0xbc, 0x8b, 0xe5, 0x8a, 0xa1, 0x1e, 0xba,

Remote's Public Key not found in Host's Database
Signature not verified - Authentication Failure!

```

- The Remote Secure click fails to authenticate when the host does not have the public key for the remote in its database. The host will verify only those remote devices whose public key it possesses.

```

if(key_found)
{
    status = atcab_verify_extern((const uint8_t*)&nonce,
                                (const uint8_t*)&signature,
                                (const uint8_t*)key_store[i].pub_key,
                                &verify);
}
else
{
    SYS_PRINT("Remote's Public Key not found in Host's Database \r\n\r\n");
}

```

- *Pass Case:* To verify the signature of the remote, place the Disposable Public Key of the remote in the database of the host. Copy the Remote Disposable Public Key from the previous figure and paste the key into the array `key_store[]` in `ecc508a_crypto_asymmetric_app.c` file as shown in the following figure.

Figure 4-11. Pass Case

```

asymm_public_key_t key_store[4] =
{
    0x07, 0x8c, 0xe2, 0x2c, 0x19, 0xbc, 0xf4, 0x09,
    0x4c, 0x56, 0xcb, 0x9a, 0x3c, 0xa0, 0x93, 0x74,
    0xc7, 0xc2, 0xd2, 0x2d, 0x15, 0x76, 0x74, 0x9d,
    0xdd, 0x8a, 0x6a, 0x22, 0xae, 0x67, 0xee, 0xc3,
    0x97, 0x90, 0x73, 0xa2, 0x39, 0x72, 0x68, 0x30,
    0x7e, 0xdf, 0x21, 0xa0, 0x37, 0xad, 0xcc, 0x2d,
    0x7a, 0x0b, 0xa5, 0xd6, 0xac, 0x64, 0xab, 0x01,
    0x00, 0xbc, 0x8b, 0xe5, 0x8a, 0xa1, 0x1e, 0xba,
}; //cut and paste in remote public key

```

- Build and program the code. Repeat the user actions to select Authentication mode. The application now passes to verify the signature from the Remote Secure click (plugged in J10). This is indicated by a green LED (LED2) on the board.

Figure 4-12. Authentication Verification

```

-----In Authentication Mode-----
Random from host
0xe4, 0x46, 0x63, 0x22, 0xd3, 0x2a, 0x88, 0x40,
0x69, 0xef, 0xdb, 0x0d, 0x42, 0xd0, 0x01, 0x53,
0xfb, 0x1a, 0x61, 0xd0, 0x1a, 0x08, 0xcc, 0x72,
0x81, 0xdd, 0x4d, 0x64, 0x36, 0x05, 0xa8, 0x86,

Signature from remote
0x3e, 0x73, 0x77, 0x25, 0xb3, 0xc8, 0x0d, 0xe7,
0xb1, 0xe0, 0xb4, 0xb5, 0xca, 0x11, 0x53, 0xe9,
0x99, 0xf9, 0xc5, 0x33, 0x64, 0x8c, 0xb2, 0x3a,
0x12, 0xc2, 0xc7, 0x67, 0xa8, 0xb7, 0x13, 0x50,
0xf9, 0x2c, 0x7f, 0xaf, 0x8d, 0x95, 0x9c, 0x42,
0xca, 0xde, 0x1f, 0xb4, 0x92, 0x9b, 0x86, 0x61,
0x8f, 0xb3, 0x6c, 0x8b, 0x93, 0xc0, 0x6a, 0x9a,
0xb9, 0x0e, 0x96, 0x8f, 0x81, 0xf2, 0x52, 0xca,

Remote disposable public key
0x07, 0x8c, 0xe2, 0x2c, 0x19, 0xbc, 0xf4, 0x09,
0x4c, 0x56, 0xcb, 0x9a, 0x3c, 0xa0, 0x93, 0x74,
0xc7, 0xc2, 0xd2, 0x2d, 0x15, 0x76, 0x74, 0x9d,
0xdd, 0x8a, 0x6a, 0x22, 0xae, 0x67, 0xee, 0xc3,
0x97, 0x90, 0x73, 0xa2, 0x39, 0x72, 0x68, 0x30,
0x7e, 0xdf, 0x21, 0xa0, 0x37, 0xad, 0xcc, 0x2d,
0x7a, 0x0b, 0xa5, 0xd6, 0xac, 0x64, 0xab, 0x01,
0x00, 0xbc, 0x8b, 0xe5, 0x8a, 0xa1, 0x1e, 0xba,

Verified Signature - Authentication Successful!

```

- The signature of the Remote Secure click is successfully verified as the remote had generated the signature using its private key and the random challenge from the host, while the host verified the signature by authenticating it against the public key of the remote.
- The application provides the following option to return to the main menu: Press a key followed by 'enter' key to return to Options Menu.

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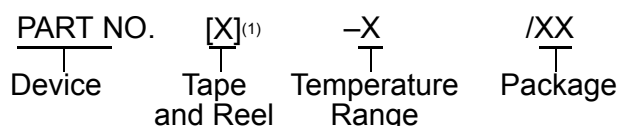
- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

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To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.



Device:	Device A, Feature A, (Package A) Device B, Feature B, (Package B)	
Tape & Reel Option:	Blank	= Tube
	T	= Tape & Reel
Temperature Range:	I	= -40°C to +85°C (Industrial)
	E	= -40°C to +125°C (Extended)
Package:	AA	= Package AA
	BB	= Package BB

Examples:

- MCPXXXXXAT-E/AA: Tape and Reel, Extended temperature, XAA package
- MCPXXXXXBT-E/BB: Tape and Reel Extended temperature, XBB package

Note:

1. Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.
2. Small form-factor packaging options may be available. Please check <http://www.microchip.com/packaging> for small-form factor package availability, or contact your local Sales Office.

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