

SAM 4E Series Family Silicon Errata and Data Sheet Clarification

The SAM 4E Series family of devices that you have received conform functionally to the current Device Data Sheet (11157H-ATARM-SAM4E16-SAM4E8-Data-sheet_31-Mar-16), except for the anomalies described in this document.

New Silicon Errata Issues

Note: This document provides information on new errata issues for the SAM 4E Series of devices. Please refer to the current device data sheet for all pre-existing silicon errata issues.

1. Module: FPU

The FPIXC (Masked Floating-Point Inexact Exception) signal is not used.

Work around

None.

Affected Silicon Revisions

A	B						
	X						

SAM 4E SERIES

Data Sheet Clarifications

In the current device data sheet, the SAM4E Chip ID Registers table (Table 33-1) was incorrect. The correct information is provided in the following table.

TABLE 1: SAM4E CHIP ID REGISTERS

Chip Name	CHIPID_CIDR	CHIPID_EXID
SAM4E16EA	0xA3CC_0CE0	0x0012_0200
SAM4E8EA	0xA3CC_0CE0	0x0012_0208
SAM4E16CA	0xA3CC_0CE0	0x0012_0201
SAM4E8CA	0xA3CC_0CE0	0x0012_0209
SAM4E16EB	0xA3CC_0CE1	0x0012_0200
SAM4E8EB	0xA3CC_0CE1	0x0012_0208
SAM4E16CB	0xA3CC_0CE1	0x0012_0201
SAM4E8CB	0xA3CC_0CE1	0x0012_0209

APPENDIX A: REVISION HISTORY

Rev A Document (1/2018)

Initial release of this document.

SAM 4E SERIES

NOTES:

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