
Protection against Pulses Exceeding the Rated Tolerance

ATA6823C/ATA6824C

Scope of Document

This document provides additional information designed to protect Atmel® ATA6823C/ATA6824C integrated circuits from exceeding the maximum pulse rating.

Preface

Pulses exceeding the maximum rating cause damage to integrated circuits. Some pins are subject to greater risk when deployed in an automotive environment.

The pins involved:

- The VRES charge pump pin
- The S1 and S2 sense pins

1.1.3 The S1 and S2 Sense Pins

The maximum ratings for the S1 and S2 sense pins are mandatory. Voltages exceeding this will lead to destruction. To reduce energy into S1 and S2, serial resistances of a few ohms are sufficient. In the event of an additional sense resistor, the cross conduction timer needs to be adapted.

For further details please refer to the Atmel® datasheets ATA6823C/ATA6824C or contact info@atmel.com

