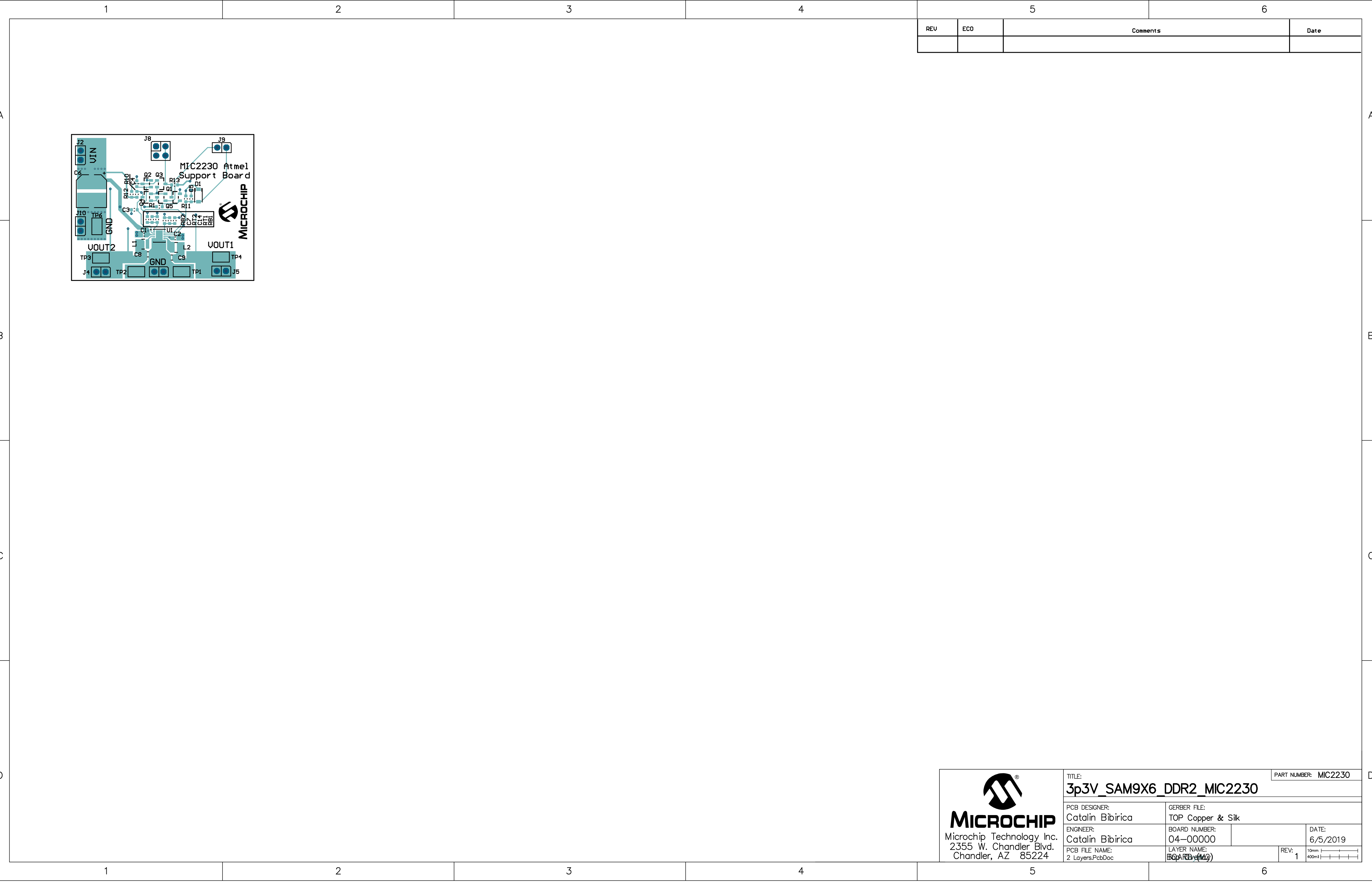
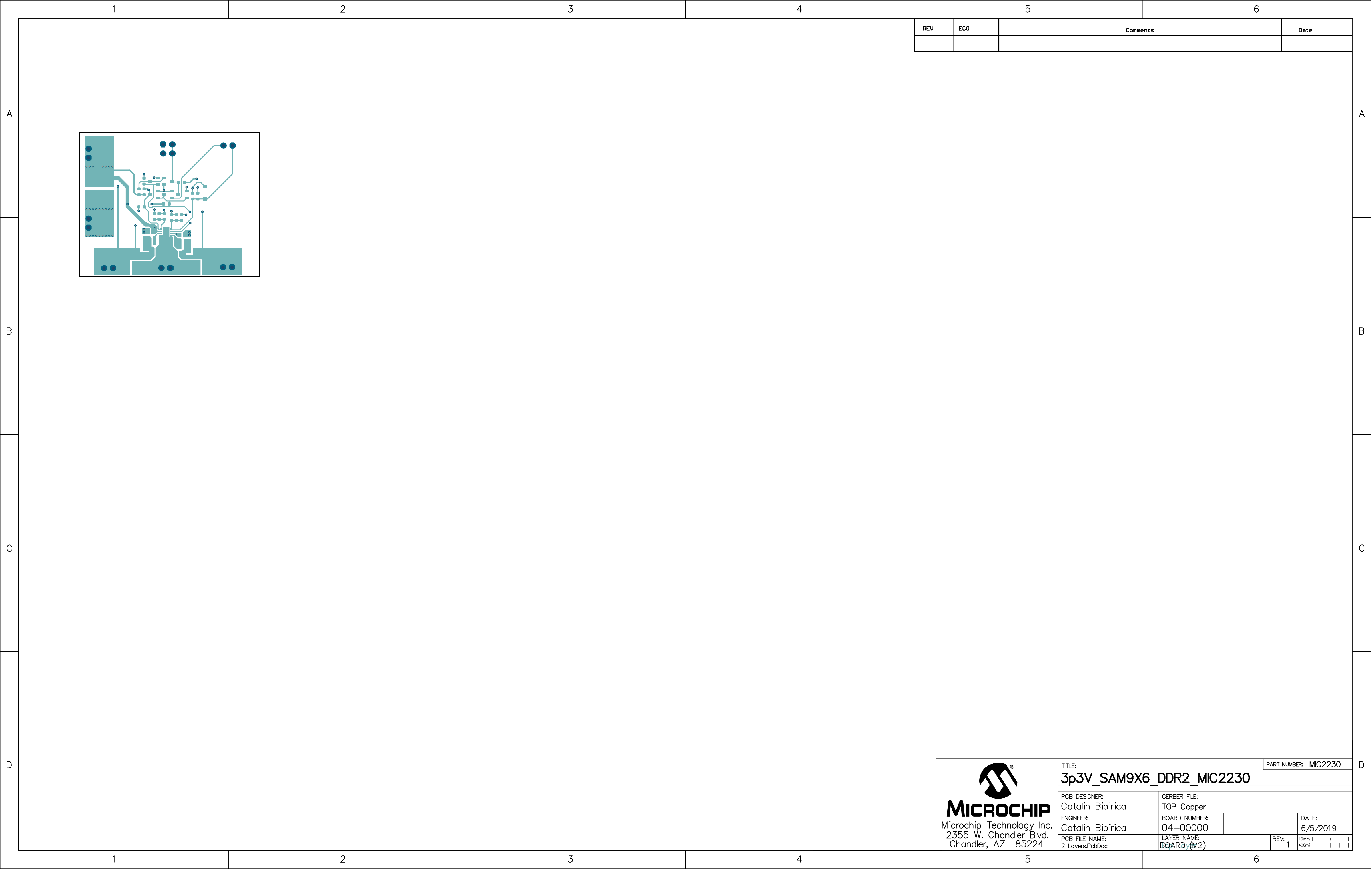


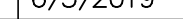


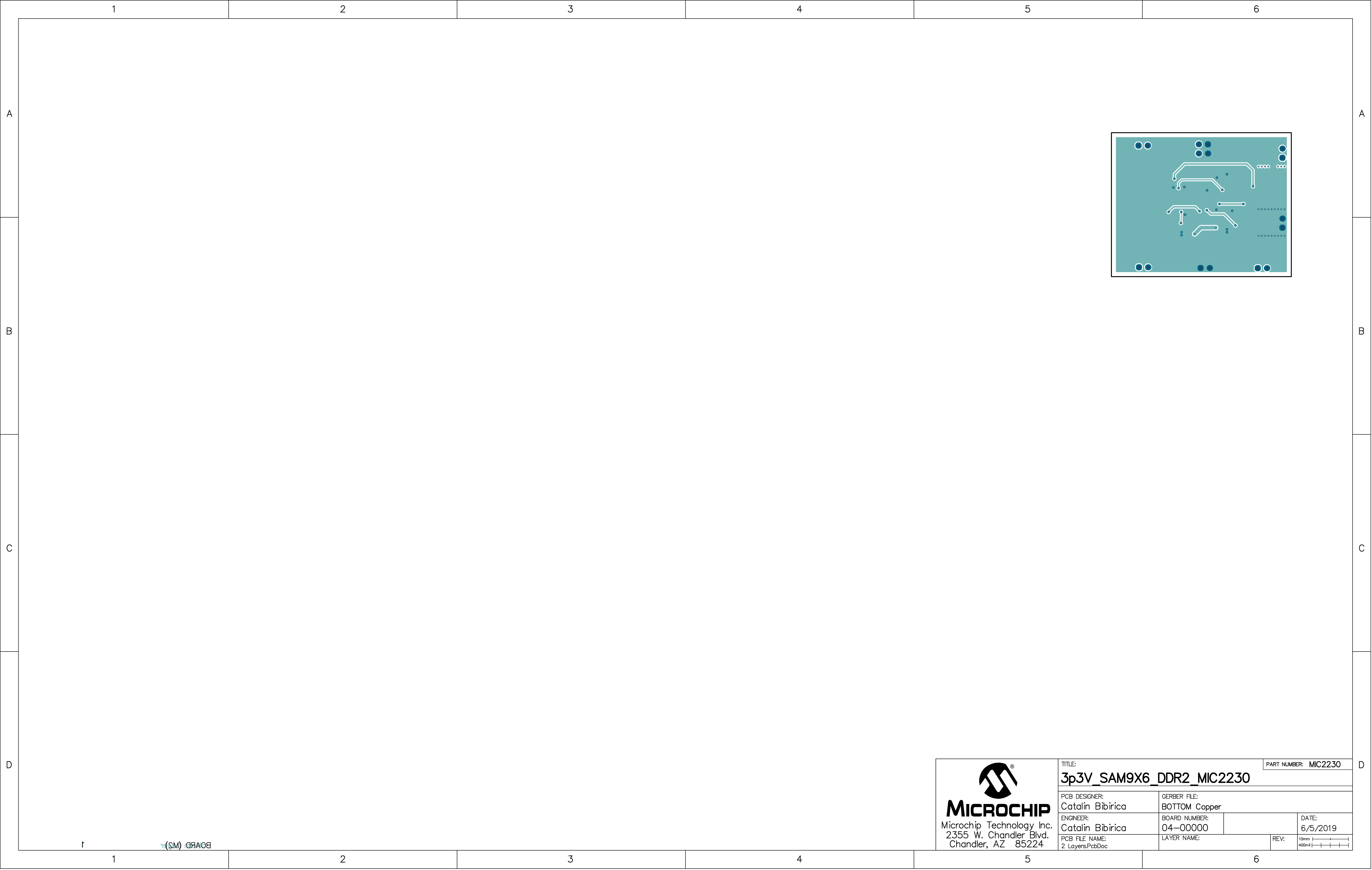


REV	ECO	Comments	Date



MICROCHIP
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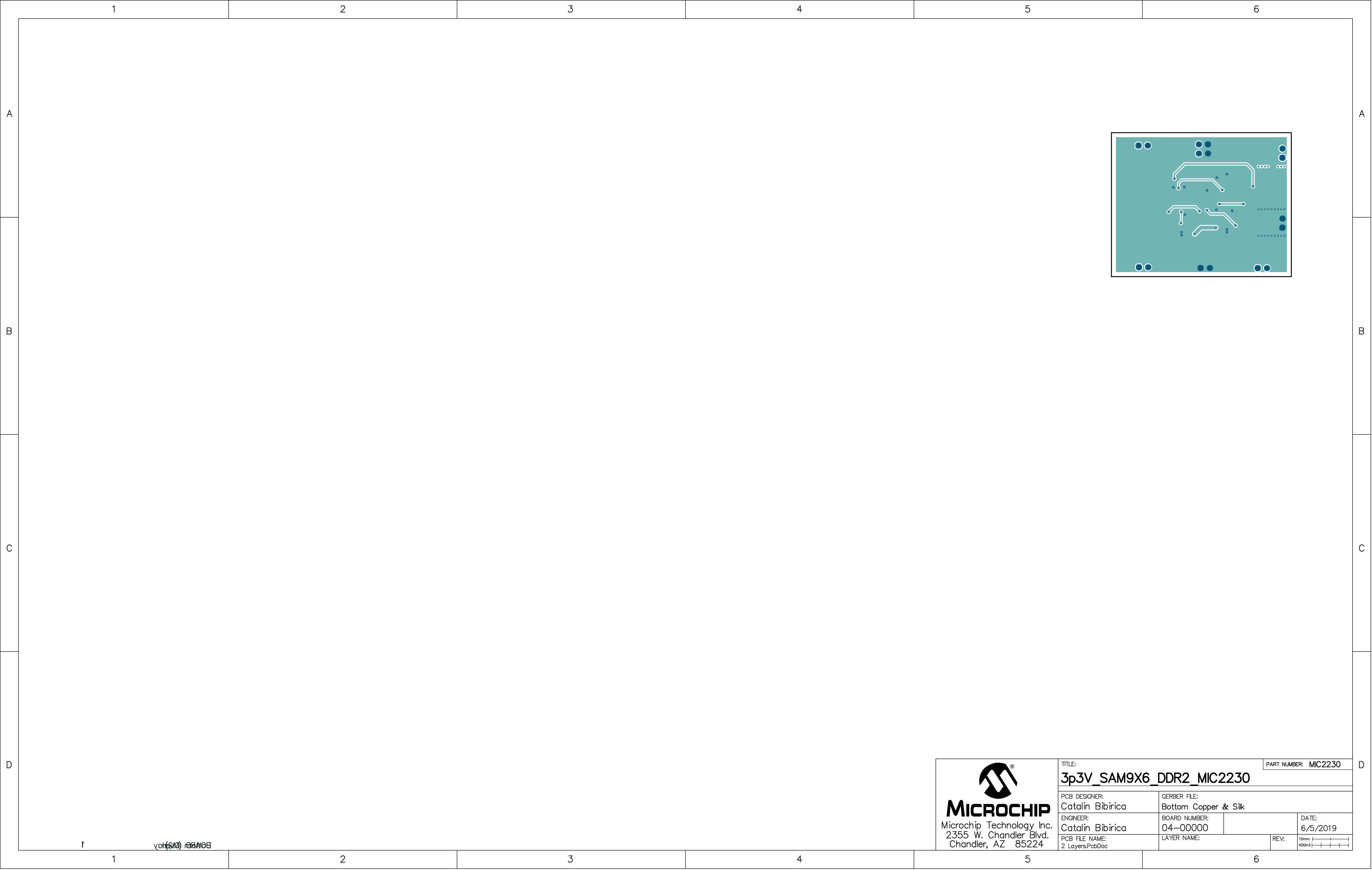
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ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME: BOARD (M2)	REV: 1 





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TITLE:		PART NUMBER: MIC2230	
3p3V_SAM9X6_DDR2_MIC2230			
PCB DESIGNER: Catalin Bibirica		GERBER FILE: BOTTOM Copper	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME:	REV: 10mm 400mil

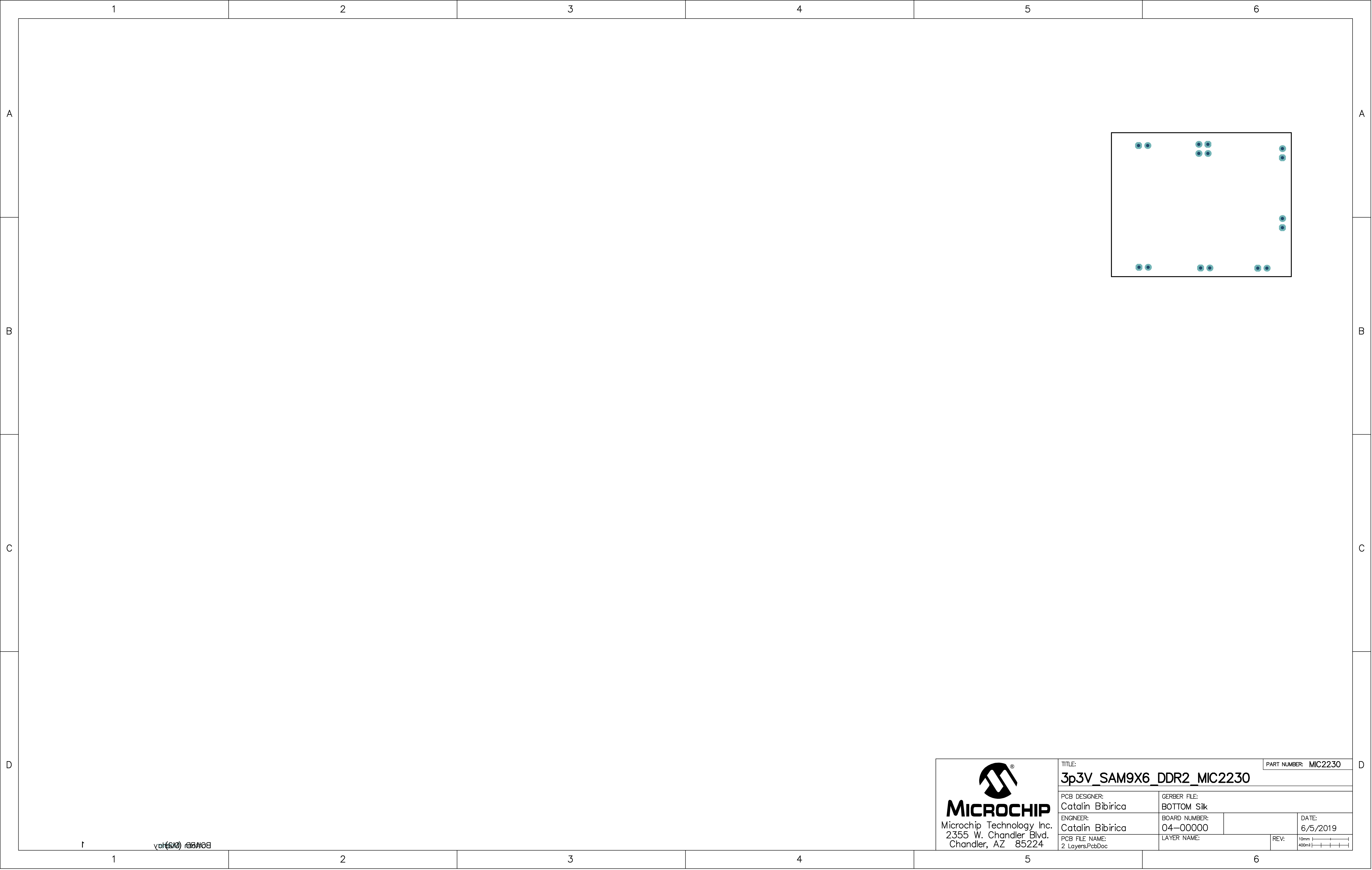


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TITLE:		PART NUMBER: MIC2230	
3p3V_SAM9X6_DDR2_MIC2230			
PCB DESIGNER: Catalin Bibirica		GERBER FILE: Bottom Copper & Silk	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
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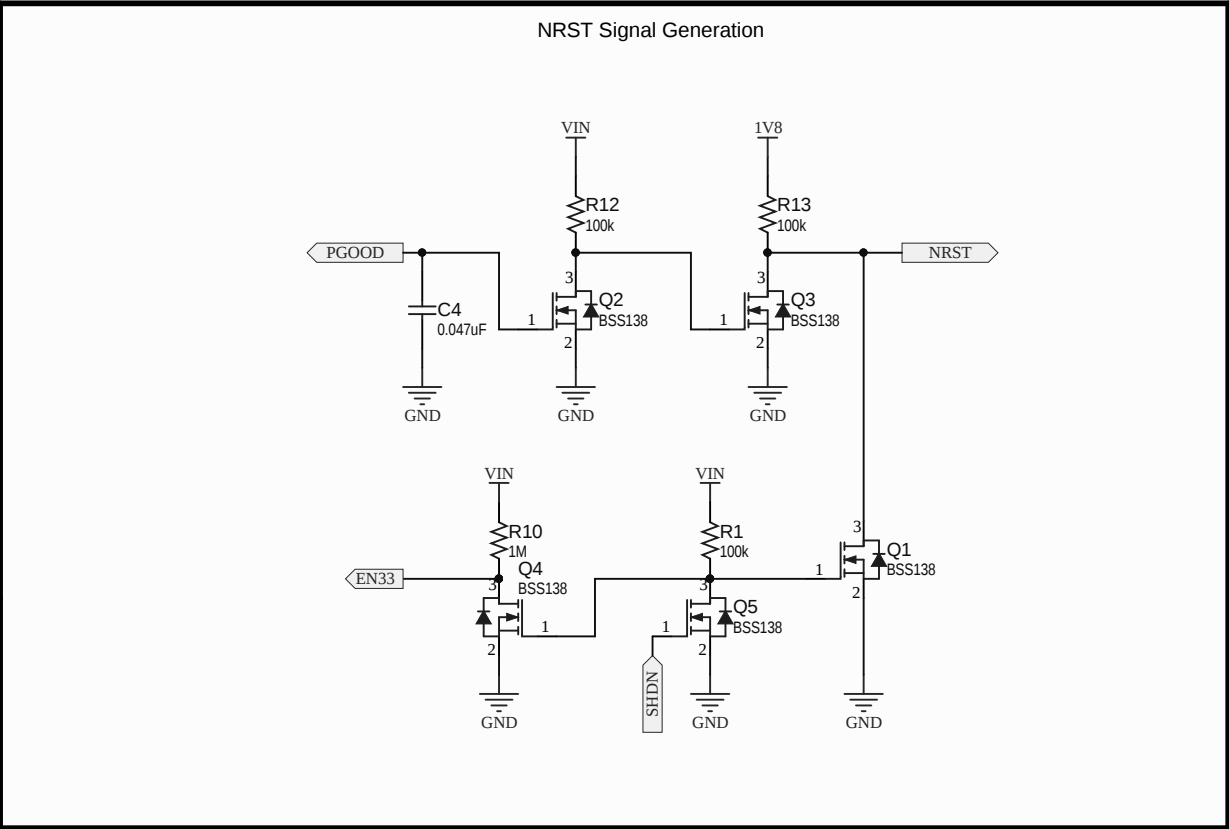
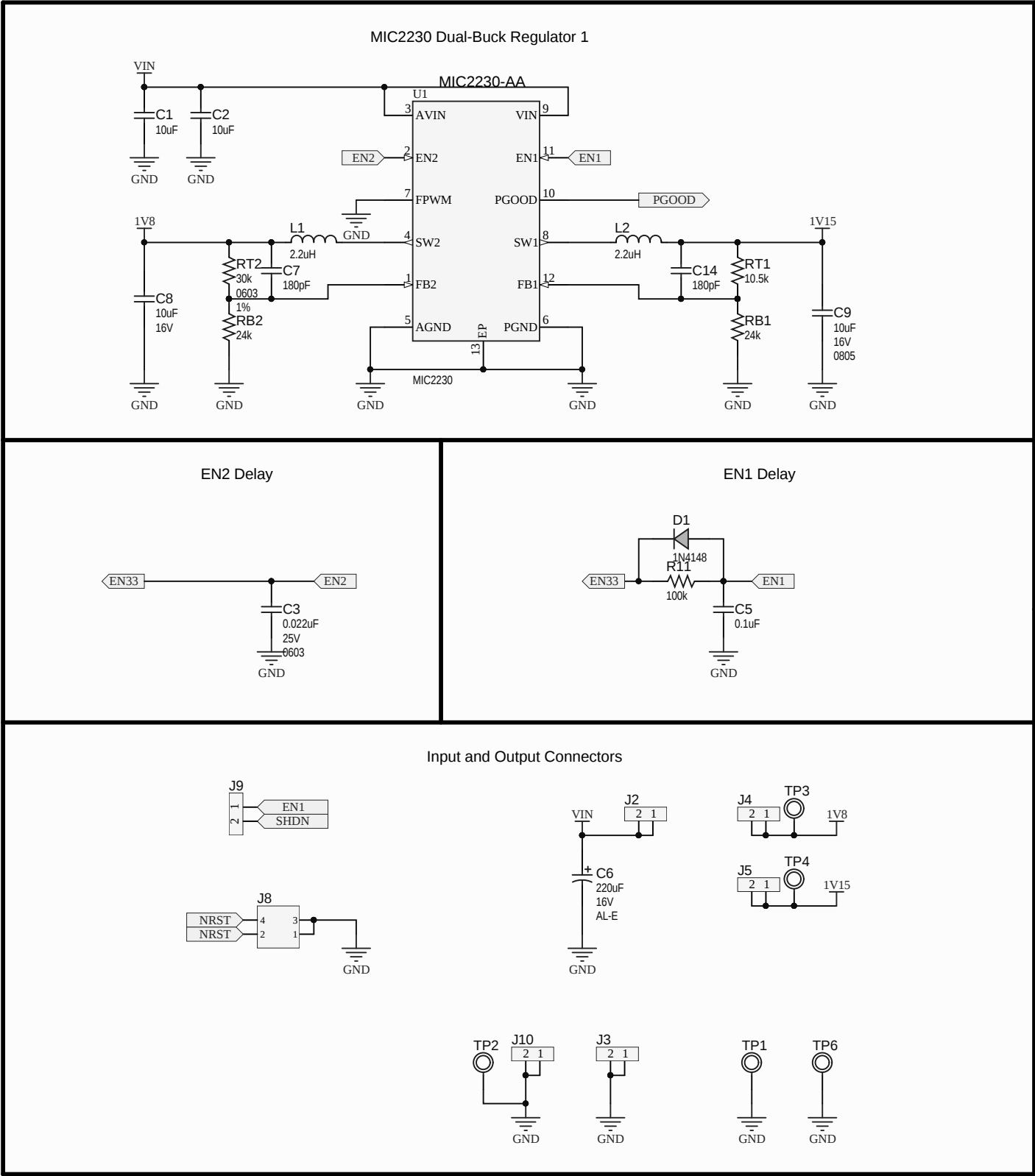


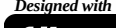


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TITLE: 3p3V_SAM9X6_DDR2_MIC2230		PART NUMBER: MIC2230	
PCB DESIGNER: Catalin Bibirica		GERBER FILE: BOTTOM Silk	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04-00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME:	REV: 10mm 400mil

REV	ECO#	DESCRIPTION	DATE



Drawn By: Catalin Bibirica		 MICROCHIP
Engineer: Catalin Bibirica		
PartNumber: MIC2230	Project Title 3p3V_SAM9X6_DDR2_MIC2230	
Sheet Title 3p3V_SAM9X6_DDR2_MIC2230		
Size B	Sch #: #03-00000	Date: 6/5/2019 10:12:17 AM
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File: B_Microchip.SchDoc		Designed with  Altium.com