
Indoor Air Quality Monitor: Firmware Creation Using Atmel START and MPLAB® Code Configurator (MCC)

Introduction

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The indoor Air Quality Monitor (AQM) is implemented using the AVR-IoT WG development board from Microchip, and sensors and several click boards from MikroElektronika. The AVR-IoT WG development board is equipped with Microchip's ATmega4808 microcontroller (AVR® MCU), ATECC608A CryptoAuthentication™ secure element, and ATWINC1510 Wi-Fi® module.

The AQM monitors the humidity and temperature along with main airborne contaminants such as particulate matter (PM_{2.5}), Carbon dioxide (CO₂) and total volatile organic components (TVOC). The microcontroller processes these acquired readings and calculates the Air Quality Index (AQI) from the readings of the PM_{2.5} sensor. The AQI and other acquired air quality parameters are stored on external EEPROM, displayed on the OLED, and uploaded to Google Cloud.

AQM Documentation Overview

The following documents cover the AQM system documentation.

1. This application note covers the usage of AVR-IoT stack (AVR-IoT WG source code) for the AQM application. Additionally, it covers configuration details of microcontroller peripherals and click boards, using Atmel START and MCC. The indoor AQM application is created on top of the AVR-IoT WG source code.
2. The [“AN3403 - Indoor Air Quality Monitor: Concept and Implementation”](#) application note describes the hardware and firmware overview of the AQM system and its power considerations.
3. The [“Indoor Air Quality Monitor: User Guide”](#) covers hardware setup, hardware connections, operating steps, LED indications, provisioning of the AVR-IoT WG board's Wi-Fi module, and visualization of AQM Data over the cloud.

It is recommended to read the [“AN3403 - Indoor Air Quality Monitor: Concept and Implementation”](#) application note before reading this firmware creation guide.

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1. Indoor Air Quality Monitor Firmware Creation Using Atmel START

This section covers the list of steps to generate AVR-IoT WG source code with Atmel START, and the process to create indoor AQM application firmware.

Table 1-1 lists the software tools and versions used for the creation of the firmware using Atmel START. It is recommended to use tools of the mentioned or higher versions.

Table 1-1. Software Tools and Versions

Software Tool	Version
Atmel Studio 7	v7.0.2397
Atmel START	v1.7.279
ATmega_DFP	v1.4.331
AVR GCC	v5.4.0
avr8-gnu	v3.6.2.1778

1.1 Generating AVR-IoT WG Sensor Node Example Project

- Open Atmel Studio. Go to *File* → *New* → *Atmel START Example Project*.
- Wait until the *BROWSE EXISTING EXAMPLE* page loads.



Tip: Maximize the page for better experience.

- Search and select **AVR-IoT WG Sensor Node** example. Then click **OPEN SELECTED EXAMPLE**.

Figure 1-1. AVR-IoT WG Sensor Node Project

Atmel | START What's New | Help

BROWSE EXISTING EXAMPLES

Atmel START examples are a great starting point for embedded programmers. Example projects will work "out of the box" but are also easily to modify using Atmel START.

Example projects are tailored for each compatible board and device, resulting in high quality production ready code with a small memory footprint. Read the available user guides to get more information about each example.

Looking for older examples? [Atmel Software Framework 3](#) contains previous releases of non-configurable examples. Click File - New - Example Project menu in Atmel Studio to see the ASF3 examples.

Search: Category: Board:

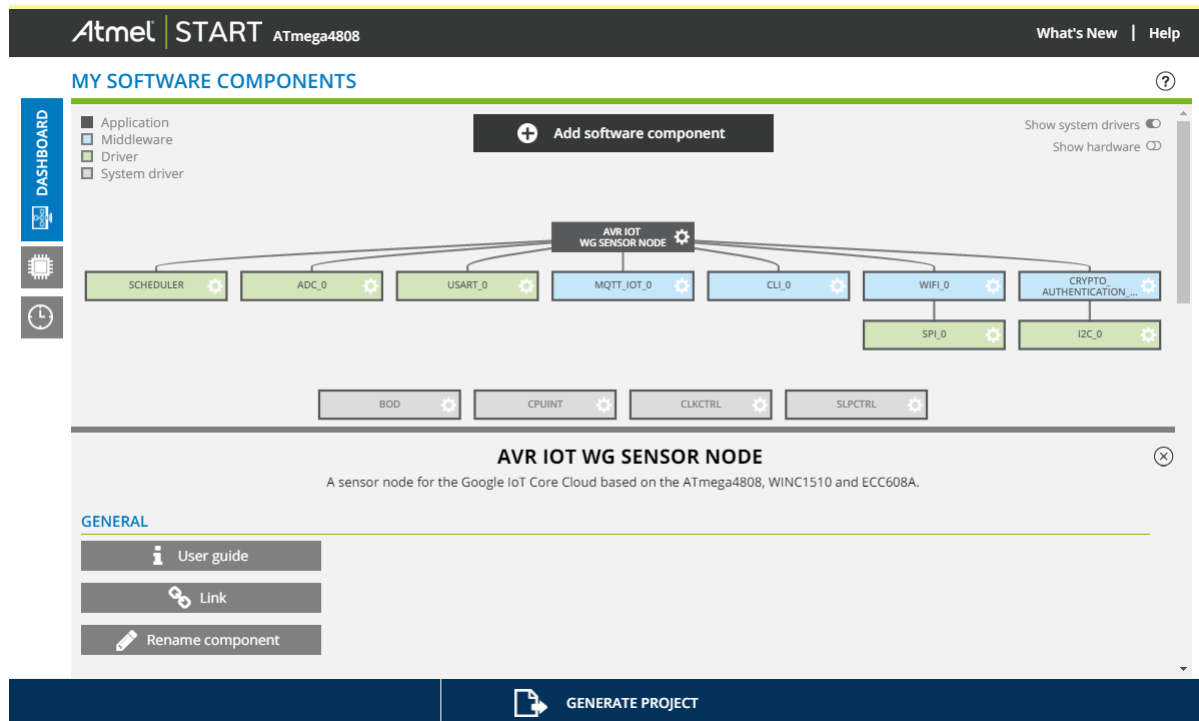
Name	Categories	Description	Board(s) supported	User guide
AVR IoT Alibaba Cloud Sensor Node		A sensor node for the Alibaba IoT Cloud based on the ATmega4808, WINC1510 and ECC608A.	ATmega4808 AVR IoT WG	User guide
AVR IoT WG Sensor Node		A sensor node for the Google IoT Core Cloud based on the ATmega4808, WINC1510 and ECC608A.	ATmega4808 AVR IoT WG	User guide
AVR IoT WG Sensor Node with AirQuality Click		A sensor node for the Google IoT Core Cloud based on the ATmega4808, WINC1510 and ECC608A.	ATmega4808 AVR IoT WG	User guide
AVR IoT WG Sensor Node with Weather Click		A sensor node for the Google IoT Core Cloud based on the ATmega4808, WINC1510 and ECC608A.	ATmega4808 AVR IoT WG	User guide

Showing 4 of 309 examples.

OPEN SELECTED EXAMPLE

- The *Dashboard* view of the microcontroller peripherals and its driver's configuration window opens.

Figure 1-2. Dashboard View



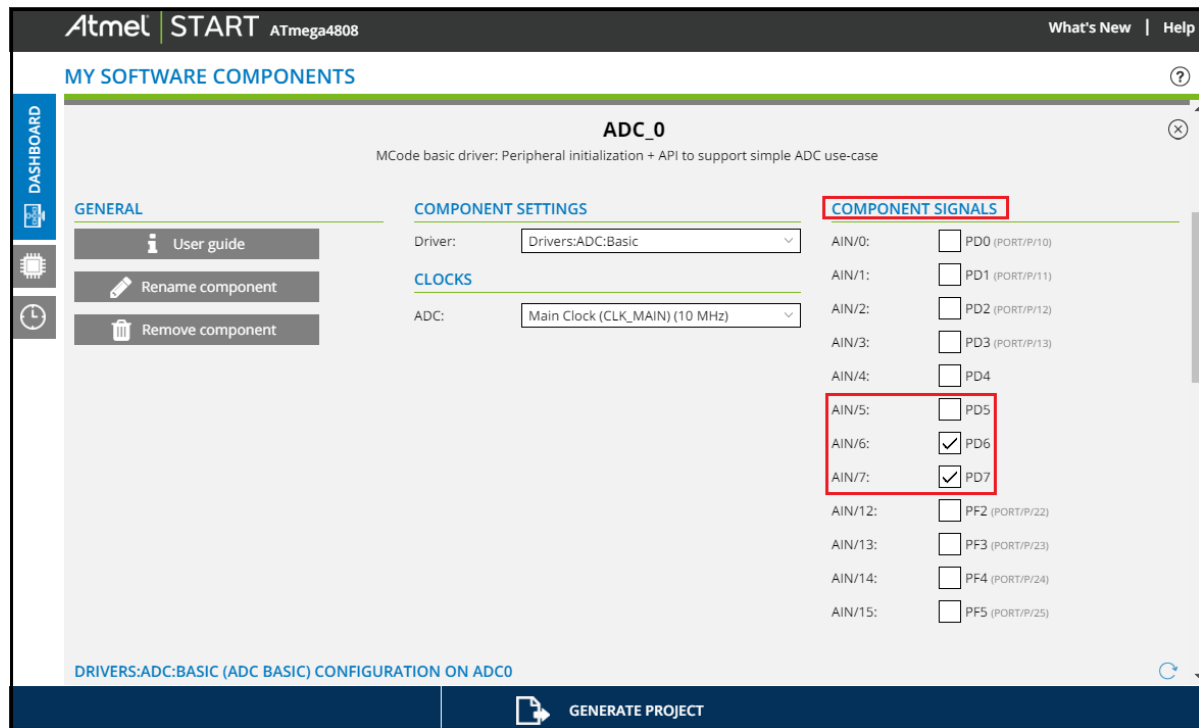
1.2 Configuring Additional Peripherals

The subsections below explain the configuration of additional modules/peripherals required for the indoor Air Quality Monitor application.

1.2.1 ADC

- The SHT31 analog sensor is used for humidity and temperature measurement. The ADC peripheral is configured to read the sensor output. The **ADC** peripheral driver is added to the project as part of the AVR-IoT WG Sensor Node example project. So, there is no need to add the **ADC** peripheral driver once more to the project.
- To configure ADC, click **ADC_0**. The ADC configuration window appears.
- The temperature and humidity output pins of the SHT31 sensor are connected to the port pins PD6 and PD7, respectively. To configure these port pins as ADC channels, go to the *COMPONENT SIGNALS* section and select pins **PD6** and **PD7** as ADC pins. Deselect pin **PD5**, because the light sensor connected to PD5 on the AVR-IoT WG board is not used in the indoor AQM application.

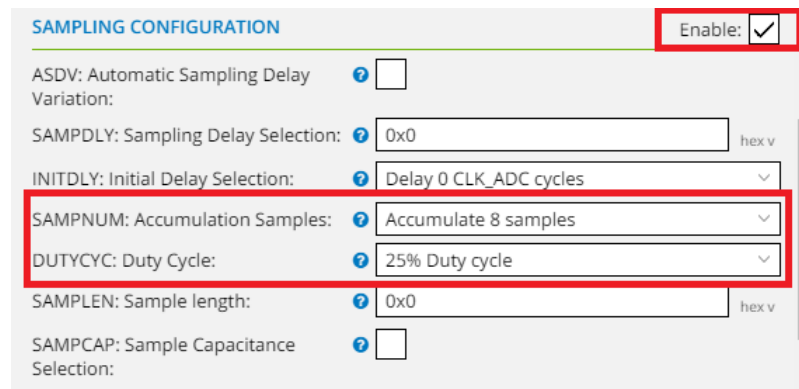
Figure 1-3. ADC PIN Configuration



Note: The AQM hardware has two temperature sensors: One is the analog sensor (SHT31-ARP), and another one is the on-board (AVR-IoT WG board) digital sensor MCP9808. The AQM uses the MCP9808 digital sensor for monitoring temperature.

- The Sample Accumulation feature of the ADC is used for the indoor AQM application. In the ADC configuration window, enable *SAMPLING CONFIGURATION*. Configure *SAMPNUM*: Accumulation Samples to Accumulate 8 samples, and *DUTYCYC*: Duty Cycle to 25% Duty cycle, in the *SAMPLING CONFIGURATION* section.

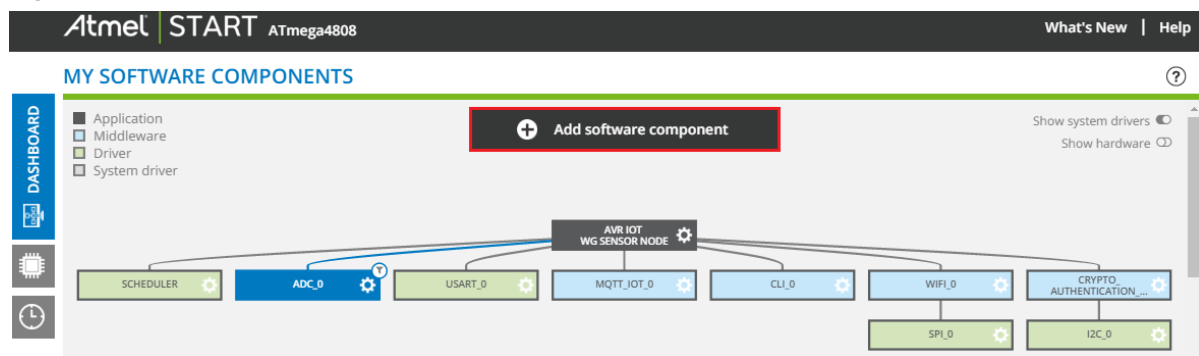
Figure 1-4. ADC Sampling Configuration



1.2.2 UART

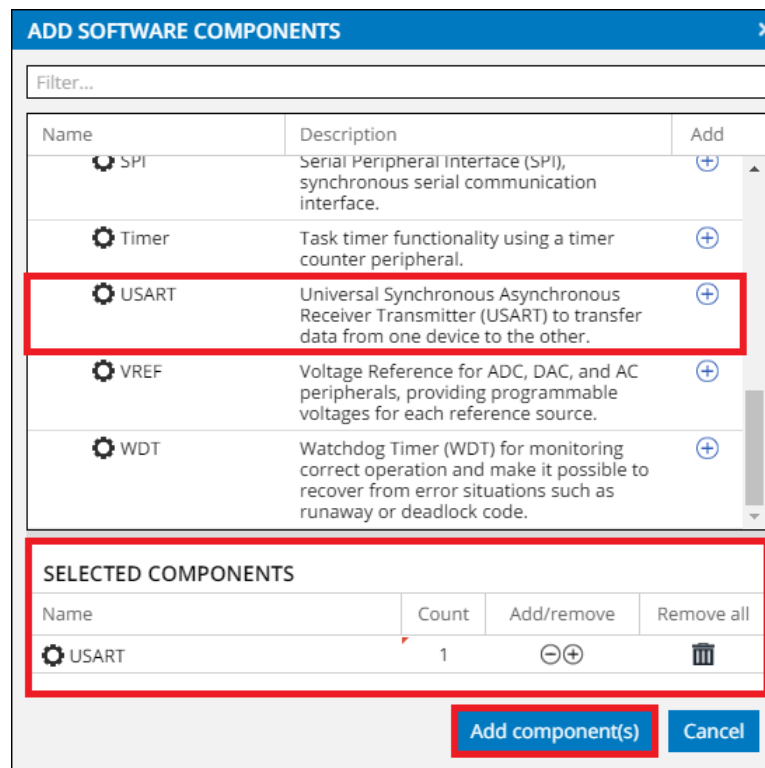
- The SPS30 PM_{2.5} sensor communicates with the microcontroller through the UART interface.
- To configure UART1, add the **USART** peripheral driver by clicking **Add Software Component**.

Figure 1-5. Add Software Component



- Expand the **Drivers** tab and add the **USART** peripheral driver. The USART appears on the **SELECTED COMPONENTS** box. Click **Add component(s)**.

Figure 1-6. USART Module Driver Addition



- In the *Dashboard* view, click **USART_1** to configure the UART peripheral. Configure *Mode* as *Async IRQ Mode*, and *Instance* as *USART1* in the *COMPONENT SETTINGS* section. Configure *RXD* to *PC1*, and *TXD* to *PC0* in the *COMPONENT SIGNALS* section. Set *Baud Rate* to *115200* in the *BASIC CONFIGURATION* section.

Figure 1-7. USART Module Configuration

GENERAL	COMPONENT SETTINGS	COMPONENT SIGNALS
User guide Rename component Remove component	Driver: Drivers:USART:Basic Mode: Async IRQ Mode Instance: USART1 CLOCK: USART: Main Clock (CLK_MAIN) (10 MHz)	RXD: PC1 TXD: PC0 XCK: ---- XDIR: ----

DRIVERS:USART:Basic (ASYNC IRQ MODE) CONFIGURATION ON USART1

DRIVER CONFIGURATION	ADVANCED CONFIGURATION
Printf support: ☐ RX Buffer Size: 8 TX Buffer Size: 8	LBME: Loop-back Mode Enable: ☐ DBG RUN: Debug Run: ☐

BASIC CONFIGURATION

RXEN: Receiver enable:	☒
TXEN: Transmitter Enable:	☒
PMODE: Parity Mode:	No Parity
SBMODE: Stop Bit Mode:	1 stop bit
CHSIZE: Character Size:	Character size: 8 bit
Baud Rate:	115200 dec v

1.2.3 NVM

- The internal EEPROM is used to store the default parameters and the external EEPROM's send and write indices. The internal EEPROM is accessed using the NVM control peripheral.
- To configure NVM control, add the **Flash** peripheral driver by following the steps described in the USART section.
- Select **FLASH_0** in the *Dashboard* view.
- Rename component as *NVM*.

Figure 1-8. NVM Configuration

MY SOFTWARE COMPONENTS

DASHBOARD

AVR-IOT WG SENSOR BOARD

SCHEDULER, ADC, USART, MQTT, CCL, WDT, SPI, I2C, FLASH_0

RENAME COMPONENT

Specify new name: NVM

GENERAL

COMPONENT SIGNALS

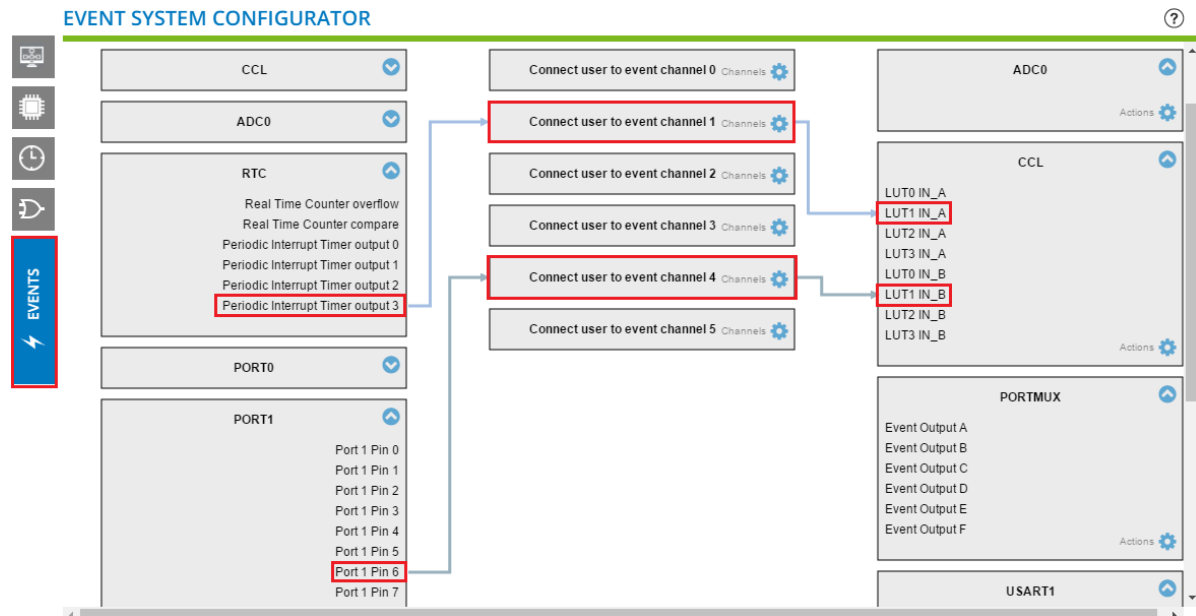
DRIVERS:NVMCTRL-BASIC (NVMCTRL-BASIC) CONFIGURATION ON NVMCTRL

1.2.4 Event System

- The on-board switch on the AVR-IoT WG board is used to turn on the OLED display and to change the parameter to be displayed on the OLED. The on-board switch SW0 on the AVR-IoT WG board is connected to PORT1_PIN6 of the microcontroller. The switch debounce mechanism is implemented to detect switch press event without any software intervention, using Event System and CCL peripherals of the microcontroller.

- Add both **Event System** and **Digital Glue Logic (CCL)** drivers by following the steps as described in the *USART* section.
- Click the **EVENT** symbol to configure the Event System peripheral, as shown in [Figure 1-9](#). The *EVENT SYSTEM CONFIGURATOR* window opens.
- Connect *Periodic Interrupt Timer output 3* to *Connect user to event channel 1*, and *Port 1 Pin 6* to *Connect user to event channel 4*. Click, hold, and drag to the channel for connection. Thus, PIT3 and Port1 Pin6 act as event generators for channel 1 and channel 4, respectively.

Figure 1-9. Event System Configuration

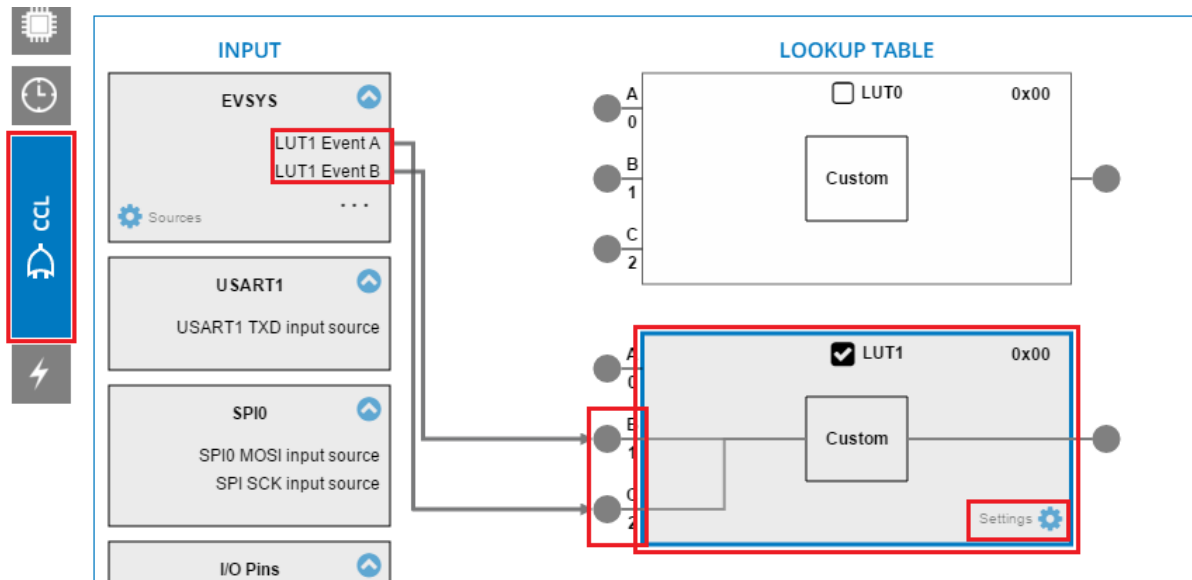


- In the same way, connect *Connect user to event channel 1* to *LUT1IN_A* and *Connect user to event channel 1* to *LUT1IN_B*, as shown in [Figure 1-9](#). Thus, LUT1IN_A and LUT1IN_B of the CCL act as event user for channel 1 and channel 4, respectively.

1.2.5 CCL

- To implement the switch debounce mechanism without any software intervention, Look-Up Table 1 (LUT1) of the CCL peripheral with filter option is used. The CCL peripheral generates an interrupt for each switch press event.
- Click the **CCL** symbol, as shown in [Figure 1-10](#). The *CCL CONFIGURATOR* window opens.
- Click **LUT1** to select **LUT1** for configuration. Enable the LUT1 by checking the **LUT1 checkbox**.

Figure 1-10. LUT1 Inputs Configuration



- Connect *LUT1 Event A* to the C input of the LUT1 and *LUT1 Event B* to the B input of the LUT1, as shown in Figure 1-10.
- Click the **Settings** icon inside the LUT1, as shown in Figure 1-10.
- Configure **CLKSRC: Clock Source Selection** to *IN[2] is clocking the LUT* and close the settings window.

Figure 1-11. LUT1 Clock Source Configuration

The screenshot shows the 'CCL SETTINGS' window for LUT1. The 'CLKSRC: Clock Source Selection' dropdown is set to 'IN[2] is clocking the LUT'. Other settings include: INSEL0: LUT Input 0 Source Selection: Masked input; INSEL1: LUT Input 1 Source Selection: Event input source B; INSEL2: LUT Input 2 Source Selection: Event input source A; TRUTH1: Truth 1: 0; and OUTEN: Output Enable: (unchecked). A 'Close' button is at the bottom right.

- Go to the *LUT1 CONFIGURATION* section. In *Truth Logic*, set *Value(hex)* to 1. Configure *Filter Options* to *Enable Filter* and configure *Clock Selection* to *IN2 Clock*.

Figure 1-12. LUT1 Configuration

CCL CONFIGURATOR

Enable CCL ☒ Settings Add software component Reset Undo Redo

INPUT **LOOKUP TABLE** **OUTPUT**

LUT1 CONFIGURATION

Gate Type: Custom

Truth Logic:

☐ Expression: Try a*b*c

☒ Value (hex): 1

C	B	A	VALUE
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Filter Options: Enable Filter

Enable Edge Detector: ☐

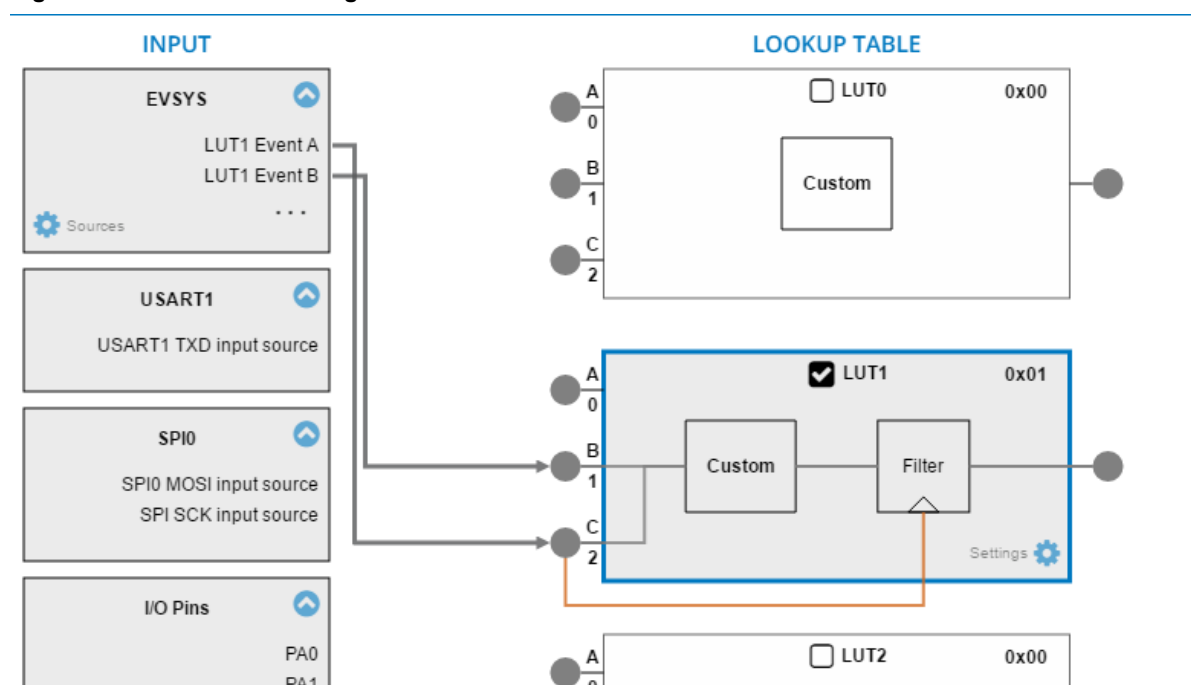
Sequential Logic - (LUT0 & LUT1):

Sequential Logic Options: Sequential logic disabled

Clock Selection: IN2 Clock

- Finally, LUT1 looks as shown in Figure 1-13.

Figure 1-13. LUT1 after Configuration



- The microcontroller goes to Standby sleep mode periodically. To detect switch press event while the microcontroller is in Standby sleep mode, the CCL has to run in Standby sleep mode.
- Go to *Dashboard* view and select **Digital Glue Logic**.
- Check the **RUNSTDBY: Run in Standby** option in the *BASIC CONFIGURATION* section.

Figure 1-14. Run in Standby Bit Configuration

MY SOFTWARE COMPONENTS

DRIVERS:CCL:INIT (CCL) CONFIGURATION ON CCL

BASIC CONFIGURATION		LOOKUP TABLE 0 CONFIGURATION	
ENABLE: Enable:	☒	LUTEN: LUT Enable:	☒
RUNSTDBY: Run in Standby:	☒	OUTEN: Output Enable:	☐
SEQSEL0: Sequential Selection 0:	Sequential logic disabled	CCL Configurator Clock: CCL clock:	
SEQSEL1: Sequential Selection 1:	Sequential logic disabled	CLKSRC: Clock Source Selection:	
LOOKUP TABLE 1 CONFIGURATION		INSEL0: LUT Input 0 Source Selection:	
LUTEN: LUT Enable:	☒	INSEL1: LUT Input 1 Source Selection:	

- In the **INTERRUPT CONTROL CONFIGURATION** section, configure **INTMODE1: Interrupt Mode 1 Selection** to *Sense rising edge*, and all other Modes Selection to *Interrupt disabled*.

Figure 1-15. Interrupt Control Configuration

INTERRUPT CONTROL CONFIGURATION

INTMODE0: Interrupt Mode 0 Selection:	Interrupt disabled
INTMODE1: Interrupt Mode 1 Selection:	Sense rising edge
INTMODE2: Interrupt Mode 2 Selection:	Interrupt disabled
INTMODE3: Interrupt Mode 3 Selection:	Interrupt disabled

1.2.6 Sleep Mode

- The AQM application uses Standby sleep mode. The microcontroller goes to Sleep mode when there is no task to be performed.
- To configure the Sleep mode, click **SLPCTRL** in the *Dashboard* view. Enable **SEN: Sleep enable** and configure **SMODE: Sleep mode** to *Standby Mode*.

Figure 1-16. Sleep Mode Configuration

SLPCTRL

SLPCTRL

GENERAL

User guide

COMPONENT SETTINGS

Driver: Drivers:SLPCTRL:Init

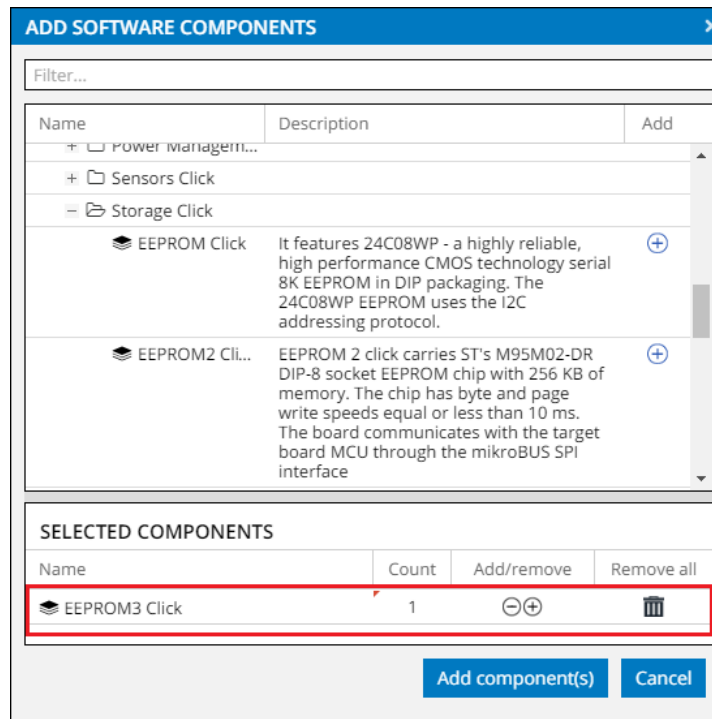
DRIVERS:SLPCTRL:INIT (SLPCTRL) CONFIGURATION ON SLPCTRL

ENABLE CONFIGURATION	SLEEP MODE CONFIGURATION
SEN: Sleep enable: ☒	SMODE: Sleep mode: Standby Mode

1.2.7 EEPROM 3 Click

Air quality sensor readings are stored on external EEPROM, present on the EEPROM 3 click board. Include EEPROM 3 by clicking **Add Software Component**. Expand *Middleware*, then *Storage Click*, and add **EEPROM3 Click**.

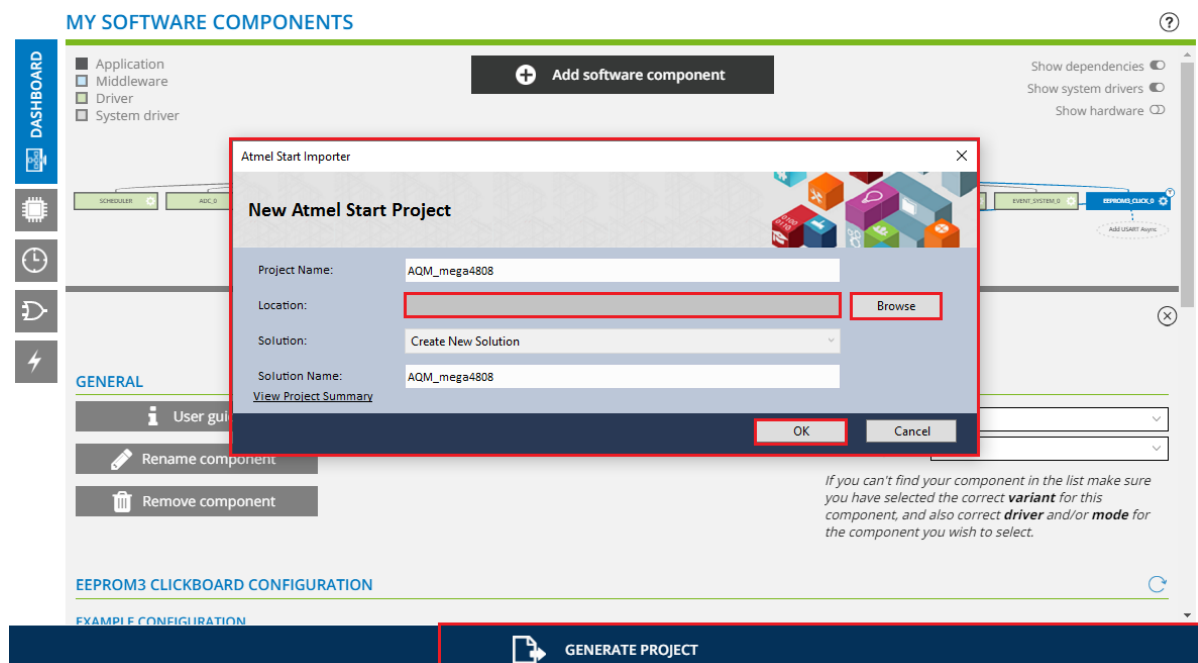
Figure 1-17. EEPROM 3 Click Addition



1.3 Generating START Code Files

- To generate the code, click **GENERATE PROJECT** present at the bottom. The *New Atmel Start Project* window appears for setting up the project parameters. Enter the project name and location and then click **OK**, as shown in Figure 1-18.

Figure 1-18. Project Generation



- Close the start window as the configuration and code generation are completed.

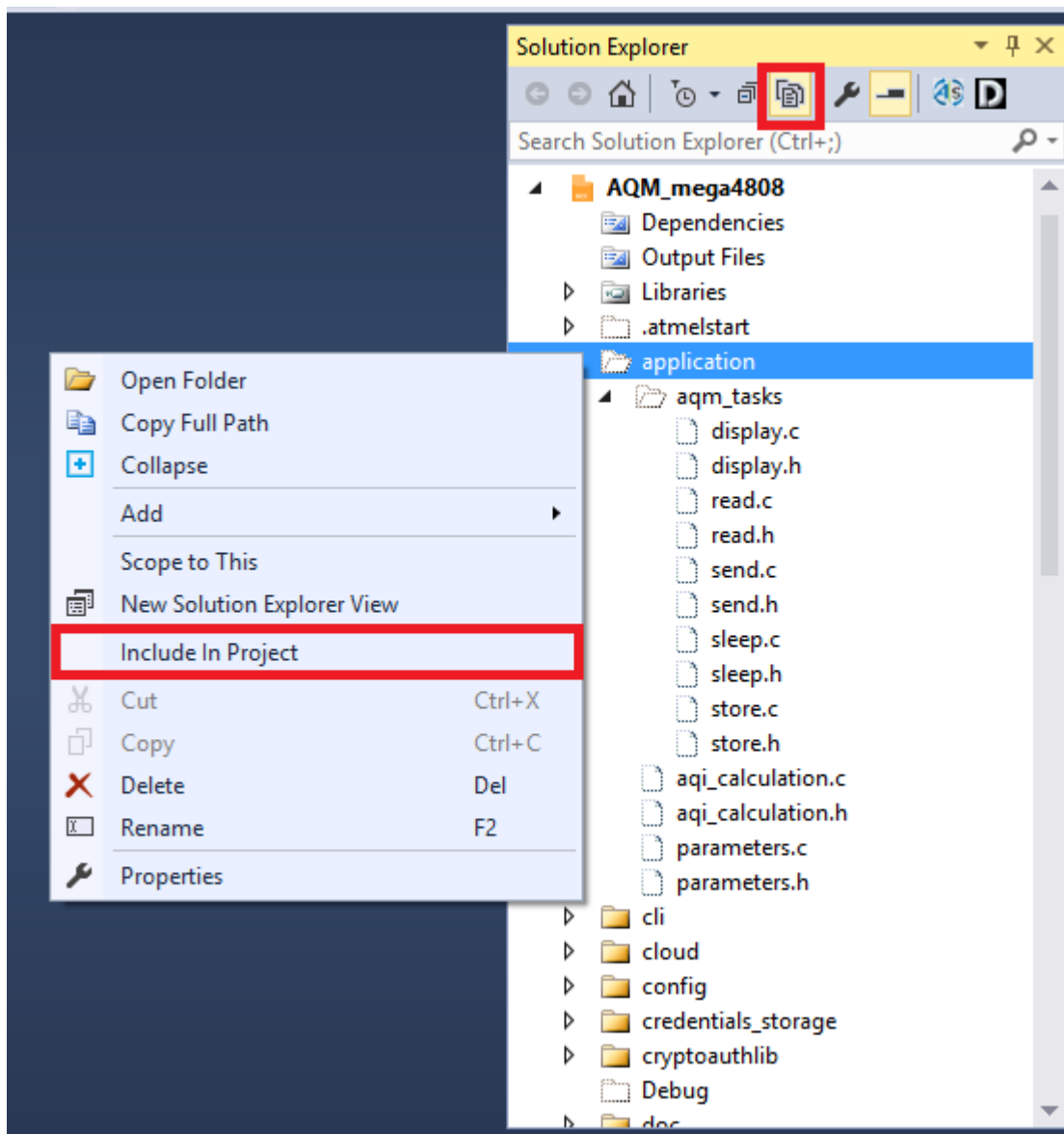
1.4 Adding Application Code and Sensor Drivers

- The firmware is available for download on GitHub, as below:



- Copy the **application** and **sensor** folders from the downloaded firmware and place them inside the project folder.
- To add both folders to the project, go to *Solution Explorer* in Atmel Studio and click the **Show All Files** symbol, as shown in Figure 1-19. Now, the **application** and **sensor** folders added in the previous step can be seen. Right click the **application** folder and select **Include in project**, as shown in Figure 1-19.
- Repeat these steps for adding the **sensor** folder.

Figure 1-19. Adding Application Files



1.5 Modifying Atmel START Generated Code

The Atmel START generated AVR-IoT WG source code and some of the peripherals' APIs need modifications. The user needs to compare both the AVR-IoT WG source code generated by Atmel START and the firmware available for download on GitHub, as below, to find more details about the required changes. Table 1-2 lists files and functions along with change description, where changes are required.



View Code Example on GitHub (Atmel Studio)

Click to browse repository

Table 1-2. List of Changes to START Generated Code

Source File	Function	Change Description
main.c	SendToCloud()	A dedicated task is created to send data to the cloud. Thus, this function is no longer required.
	main()	Parameter initialization and Cloud configuration.
application_manager.c	application_init()	PIT initialization.
		Turn on the display.
		Created application tasks.
	MAIN_dataTask()	No call to SendtoCloud() function.
cryptoauthlib\lib\jwt\atca_jwt.c	atca_jwt_finalize()	Switched to use the hardware SHA function during the JWT creation (to save memory).

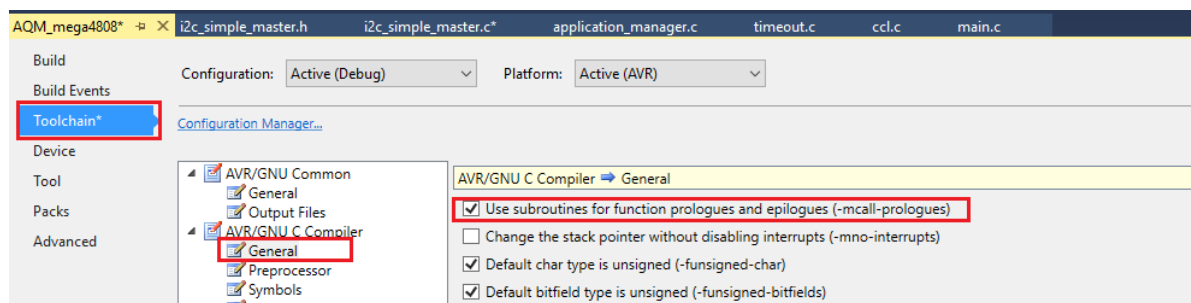
1.6 Compiler Settings

This section explains about the compiler settings which are required for code optimization.

1.6.1 Activate the “Use subroutines for function prologue/epilogue”

- Right click on the project name and select **Properties**.
- In the **Toolchain** tab, click **General** under **AVR/GNU C Compiler**. Enable *Use subroutines for function prologue/epilogue* option, as shown in Figure 1-20.

Figure 1-20. Configuration of Use Subroutines for Function Prologue/Epilogue

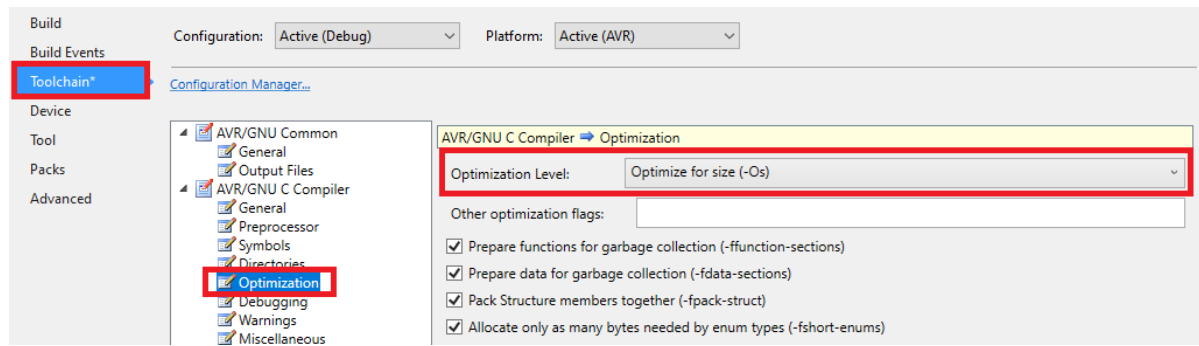


Note: This is required to optimize the code size.

1.6.2 Compiler Optimization Setting

- Right click on the project name and select **Properties**.
- In the **Toolchain** tab, click **Optimization** under **AVR/GNU C Compiler**. Choose *Optimize for size (-Os)* from the drop-down menu of the *Optimization Level*.

Figure 1-21. Compiler Optimization Setting



2. Indoor Air Quality Monitor Firmware Creation Using MCC

This section covers the list of steps to generate AVR-IoT WG source code with MCC, and the process to create indoor AQM application firmware.

Table 2-1 lists the software tools and versions used for the creation of the firmware using MCC. It is recommended to use tools of mentioned or higher versions.

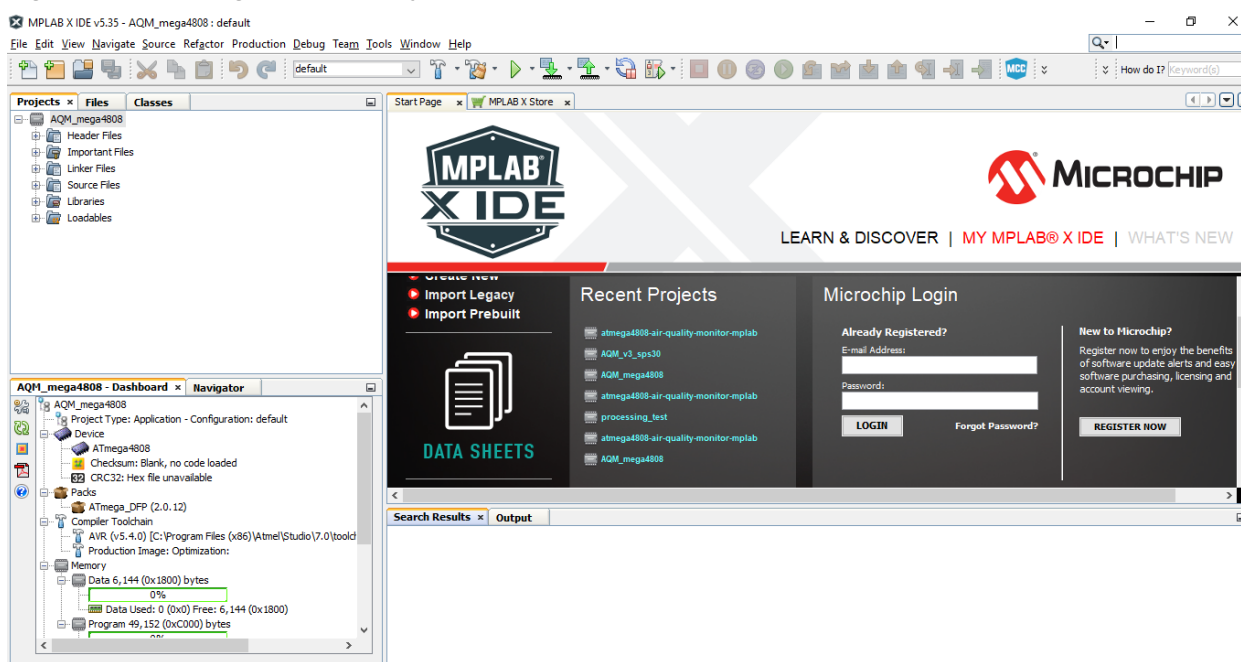
Table 2-1. Software Tools and Versions

Software Tool	Version
MPLAB® X	v5.35
AVR GCC	v5.4.0
avr8-gnu	v3.6.2.1778
MCC	v3.95.0
8-bit AVR MCUs Library	v2.1.0
AVR-IoT WG Sensor Node	v1.2.0
Foundation services	v0.1.34
MikroElektronika Click Library	v1.1.1
Atmega_DFP	v2.1.87

2.1 Creating a New MPLAB® X Project

Go to **File → New Project** and select **Standalone project**. Select **ATmega4808** as the device, and the **AVR** (GCC Compiler) as the compiler. Enter the project name and location details and create the project. The Start page of a new MPLAB X project appears, as shown in Figure 2-1.

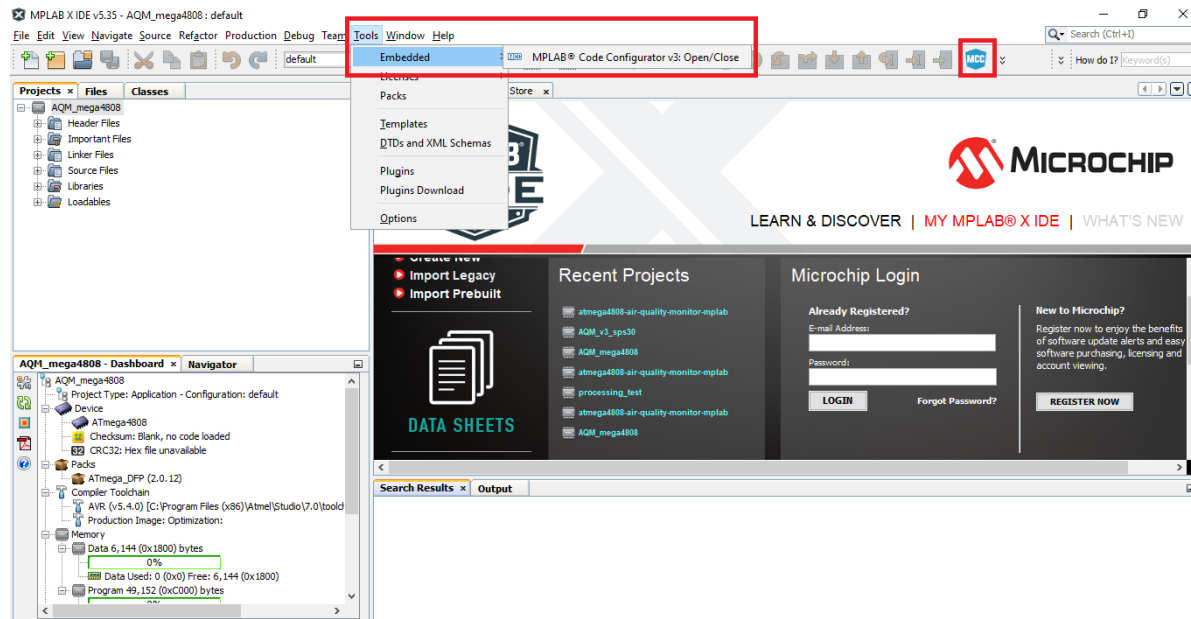
Figure 2-1. Start Page of a New Project Created with MPLAB® X



2.2 Generating AVR-IoT WG Sensor Node Example Project

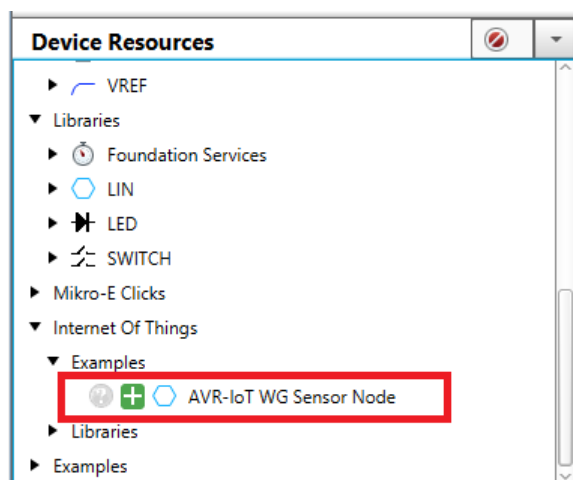
- Open MCC by clicking the **MCC** icon with MPLAB X, or click on *Tools* → *Embedded* → *MPLAB X Code Configurator v3 Open/Close*.

Figure 2-2. Open MCC



- Wait for the MCC configuration window to open. Once the configuration window appears for the new project, click **Save**.
- In the *Device Resources* section, expand the **Internet of Things** tab. Add the **AVR-IoT WG Sensor Node** example project.

Figure 2-3. AVR-IoT WG Sensor Node



- With the above steps, *AVR-IoT WG Sensor Node* is added to the project.

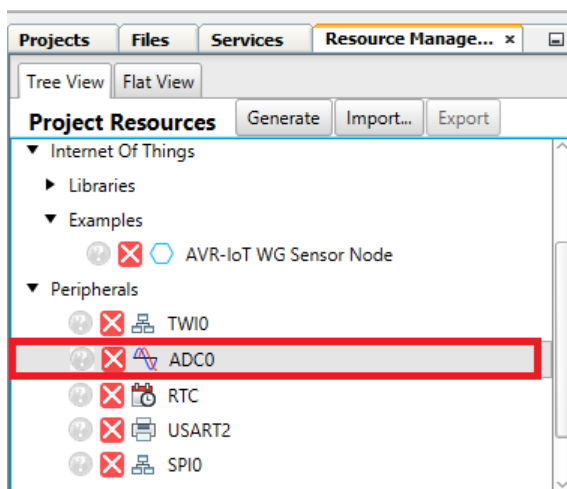
2.3 Configuring Additional Peripherals

The subsections below explain the configuration of other MCC modules/peripherals required for the indoor Air Quality Monitor application.

2.3.1 ADC0

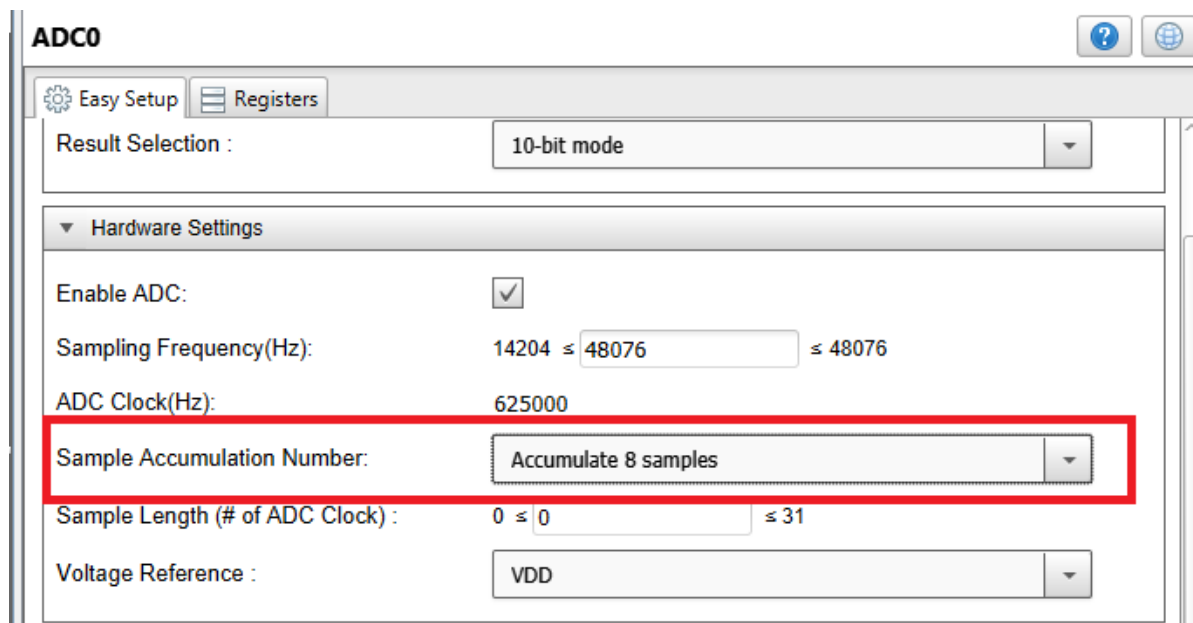
- The SHT31 analog sensor is used for humidity and temperature measurement. The ADC0 peripheral is configured to read the sensor output. The **ADC0** peripheral driver is added to the project as part of the AVR-IoT WG Sensor Node example project. There is no need to add the **ADC0** peripheral driver once more to the project.
- To configure ADC, click the **ADC0** peripheral under the **Project Resources** tab.

Figure 2-4. ADC Peripheral Selection



- The ADC0 configuration window opens.
- The Sample Accumulation feature of the ADC is used in the indoor AQM application. In the *Hardware Settings* section, configure *Sample Accumulation Number* to *Accumulate 8 samples*.

Figure 2-5. ADC0 Configuration



Note: The AQM hardware has two temperature sensors: One is the analog sensor (SHT31-ARP), and another one is the on-board digital sensor MCP9808 on the AVR-IoT WG board. The AQM application uses the MCP9808 digital sensor for monitoring temperature.

2.3.2 Pin Manager

- The temperature and humidity output pins of the SHT31 sensor are connected to the port pins PD6 and PD7, respectively.

- To configure PD6 and PD7 pins as ADC channels, go to *Pin Manager Grid View*. Configure PD6 and PD7 for **ADC0**. Pin PD5, which is connected to the light sensor output, is preconfigured as the ADC channel by the AVR-IoT WG Sensor Node example project. Deselect the **pin PD5 configuration** as ADC channel, because the light sensor is not used in the indoor AQM application.

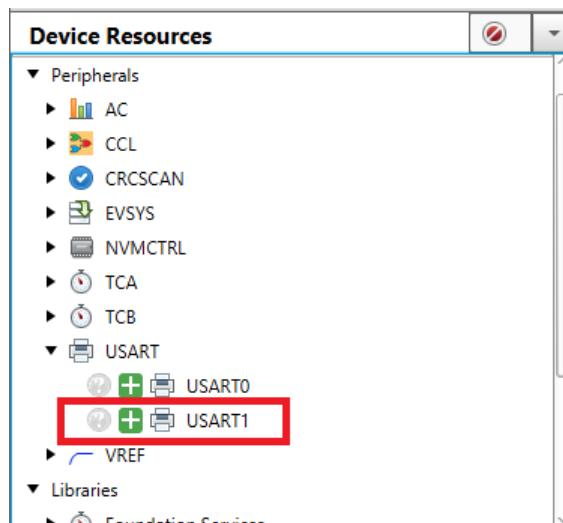
Figure 2-6. ADC Pins Configuration Using Pin Manager

Search Results		Output	Notifications [MCC]		Pin Manager: Grid View ×																									
Package:	QFP32	▼	Pin No:	30	31	32	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	20	21	22	23	24	25	26
				Port A ▼							Port C ▼				Port D ▼							Port F ▼								
Module	Function	Direction	0	1	2	3	4	5	6	7	0	1	2	3	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	
ADC0	AINx	input													🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒			🔒	🔒	🔒	🔒		
CLKCTRL ▼	CLKI	input	🔒																							🔒	🔒	🔒	🔒	
	CLKO	output								🔒																				
	TOSC1	input																						🔒						
	TOSC2	input																							🔒					
Pin Module ▼	GPIO	input	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒
	GPIO	output	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒	🔒
RSTCTRL	RESET	input																												🔒
	MISO	input						🔒				🔒																		

2.3.3 UART1

- The PM_{2.5} sensor SPS30 communicates with the microcontroller through the UART interface.
- To configure UART, add the **USART1** driver from the *Device Resources* section. Navigate through *Device Resources* → *Peripherals* → *USART* → *USART1* and click the **Add** symbol.

Figure 2-7. USART1 Peripheral Selection



- The USART1 configuration window opens.
- In the *Software Settings* section, enable the *Interrupt Driven* option. Set the *Baud Rate* to 115200 as the SPS30 sensor supports a baud rate of 115200.

Figure 2-8. USART1 Configuration

USART1

Easy Setup Registers

▼ Software Settings

API Prefix: USART1

Interrupt Driven: ☒

RX Buffer Size (Bytes): 8

TX Buffer Size (Bytes): 8

Printf support: ☐

▼ Hardware Settings

Mode: Async Mode

Baud Rate: 1 ≤ 115200 ≤ 1000000

Error Percent: -0.064%

Enable USART Receiver: ☒

Enable USART Transmitter: ☒

Parity Mode: No Parity

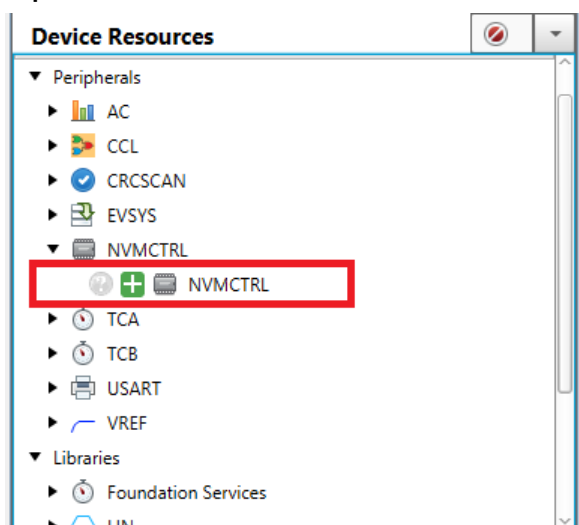
Stop Bit Mode: 1 stop bit

Character Size: Character size: 8 bit

2.3.4 NVM

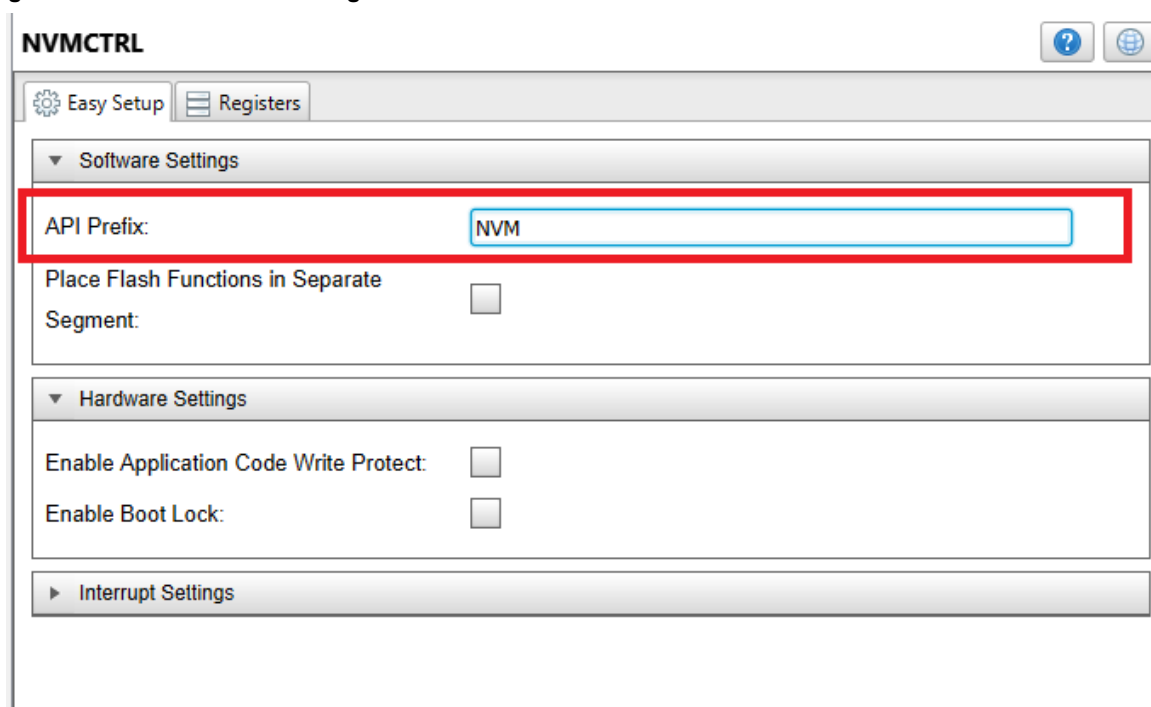
- The internal EEPROM is used to store the default parameters and external EEPROM's indices. The internal EEPROM is accessed using the NVM control peripheral.
- To configure NVM control, add the **NVM control** driver from the *Device Resources* section. Navigate through *Device Resources* → *Peripherals* → *NVMCTRL* and click the **Add** symbol.

Figure 2-9. NVM Control Peripheral Selection



- The NVMCTRL configuration window opens.
- In the *Software Settings* section, configure the *API Prefix* to *NVM*.

Figure 2-10. NVM Control Configuration



2.3.5 Event System

- The on-board switch on the AVR-IoT WG board is used to turn on the OLED display and to change the parameters to be displayed on the OLED. The on-board switch SW0 on the AVR-IoT WG board is connected to the PORT1_PIN6 of the microcontroller. The switch debounce mechanism is implemented without any software intervention, using the Event System and CCL peripherals of the microcontroller.
- To configure the Event System, add the **Event System** driver from the *Device Resources* section. Navigate through *Device Resources* → *Peripherals* → *EVSYS* and click the **Add** symbol. The *EVSYS* window opens.
- Event channel 1 is used to connect the Periodic Interrupt Timer output to the LUT1 of the CCL, and event channel 4 is used to connect the switch port pin to the LUT1 of the CCL.

- Configure the *Event Generator* as *RTC_PIT3*, and the *Event User* as *CCLLUT1A* for *CHANNEL 1*, as shown in [Figure 2-11](#).
- Configure *PORT1_PIN6* as *Event Generator*, and *CCLLUT1B* as *Event User* for *CHANNEL 4*, as shown in [Figure 2-11](#).

Figure 2-11. Event System Configuration

EVSYS

Easy Setup | Registers

Software Settings

API Prefix: EVSYS

Event System Settings

Event Generator	Channels	ADC0	CCLLUT0A	CCLLUT0B	CCLLUT1A	CCLLUT1B	CCLLUT2A	CCLLUT2B	CCLLUT3A	CCLLUT3B	EVOUTA	EVOUTB	EVOUTC	EVOUTD	EVOUTE
OFF	CHANNEL0	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐
RTC_PIT3	CHANNEL1	☐	☐	☐	☒	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐
OFF	CHANNEL2	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐
OFF	CHANNEL3	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐
PORT1_PIN6	CHANNEL4	☐	☐	☐	☐	☒	☐	☐	☐	☐	☐	☐	☐	☐	☐
OFF	CHANNEL5	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐	☐

Note: The *Load peripheral CCL* warning appears to load the CCL peripheral. Ignore the warning as the next subsection explains about loading CCL peripheral.

2.3.6 CCL

- To implement switch debounce, LUT1 of the CCL peripheral with filter option is used.
- The CCL peripheral generates an interrupt for each switch press event.
- To configure the CCL peripheral, add the **CCL** driver from *Device Resources*. Navigate through *Device Resources* → *Peripherals* → *CCL* and click the **Add** symbol.
- The CCL configuration window opens.
- In the *Interrupt Settings* window, choose *Sense rising edge* from the drop-down menu of the *LUT1-OUT Interrupt* option.

Figure 2-12. LUT1 Interrupt Configuration

CCL

Easy Setup | Registers

Hardware Settings

Enable CCL: ☒

Interrupt Settings

LUT0-OUT Interrupt: Interrupt disabled

LUT1-OUT Interrupt: Sense rising edge

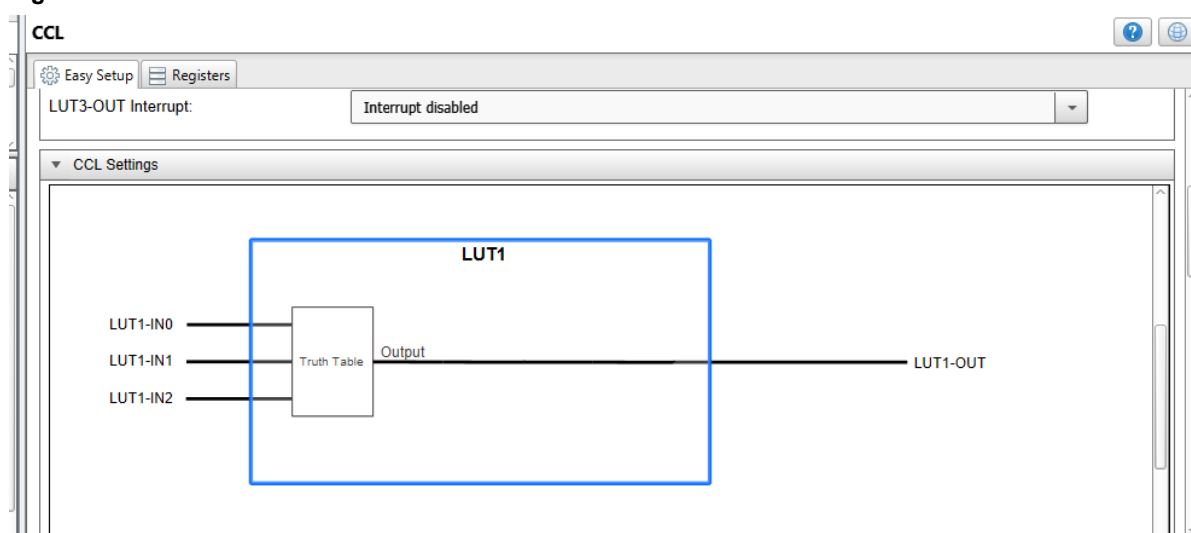
LUT2-OUT Interrupt: Interrupt disabled

LUT3-OUT Interrupt: Interrupt disabled

CCL Settings

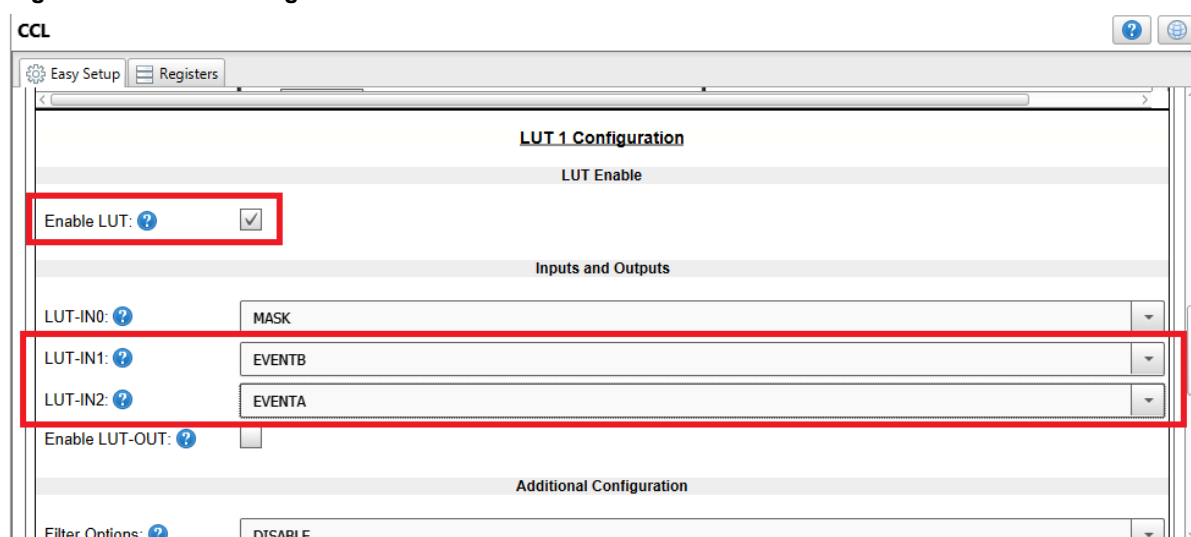
- Select **LUT1** by clicking **LUT1** in the *CCL Settings* section.

Figure 2-13. LUT1 Selection



- To enable LUT1, check the **Enable LUT** option in the *LUT1 Configuration* section, as shown in Figure 2-14.
- Select **EVENTB** from the drop-down menu of the *LUT-IN1* option, as shown in Figure 2-14. As *PORT1_PIN6* is connected to *EVENTB* through event channel 4, the switch signal acts as the first input to LUT1.
- Select **EVENTA** from the drop-down menu of the *LUT-IN2* option, as shown in Figure 2-14. As *RTC_PIT3* is connected to *EVENTA* through event channel 1, it provides the clock signal to the CCL filter.

Figure 2-14. LUT1 Configuration



- In the *Additional Configuration* section of the CCL configuration window, configure *Filter Options* to *FILTER* to enable the filter, as shown in Figure 2-15.
- Configure *Clock Selection* to *IN2*.

Figure 2-15. Enable Filter Mode and Clock Selection in CCL LUT1

CCL

Easy Setup Registers

LUT-IN1: ? EVENTB

LUT-IN2: ? MASK

Enable LUT-OUT: ? ☐

Additional Configuration

Filter Options: ? FILTER

Enable Edge Detector: ☐

Clock Selection: ? IN2

Truth Table

Custom

IN2	IN1	IN0	OUT
0	0	0	

- The microcontroller goes to Standby sleep mode periodically. To detect switch press event while the microcontroller is in Standby sleep mode, the CCL has to run in Standby sleep mode. In the **Registers** tab of the CCL configuration window, configure the *RUNSTDBY* option to *enabled* under the CTRLA register.

Figure 2-16. Run in Standby Bit Configuration

CCL

Easy Setup Registers

▼ CCL

Interrupt Enables

☒ INT1

☐ INT2

☐ INT0

☐ INT3

▼ Register: CTRLA 0x41

? ENABLE enabled

? RUNSTDBY enabled

▼ Register: INTCTRL0 0x4

- Configure *INSEL2* as *Event input source A* in the LUT1CTRLC register. *INSEL2* is reconfigured because its configuration is changed to masked input after Clock Selection.

Figure 2-17. LUT1 Input 2 Configuration

CCL

Easy Setup Registers

OUTEN disabled

Register: LUT1CTRLB 0x40

INSEL0 Masked input

INSEL1 Event input source B

Register: LUT1CTRLC 0x3

INSEL2 Event input source A

Register: LUT2CTRLA 0x0

CLKSRC CLK_PER is clocking the LUT

EDGEDET Edge detector is disabled

- Open the *Truth Table* in the **EASY Setup** tab of the CCL configuration window and configure it to **NOR**.

Figure 2-18. Truth Table Configuration

CCL

Easy Setup Registers

Enable Edge Detector: ☐

Clock Selection: IN2

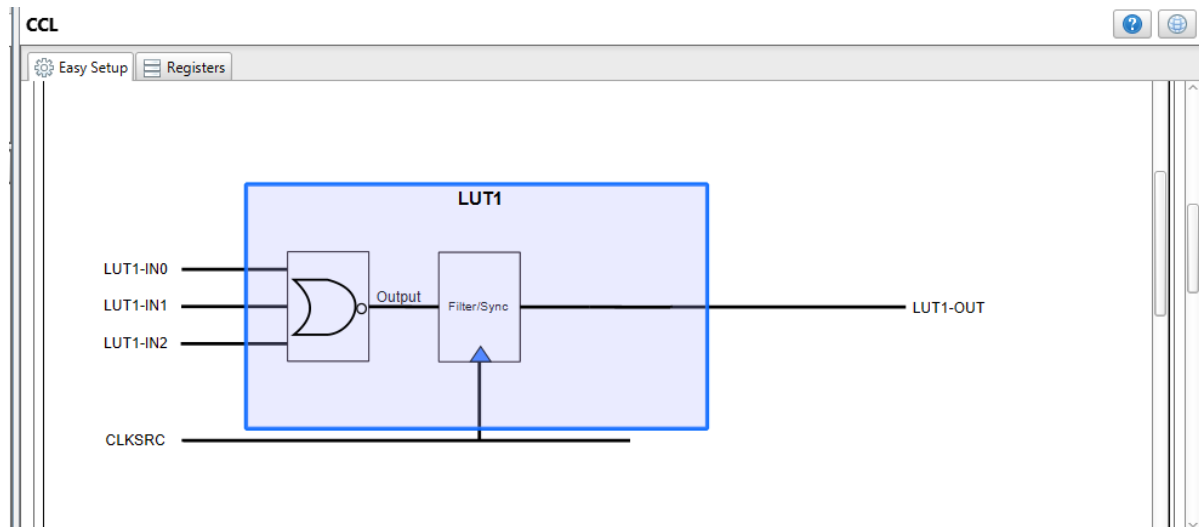
Truth Table

NOR

IN2	IN1	IN0	OUT
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

- After the completion of all the above configurations, LUT1 looks as shown in [Figure 2-19](#).

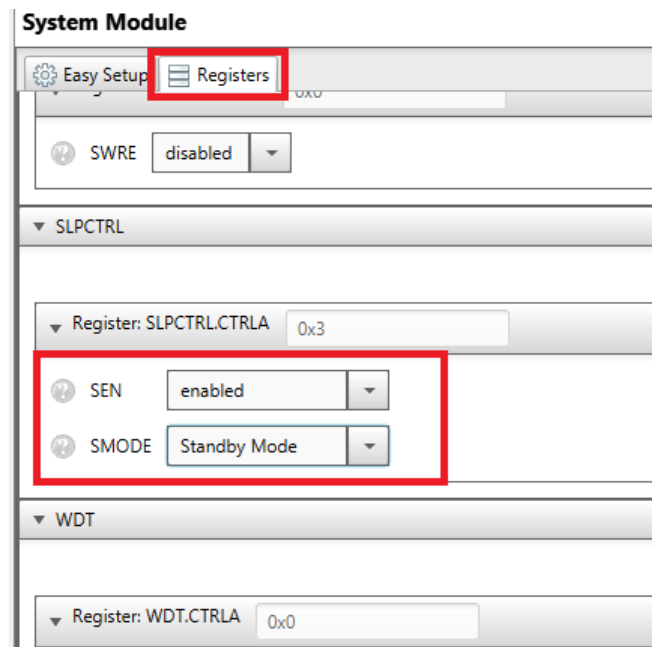
Figure 2-19. LUT1 after Configurations



2.3.7 Sleep Mode

- The microcontroller goes to Standby sleep mode after completion of all the tasks.
 - To configure the Sleep mode, click **System Module** in the *Project Resources* section. The *System Module* window opens.
 - Click the **Registers** tab, as shown in Figure 2-20.
- Note:** The **Registers** tab may take some time to open.
- Go to the SLPCTRL.CTRLA register, and enable the Sleep mode by configuring *SEN* to *enabled* and *SMODE* to *Standby Mode*.

Figure 2-20. Standby Sleep Mode Configuration

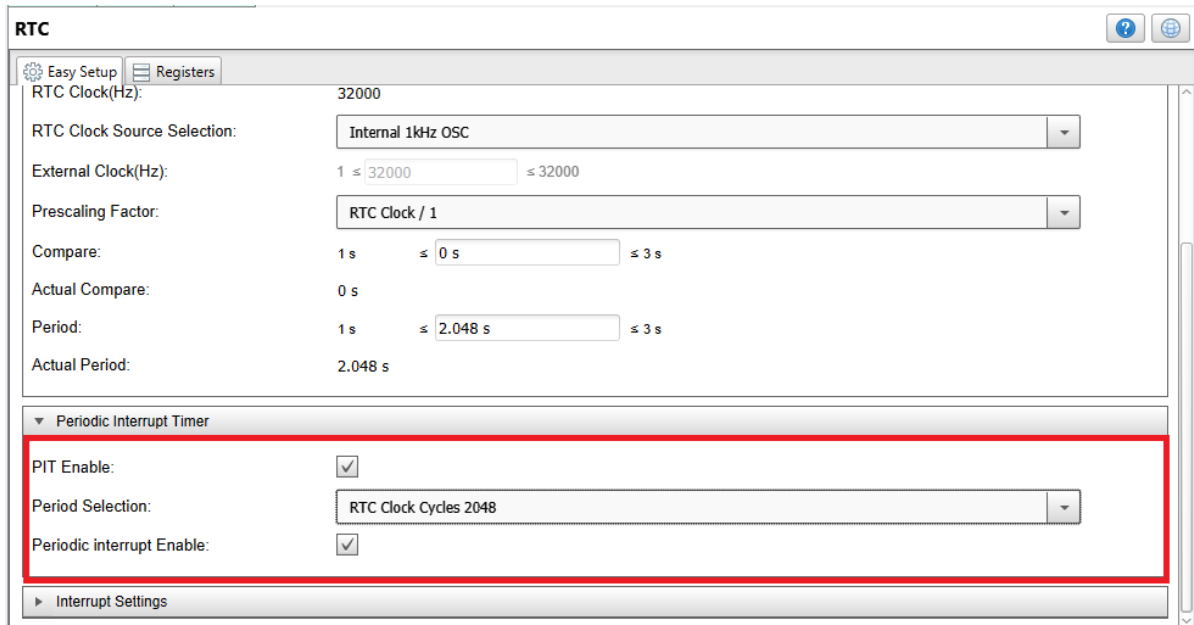


2.3.8 PIT

- The microcontroller periodically wakes up from sleep mode whenever it gets a Peripheral Interrupt Timer (PIT) interrupt. The PIT is part of the RTC module. To configure the PIT, select **RTC** from the *Project Resources* by navigating through *Project Resources* → *Peripherals* → *RTC*.

- In the *Periodic Interrupt Timer* section, enable the *PIT Enable* option. Configure *Period Selection* to *RTC Clock cycle 2048*. This divides the RTC clock by 2048.
- Enable the *Periodic interrupt Enable* option.

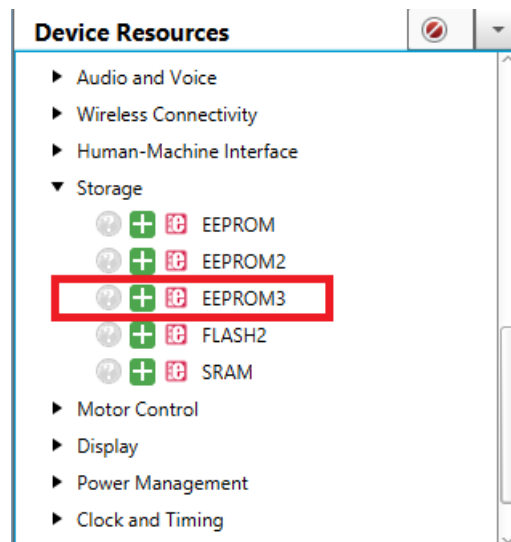
Figure 2-21. PIT Configuration



2.3.9 EEPROM 3 Click

- Air quality sensors readings are stored on the external EEPROM present on the EEPROM 3 click board. The EEPROM 3 click board is supported by the MCC framework. Include the **EEPROM3** click driver to the project from the *Device Resources* section. Navigate through *Device Resources* → *Mikro-E Clicks* → *Storage* → *EEPROM3* and click the **Add** symbol.

Figure 2-22. EEPROM 3 Click Addition



- The EEPROM 3 click configuration window opens. There is no additional configuration required for the EEPROM 3 click.

2.4 Generating MCC Code Files

- Click the **Generate** button, located on the left-hand corner of the *Resource Manager → Project Resources* window, and wait for the code generation to complete.

Note: A pop-up might appear, saying that the configuration has warnings. Click **YES** to continue as all the warnings have been taken care of.

2.5 Adding Application Code and Sensor Drivers

- The firmware is available for download on GitHub, as below:



View Code Example on GitHub (MPLAB® X)

Click to browse repository

- Copy the **application** and **sensor** folders from the downloaded firmware and place them inside the project folder.
- To add both folders to the project, go to the *Project* window and select the **Source Files** folder. Right click to open the content menu and select **Add Existing Items from folder** option. Click **Add folder**. Pick the **application** folder that was just copied containing the application code, then click the **select** option. Repeat these steps to add the **sensor** folder containing the sensor drivers. After selecting both folders, click the **Add** button.
- For a more detailed procedure on folders addition to a project, refer to [the page on Microchip Developer Help website](#).

2.6 Modifying MCC Generated Code

The MCC generated AVR-IoT WG source code and some of the peripherals' APIs need modifications. The user needs to compare both the AVR-IoT WG source code generated by MCC and the firmware available for download on GitHub, as below, to find more details about the changes. [Table 2-2](#) lists files and functions along with the change description, where changes are required.



View Code Example on GitHub (MPLAB® X)

Click to browse repository

Table 2-2. List of Changes to MCC Generated Code

Source File	Function	Change Description
main.c	SendToCloud()	A dedicated task is created to send data to the cloud. Thus, this function is no longer required.
	main()	Parameter initialization and Cloud configuration.
mcc_generated_files/src/ccl.c	ISR(CCL_CCL_vect)	This interrupt occurs upon switch press.
mcc_generated_files/src/rtc.c		Set PIT ISR callback function.
	RTC_Initialize()	Wait for all registers to be synchronized.
	ISR(RTC_PIT_vect)	Clear the PIT flag at the end.

.....continued		
Source File	Function	Change Description
mcc_generated_files/ application_manager.c	application_init()	Turn on the display.
	MAIN_dataTask()	Created application tasks.
mcc_generated_files/ cryptoauthlib/lib/jwt/ atca_jwt.c	atca_jwt_finalize()	Updated atcab_hw_sha2_256() function uses the hardware SHA function during the JWT creation (to save memory).
mcc_generated_files/cloud/ crypto_client/crypto_client.c		Commented out ATCA_PRINTF definition (to save memory).
mcc_generated_file/config/ cryptoauthlib_config.h		Commented out ATCA_PRINTF definition (to save memory).
mcc_generated_file/ EEPROM3_driver.c		I ² C functions name changed as per the MCC generated I ² C drivers.
mcc_generated_files/ cryptoauthlib/lib/hal/hal_i2c.c	I2C_0_wake_up()	I2C_BUSY function name changed as per the MCC generated I ² C drivers.
	hal_i2c_writeNBytes()	
	hal_i2c_readNBytes()	

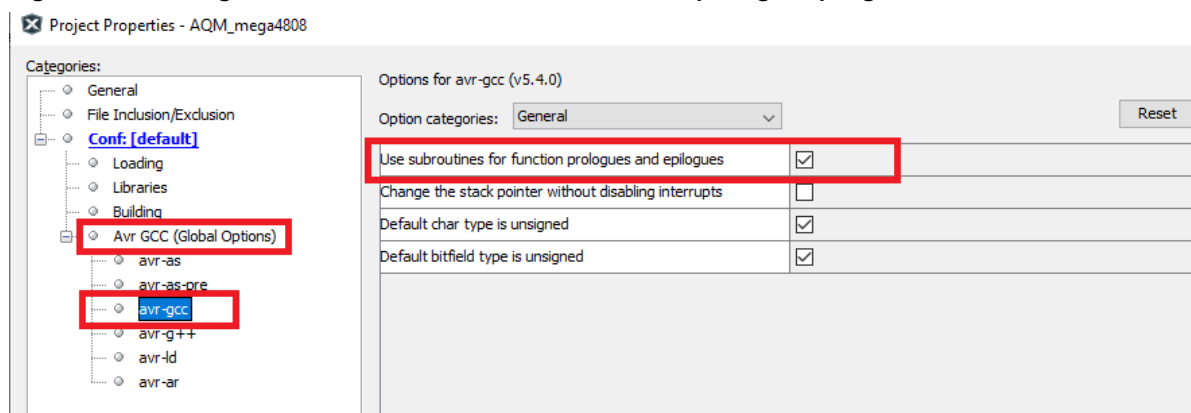
2.7 Compiler Settings

This section explains the compiler settings which are required for code optimization.

2.7.1 Activate “Use subroutines for function prologue/epilogue”

- Right click on the project name and select **Properties**. Click **avr-gcc** under AVR GCC Global Options. Enable the *Use subroutines for function prologue/epilogue* option.

Figure 2-23. Configuration of “Use subroutines for function prologue/epilogue”

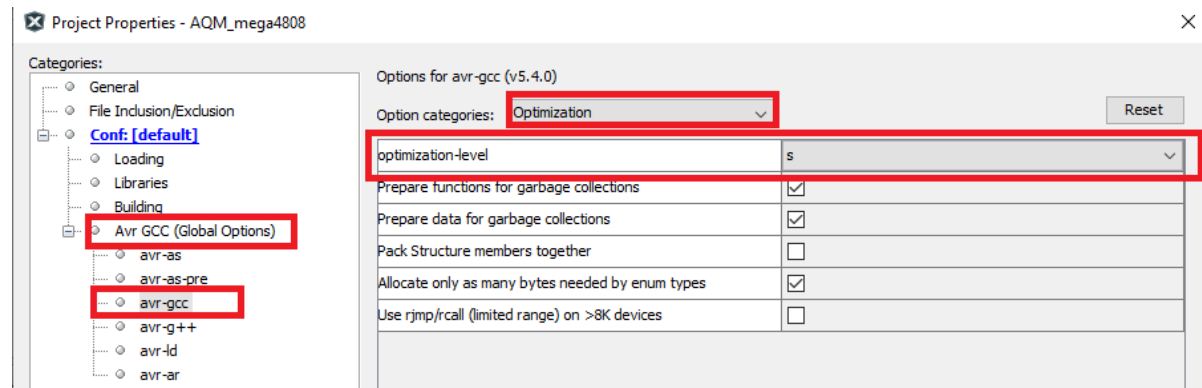


- Click **Apply** and then **OK**.
Note: This is required to optimize the code size.

2.7.2 Compiler Optimization Setting

- The application firmware is optimized for size to efficiently use the code memory. Click on the project name and select **Properties**. Click **avr-gcc** under AVR GCC Global Options. Select **Optimization** from the option categories drop-down menu. Set *optimization-level* to “s”.

Figure 2-24. Compiler Optimization Setting



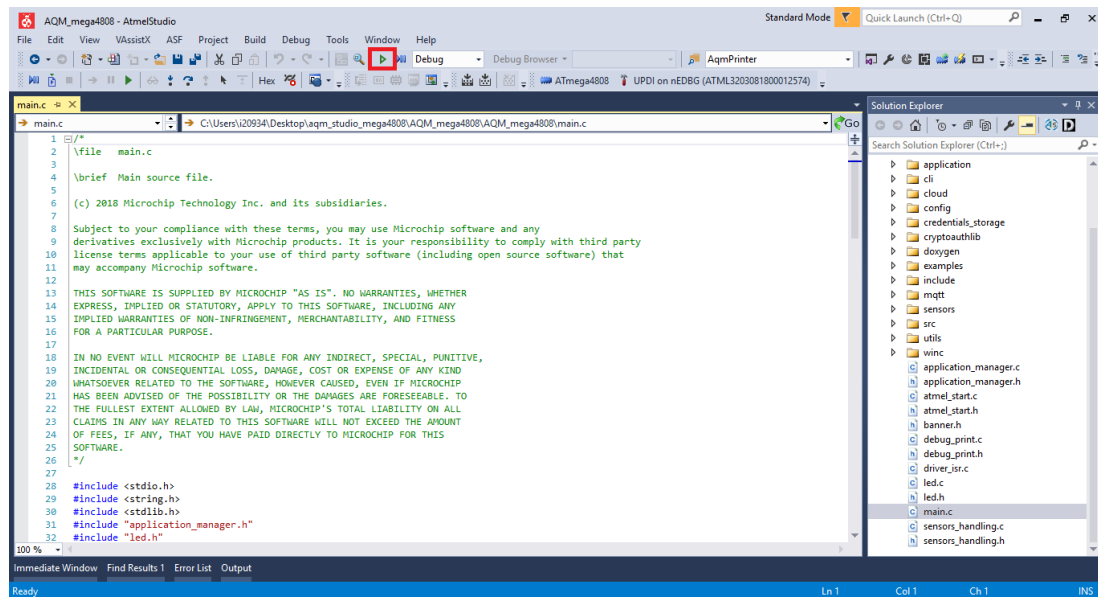
- Click **Apply** and then **OK**.

3. Programming the Application

3.1 Programming with Atmel Studio

- To program the device, connect the AVR-IoT WG board to the PC using a Micro-USB cable.
- Click the **Start without Debugging** (Ctrl + Alt + F5) icon.

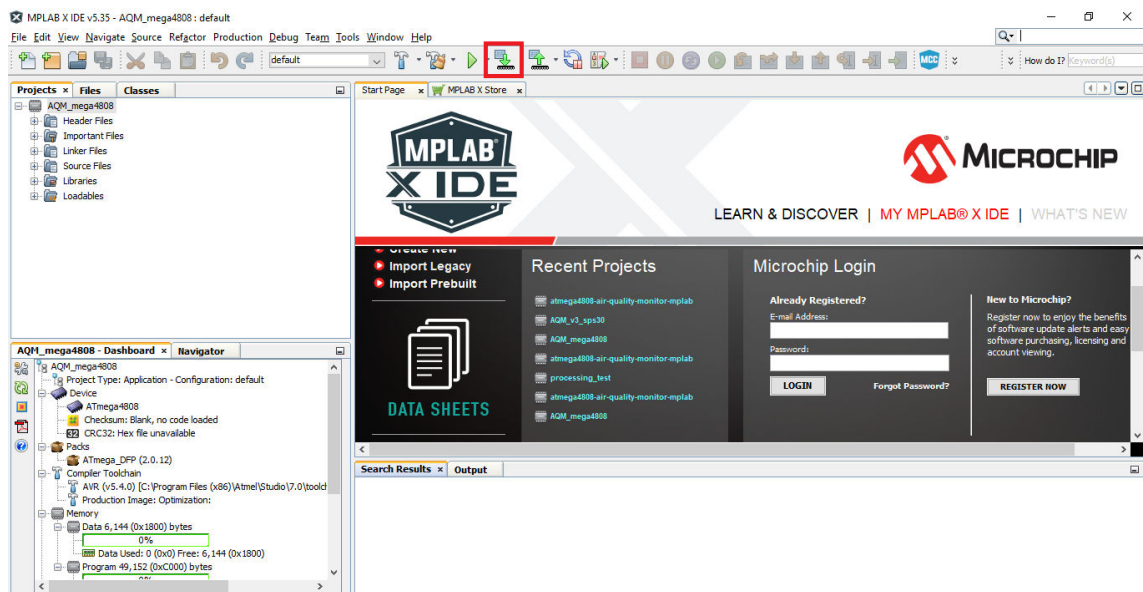
Figure 3-1. Start without Debugging



3.2 Programming with MPLAB® X IDE

- To program the device, connect the AVR-IoT WG board to the PC using a Micro-USB cable.
- Click the **Make and Program Device** icon.

Figure 3-2. Make and Program Device



4. Conclusion

This document covers details of the indoor AQM application firmware creation on top of the AVR-IoT WG source code. The document also explains the process of how to generate AVR-IoT WG source code with Atmel START and MCC.

5. Revision History

Document Revision	Date	Comments
A	03/2020	Initial document release

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