



VSC754x & VSC755x

Silicon Errata & Data Sheet Clarification

This document describes known silicon errata for the Microchip VSC754x & VSC755x family of devices. The silicon errata discussed in this document are for silicon revisions as listed in [Table 1](#). A summary of VSC754x & VSC755x silicon errata is provided in [Table 2](#).

TABLE 1: AFFECTED SILICON REVISIONS

Part Numbers	Silicon Revisions
VSC7546, VSC7549, VSC7552, VSC7556, VSC7558, VSC7546TSN, VSC7549TSN, VSC7552TSN, VSC7556TSN, VSC7558TSN	A0, A1 (Note 1)

Note 1: The A0 and A1 silicon revisions correspond with 0 and 1, respectively, in the REV_ID field of the CHIP_ID register.

TABLE 2: SILICON ISSUE SUMMARY

Item Number	Silicon Issue Summary	Affected SKUs	Affected Silicon Revisions
1.	The 100BASE-FX fiber mode does not function properly on certain ports	ALL	A0, A1

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Silicon Errata Issues

Module 1: The 100BASE-FX fiber mode does not function properly on certain ports

DESCRIPTION

The legacy 100BASE-FX 100Mbps fiber mode does not work properly on certain ports.

END USER IMPLICATIONS

The 100BASE-FX fiber mode is only supported for ports D0, D1, D5, D7, D13, D14, and D15, corresponding to SerDes S1, S2, S6, S8, S14, S15, and S16. 100BASE-FX native support is limited to this list of ports.

Work Around

For native 100Mbps 100BASE-FX fiber mode support, the above listed ports must be used. For other ports, a workaround can be made using external PHYs that have support for 100BASE-FX. E.g., the Microchip VSCxxxx copper PHYs support dual media copper and 100BASE-FX.

Fiber SFP modules with SGMII to 100BASE-FX also can be used to achieve 100BASE-FX fiber mode support for all ports.

PLAN

This erratum will not be corrected in a future revision. The data sheet will be updated to identify the ports and SerDes that can support 100BASE-FX operation.

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APPENDIX A: DOCUMENT REVISION HISTORY

Revision Level & Date	Section/Figure/Entry	Correction
DS80001051A (08-30-22)	All	Initial release

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ISBN: 9781668311493

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