
Atmel AVR1019: Migration from ATxmega128A1/64A1 to ATxmega1281U/64A1U



Features

- Enhancements and added functions
- Reset sources
- I/O ports
- DAC – digital to analog converter
- AC – analog comparator

8-bit Atmel Microcontrollers

Application Note

1 Introduction

This application note is a guide to assist users of ATxmega128A1/64A1 in converting designs to Atmel ATxmega1281U/64A1U. For complete device details, always refer to the most recent version of the ATxmega1281U/64A1U datasheet and the Atmel® AVR® XMEGA® AU manual. Errata differences between ATxmega128A1/64A1 and ATxmega1281U/64A1U are not listed in this document, only in the device datasheet.

In addition to the differences described in this document, other typical characteristics could be different. Please check the latest datasheet for details.

ATxmega1281U/64A1U also includes new configuration options and functions. As far as possible these are implemented as a superset of existing ATxmega128A1/64A1 functions, so existing code for these devices will work on the new devices without changing existing configuration or enabling new functions. The new options and functions are listed in the application note for customers who in addition to a pure migration also wish to see an overview to consider use of the new functions.

Rev. 8415A-AVR-07/11





2 Enhancements and added functions

In this section, we summarize the enhancements or added features in Atmel ATxmega128A1U/64A1U compared with Atmel ATxmega128A1/64A1. For pure migration, you can skip the section and start from the next section.

2.1 USB

- One USB 2.0 full speed (12Mbps) and low speed (1.5Mbps) device compliant interface is added

2.2 Clock system

- A divide-by-two option for the PLL output that enables output frequency down to 10MHz
- PLL lock failure detection with optionally Non-Maskable Interrupt (NMI), for improved safety and robustness
- Non-prescaled Real Time Counter clock source options: External clock from TOSC1, 32.768kHz from TOSC, and the 32.768kHz from the 32.768kHz Internal Oscillator
- The 32MHz Internal Oscillator can be tuned to run at any frequency between 30MHz and 55MHz

2.3 I/O ports

- Alternate pin locations for Timer/Counter 0 Compare Channels, USART0 and SPI
- Alternate pin locations for the Peripheral Clock and Event output functions

2.4 Analog to digital converter

- Automatic input channel scan
- VCC/2 voltage reference option
- 1/2x (divide by two) gain stage setting
- Internal ground can be used as negative input in differential mode (with gain)

2.5 Analog comparator

- A constant current source

2.6 CRC16/CRC32 generator

- A CRC16/CRC32 Generator Module that supports CRC16 (RC-CCITT) and CRC-32 (IEEE 802.3)

2.7 16-bit timer/counter0

- Split mode that enable two 8-bit Timer/Counters with 4PWM channels each

2.8 High resolution extension

- Hi-Res+ option to allow PWM resolution to be increased with 8x (3-bit)

2.9 Power management

- Possibility to enable sequential start of the components used for analog modules ADC and Analog Comparator in order to reduce start-up current

3 Reset sources

3.1 Brown-out detection

The programmable BODLEVEL settings are different in Atmel ATxmega128A1U/64A1U. See [Table 3-1](#) below for details. Please refer to the device datasheet regarding tolerance for the Brown-out levels.

Table 3-1. Brown-out levels.

BODLEVEL	VBOT – XMEGA AU	VBOT – XMEGA A
111	1.6V	1.6V
110	1.8V	1.9V
101	2.0V	2.1V
011	2.4V	2.6V
010	2.6V	2.9V
000	3.0V	3.4V

4 I/O ports

The I/O port pins are LVTTL and LVCMOS compatible for Atmel ATxmega128A1U/64A1U devices. The minimum “Input High Voltage” is never higher than 2.0V for $V_{CC} > 2.7V$.

In Atmel ATxmega128A1/64A1, the minimum “Input High Voltage” is $0.7V_{CC}$, and could be higher than 2.0V for $V_{CC} > 2.86V$.

5 DAC – digital to analog converter

The Atmel ATxmega128A1U/64A1U DAC has two continuous output channels, and not a sample and hold circuit as Atmel ATxmega128A1/64A1 devices. This gives continuous time output and higher sample rate for each channel. There is separate calibration (offset/gain) for each DAC channel in ATxmega128A1U/64A1U.

When DACA0OFFCAL (in Production Signature Row) is written to CH0OFFSETCAL in DACA, CH1OFFSETCAL is also written with this value. The details of this operation are shown below.

The first step,

- read DACA0OFFCAL from production signature row
- write DACA0OFFCAL to DACA.CH0OFFSETCAL

This will result in:

- DACA.CH0OFFSETCAL = DACA0OFFCAL
- DACA.CH1OFFSETCAL = DACA0OFFCAL

The second step,

- read DACA1OFFCAL from production signature row
- write DACA1OFFCAL to DACA.CH1OFFSETCAL

This will result in:

- DACA.CH1OFFSETCAL = DACA1OFFCAL

After that, any further writing to DACA.CH0OFFSETCAL does not change DACA.CH1OFFSETCAL until the next reset. The same is implemented for both OFFSET and GAIN calibration registers in DACA and DACB. This ensures that customers using the ATxmega128A1/64A1 DAC can continue and use the same calibration sequence and still calibrate both channels.

TIMCTRL register does not exist in ATxmega128A1U/64A1U, so there are no timing constraints on DAC operation.

6 AC – analog comparator

In Atmel ATxmega128A1U/64A1U, there is a two-cycle delay from writing a new MUX setting until it takes effect.



7 Registers

7.1 Removed registers and bits

The below table lists register bits, which exist in Atmel ATxmega128A1/64A1 but not in Atmel ATxmega128A1U/64A1U.

Table 7-1. Register bits and functionality that does not exist in ATxmega128A1U/64A1U.

Register name	Register bit	Function
FUSEBYTE2	BODACT[1:0] ⁽¹⁾	BOD functionality when in active mode
TIMCTRL	CONINTVAL[2:0]	DAC Conversion Interval
	REFRESH[3:0]	DAC Channel Refresh Timing Control

Note: 1. BODACT fuses are now located in FUSEBYTE5 for all devices.

8 Table of contents

Features	1
1 Introduction	1
2 Enhancements and added functions	2
2.1 USB	2
2.2 Clock system	2
2.3 I/O ports	2
2.4 Analog to digital converter	2
2.5 Analog comparator	2
2.6 CRC16/CRC32 generator	2
2.7 16-bit timer/counter0	2
2.8 High resolution extension	2
2.9 Power management	3
3 Reset sources	4
3.1 Brown-out detection	4
4 I/O ports	5
5 DAC – digital to analog converter	6
6 AC – analog comparator	7
7 Registers	8
7.1 Removed registers and bits	8
8 Table of contents	9



Atmel Corporation
2325 Orchard Parkway
San Jose, CA 95131
USA
Tel: (+1)(408) 441-0311
Fax: (+1)(408) 487-2600
www.atmel.com

Atmel Asia Limited
Unit 01-5 & 16, 19F
BEA Tower, Millennium City 5
418 Kwun Tong Road
Kwun Tong, Kowloon
HONG KONG
Tel: (+852) 2245-6100
Fax: (+852) 2722-1369

Atmel Munich GmbH
Business Campus
Parking 4
D-85748 Garching b. Munich
GERMANY
Tel: (+49) 89-31970-0
Fax: (+49) 89-3194621

Atmel Japan
9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chou-ku, Tokyo 104-0033
JAPAN
Tel: (+81) 3523-3551
Fax: (+81) 3523-7581

© 2011 Atmel Corporation. All rights reserved.

Atmel®, Atmel logo and combinations thereof, AVR®, XMEGA®, and others are registered trademarks or trademarks of Atmel Corporation or its subsidiaries. Other terms and product names may be trademarks of others.

Disclaimer: The information in this document is provided in connection with Atmel products. No license, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Atmel products. **EXCEPT AS SET FORTH IN THE ATMEL TERMS AND CONDITIONS OF SALES LOCATED ON THE ATMEL WEBSITE, ATMEL ASSUMES NO LIABILITY WHATSOEVER AND DISCLAIMS ANY EXPRESS, IMPLIED OR STATUTORY WARRANTY RELATING TO ITS PRODUCTS INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. IN NO EVENT SHALL ATMEL BE LIABLE FOR ANY DIRECT, INDIRECT, CONSEQUENTIAL, PUNITIVE, SPECIAL OR INCIDENTAL DAMAGES (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS AND PROFITS, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) ARISING OUT OF THE USE OR INABILITY TO USE THIS DOCUMENT, EVEN IF ATMEL HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.** Atmel makes no representations or warranties with respect to the accuracy or completeness of the contents of this document and reserves the right to make changes to specifications and product descriptions at any time without notice. Atmel does not make any commitment to update the information contained herein. Unless specifically provided otherwise, Atmel products are not suitable for, and shall not be used in, automotive applications. Atmel products are not intended, authorized, or warranted for use as components in applications intended to support or sustain life.