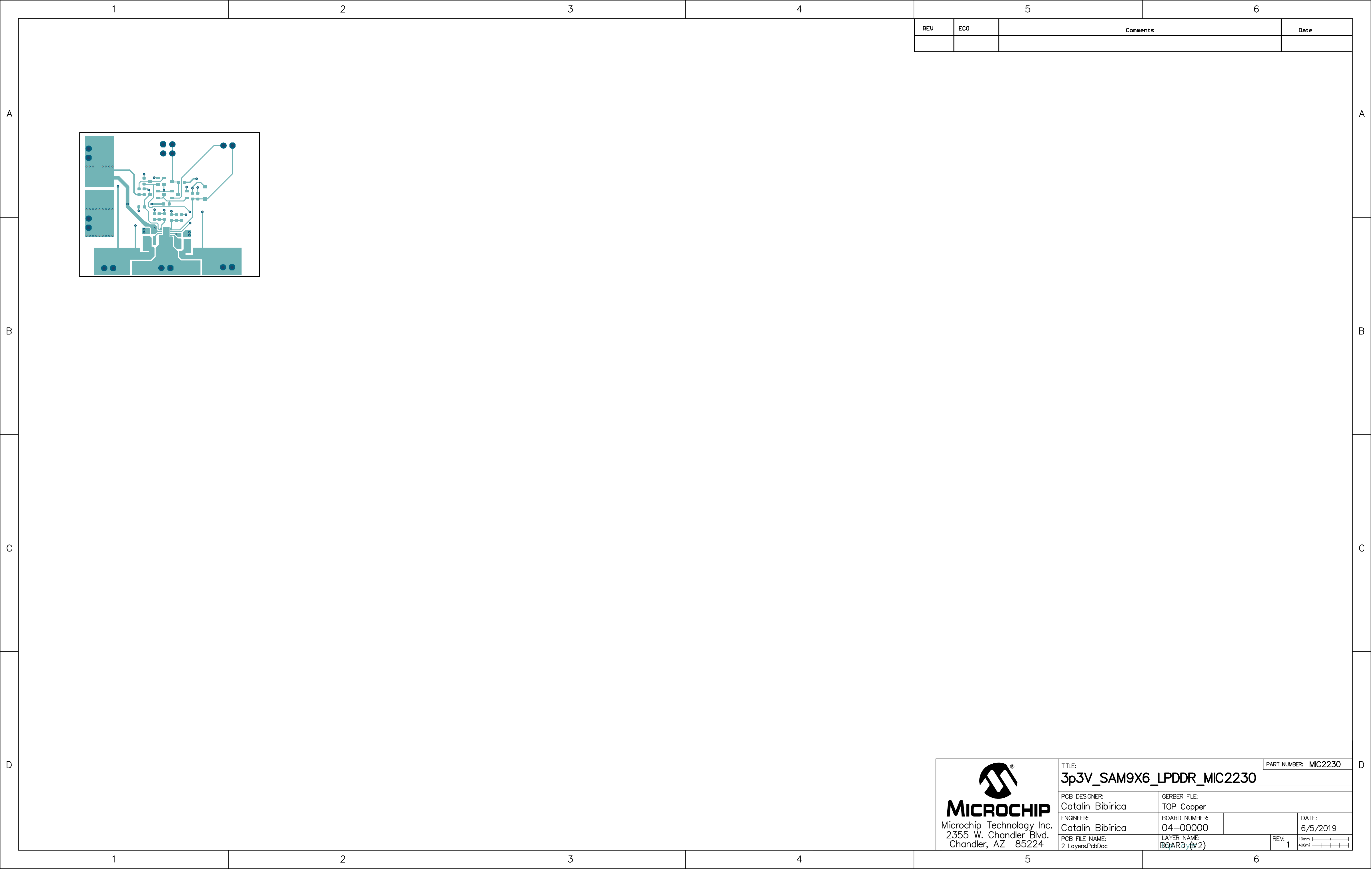


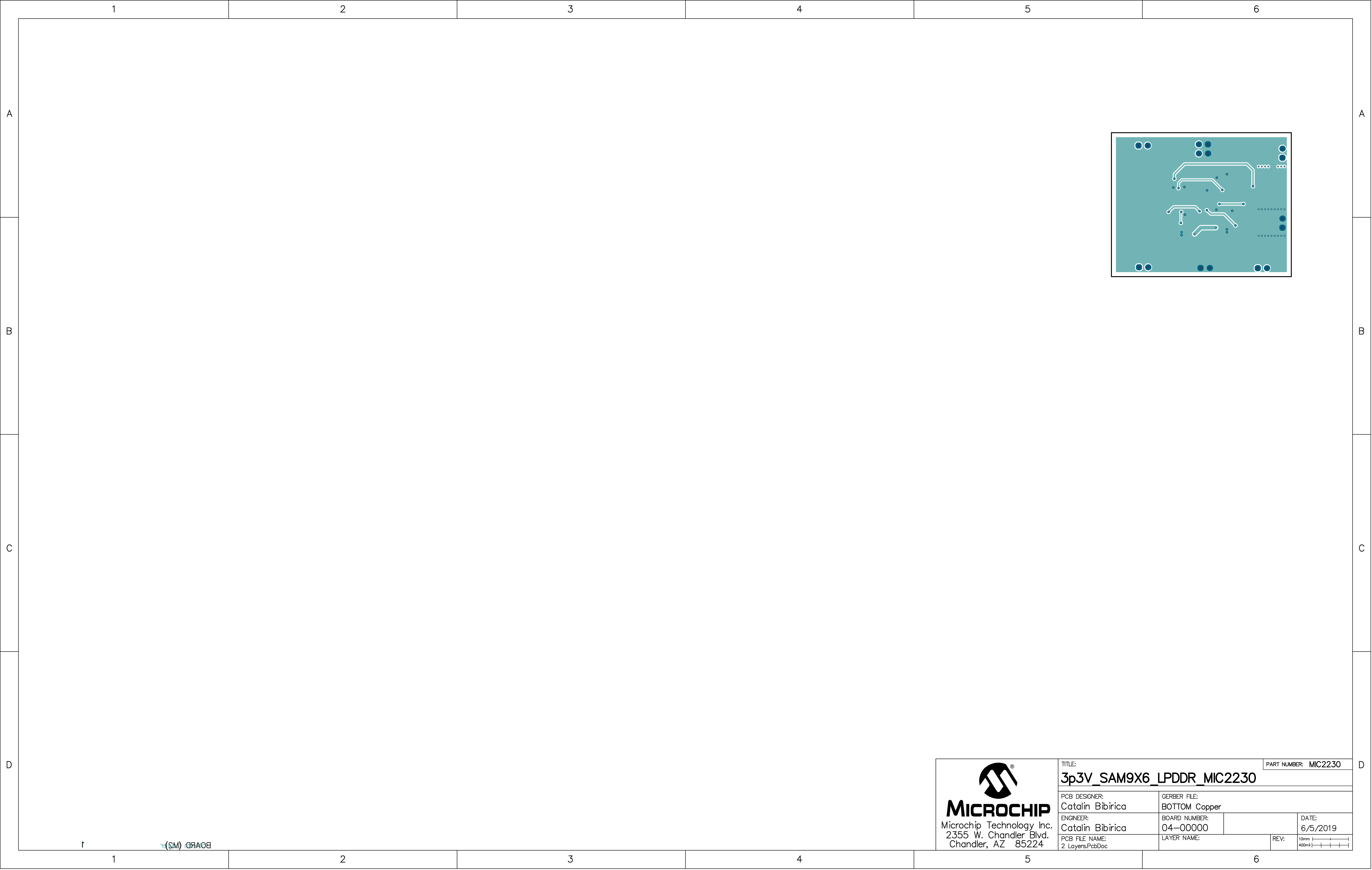


REV	ECO	Comments	Date



MICROCHIP
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Chandler, AZ 85224

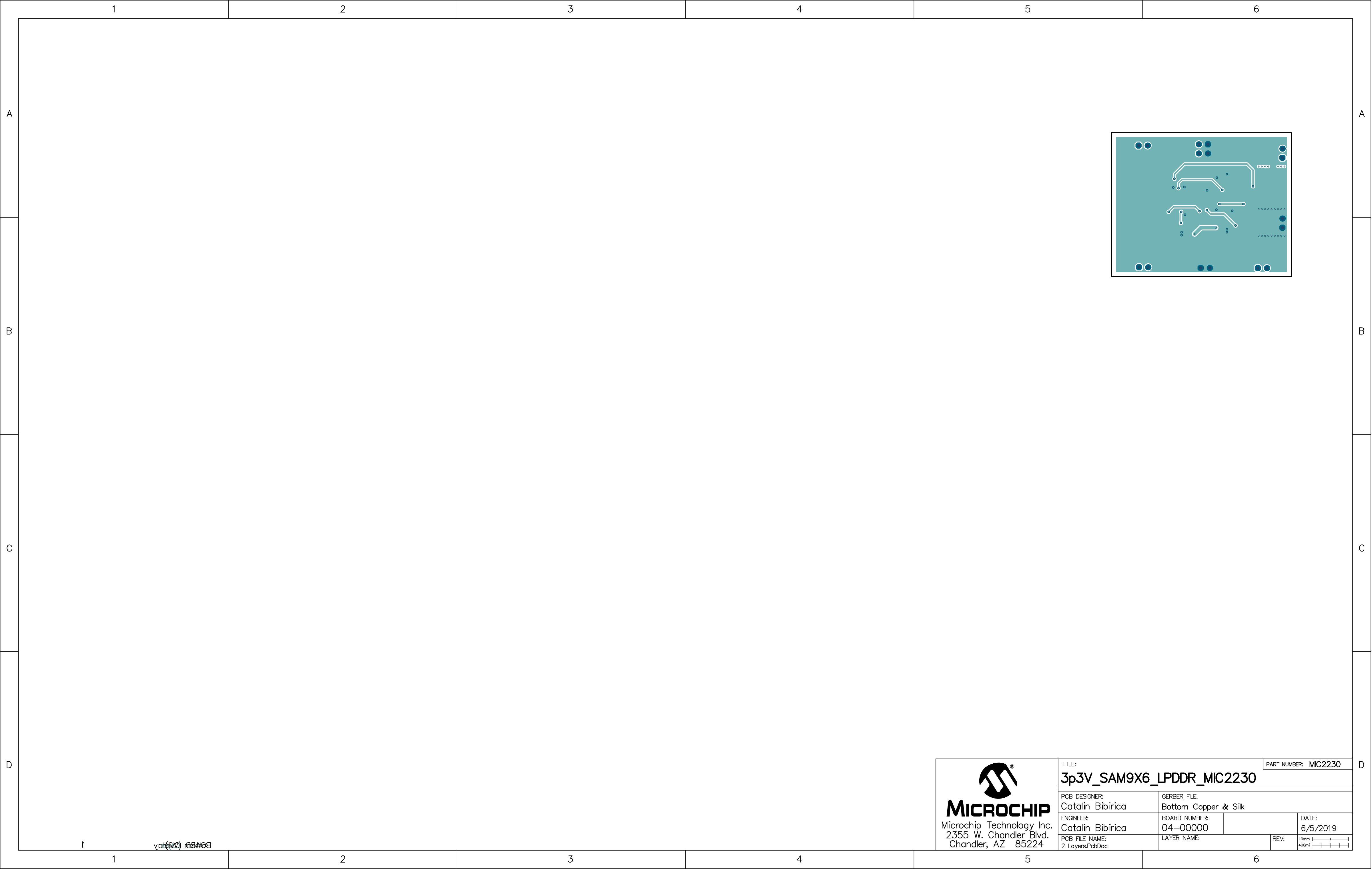
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PCB DESIGNER: Catalin Bibirica		GERBER FILE: TOP Copper	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME: BOARD.(M2)	REV: 1 10mm 400mil





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TITLE:		PART NUMBER: MIC2230	
3p3V_SAM9X6_LPDDR_MIC2230			
PCB DESIGNER: Catalin Bibirica		GERBER FILE: BOTTOM Copper	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME:	REV: 10mm 400mil





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TITLE:		PART NUMBER: MIC2230	
3p3V_SAM9X6_LPDDR_MIC2230			
PCB DESIGNER: Catalin Bibirica		GERBER FILE: Bottom Copper & Silk	
ENGINEER: Catalin Bibirica		BOARD NUMBER: 04—00000	DATE: 6/5/2019
PCB FILE NAME: 2_Layers.PcbDoc		LAYER NAME:	REV: 10mm 400mi

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2

3

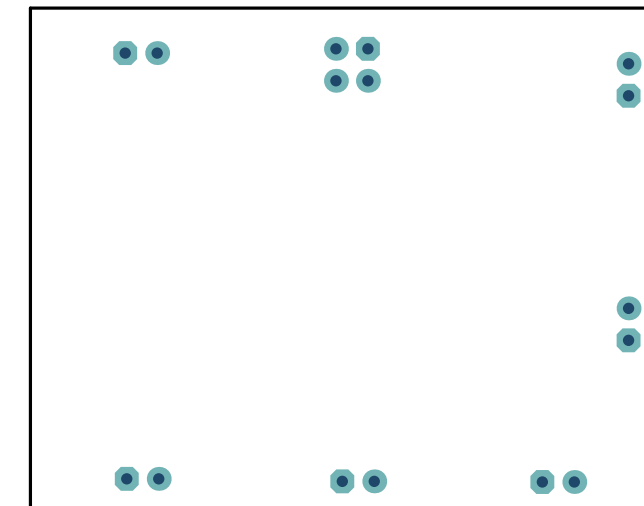
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6

A

A



B

B

C

C

D

D



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Chandler, AZ 85224

TITLE:	
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3p3V_SAM9X6_LPDDR_MIC2230

PART NUMBER:	MIC2230
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PCB DESIGNER:

Catalin Bibirica

GERBER FILE:

BOTTOM Silk

ENGINEER:

Catalin Bibirica

BOARD NUMBER	
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04-0000

DATE:	6/5/2019
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10mm |-----|
400mil |-----|-----|-----|

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2

3

4

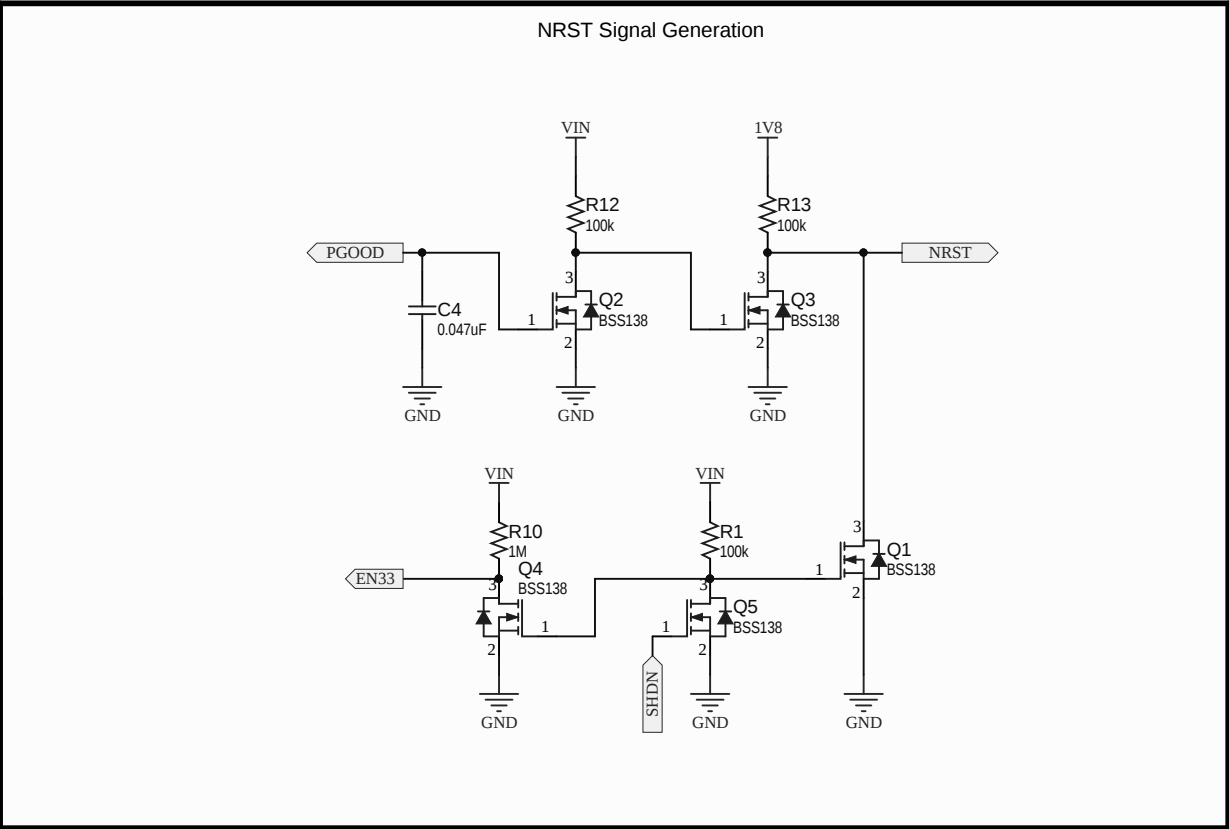
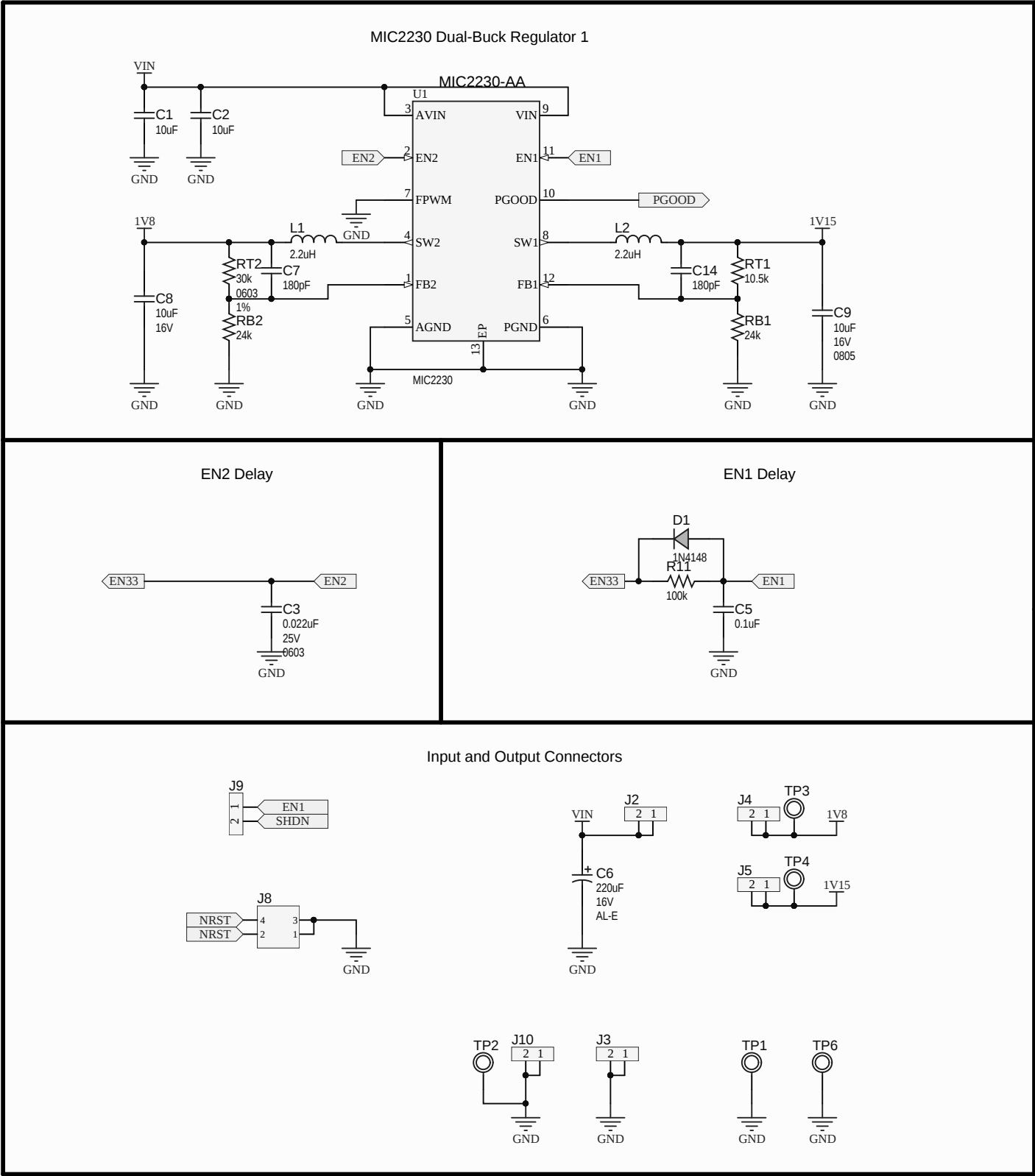
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BOARD (01/20) min

1

REV	ECO#	DESCRIPTION	DATE



Drawn By: Catalin Bibirica		
Engineer: Catalin Bibirica		
PartNumber: MIC2230	Project Title 3p3V_SAM9X6_LPDDR_MIC2230	
Sheet Title 3p3V_SAM9X6_LPDDR_MIC2230	Designed with 	
Size B	Sch #:03-00000	Date: 6/5/2019 10:05:38 AM
	Revision: 1	Sheet 1 of 1
File: B_Microchip.SchDoc		